MS-9121

INTEL (R) PLACER CHIPSET
DUAL PRESTONIA PROCESSORS SCHEMATI

淘本本
TBenBen.com

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02 BLOCK DIAGRAM
03 CPU 1_SIGNAL
04 CPU 1_POWER
05 CPU 2_SIGNAL
06 CPU 2_POWER
07 VID COMPARE
08 CLOCK
09 MCH_HOST
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12 MCH_POWER&GND
13 DDR CHA DIMMS
14 DDR CHB DIMMS
15 DDR CHA&CHB PARALLEL TERM
16 DDR VREFS&DECOUPLING
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19 ICH4-1
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32 PCIX SLOT 3
33 SCSI RAID SLOT
34 SCSI 1/5 53C1030_1
35 SCSI 2/5 53C1030_2
36 SCSI 3/5 CHANNEL 1
37 SCSI 4/5 CHANNEL 2
38 SCSI 5/5 MISC.
39 SIO(PC87366)
40 SIO HARDWARE MONITOR
41 IDE
42 USB
43 FWH
44 COM PORT
45 PRINTER PORT
46 KB&MS
47 HARDWARE MONITOR
48 SYSTEM FAN
49 POWER CONNECTOR
50 FRONT PANNEL
51 AD1885
52 SPEAKER OUT MIC&LINE IN
53 GIGA-LAN-1
54 GIGA-LAN-2
55 IBM_1
56 IBM_2
57 IBM_3
58 Mechanical Part
59 ASR
60 ACPI CIRCUIT
61 SYSTEM VOLTAGE REGULATOR
62 +2.5V VOLTAGE REGULATOR

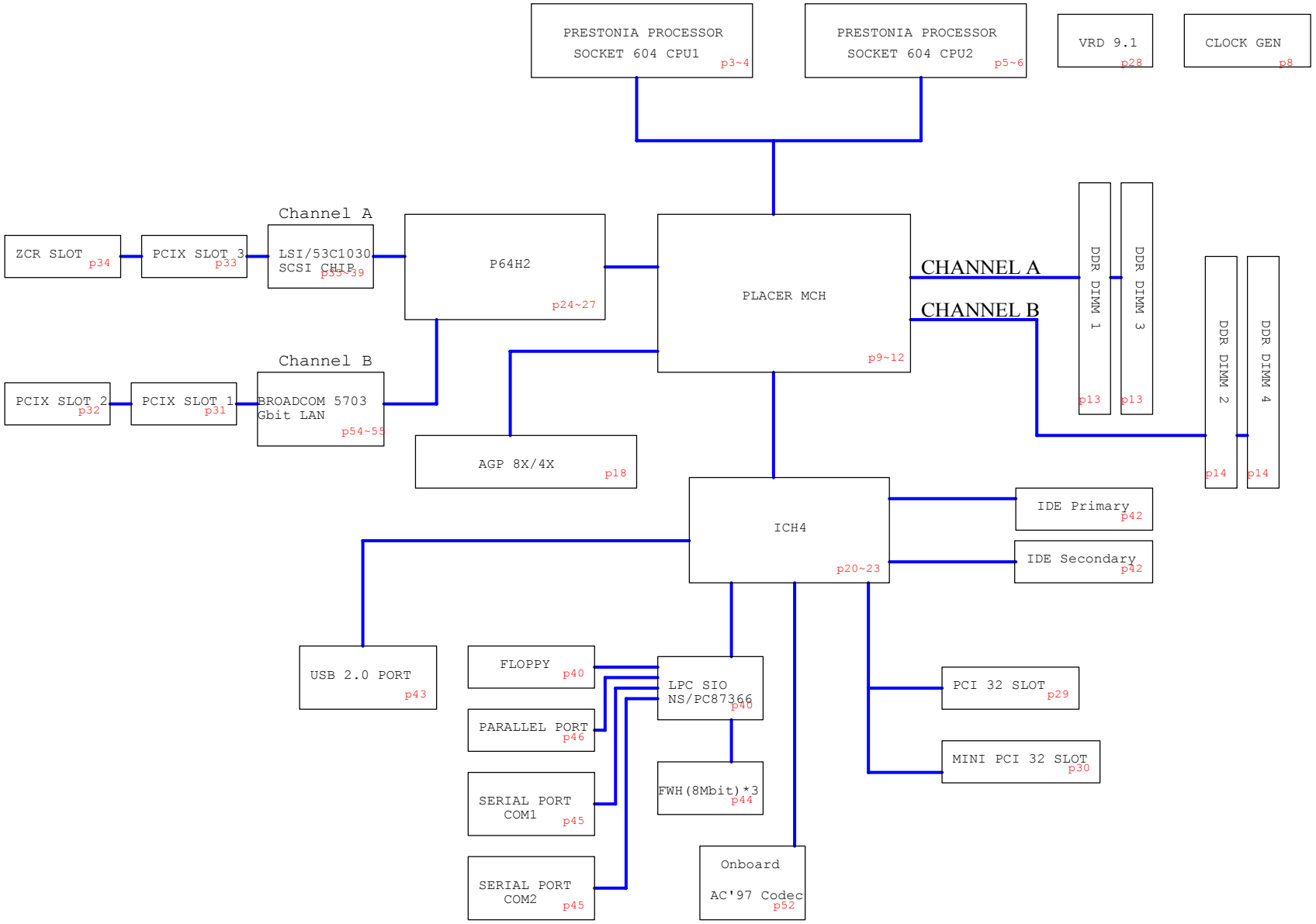
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Title: INDEX	Rev: 00C
Document Number	

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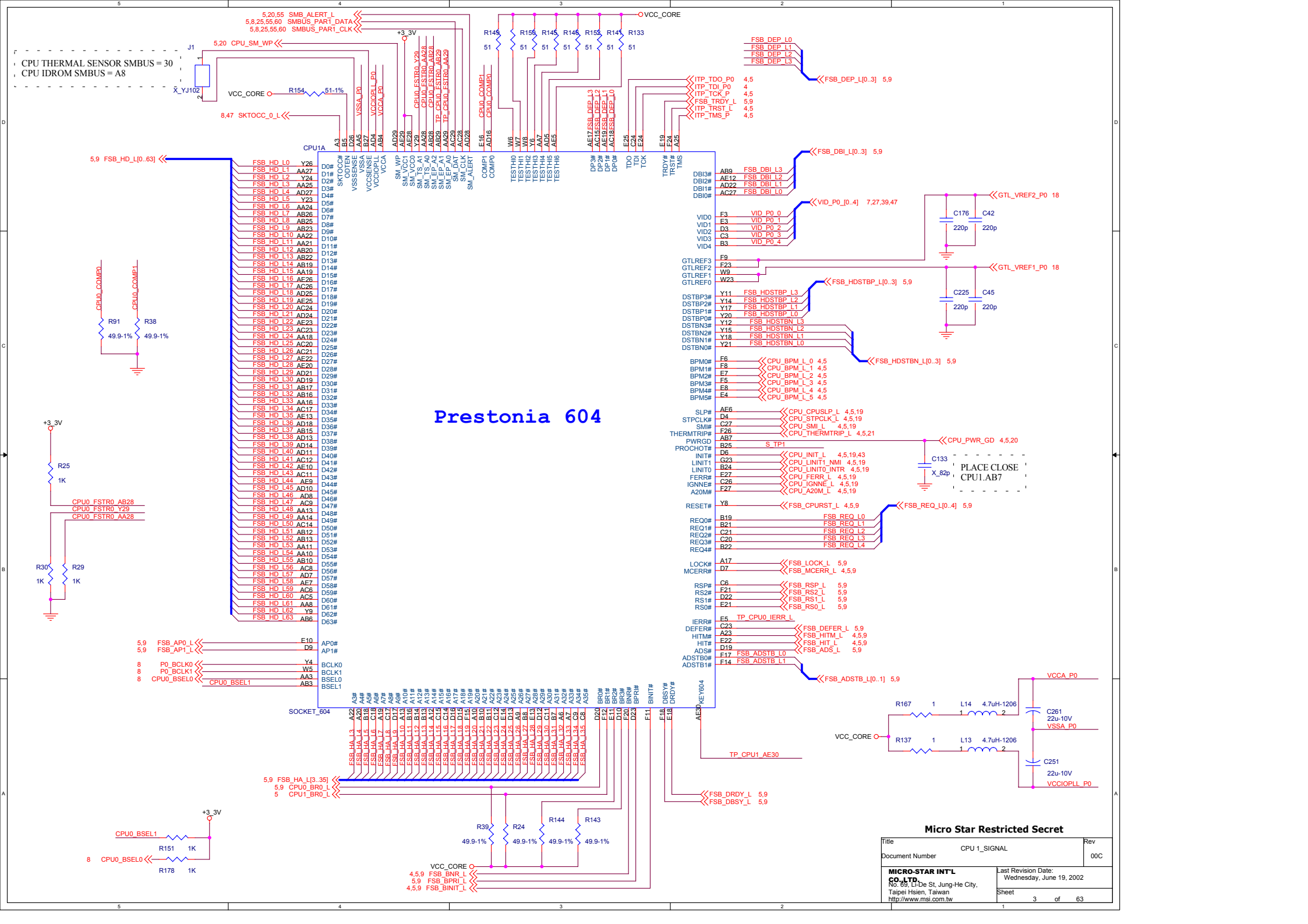
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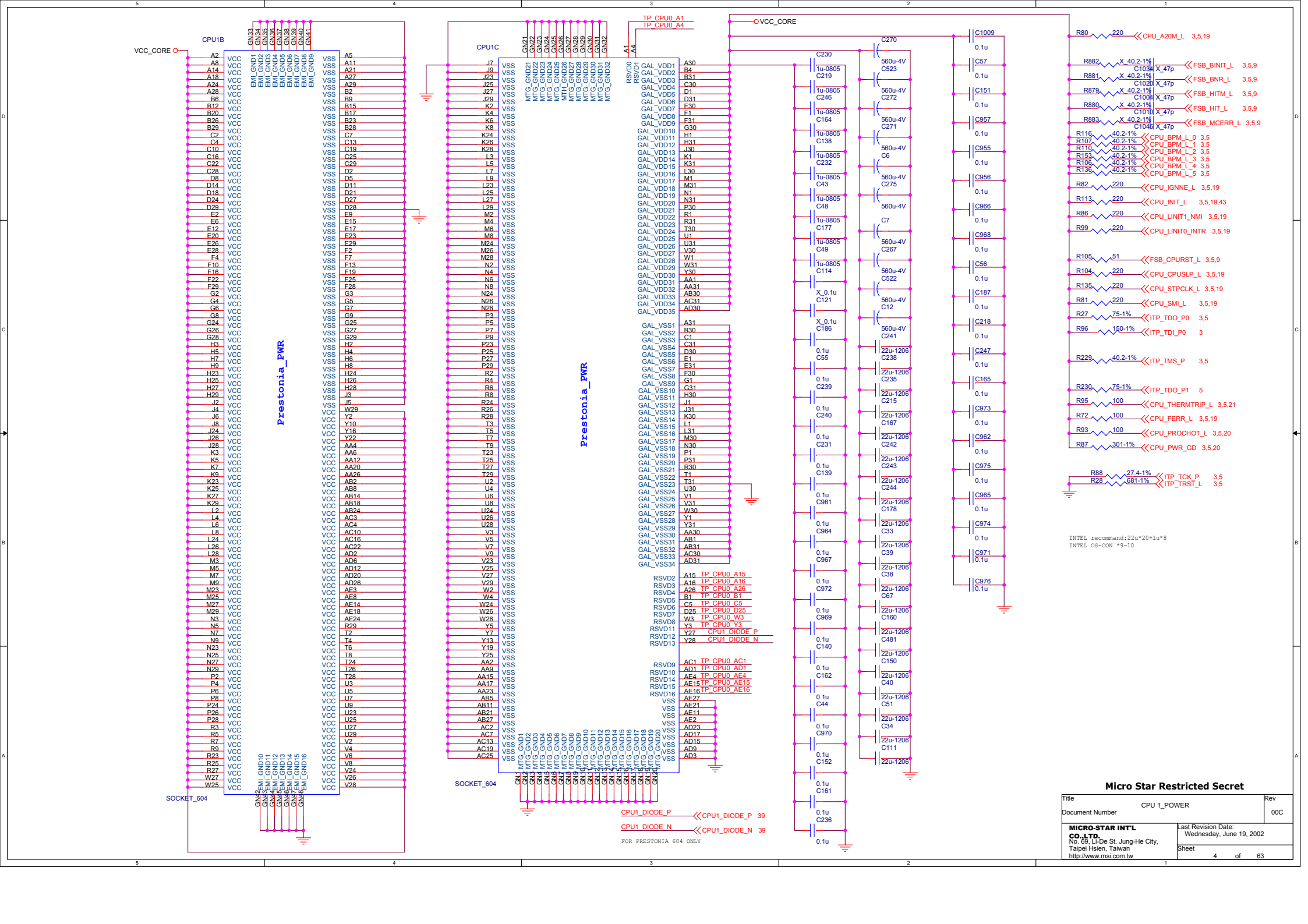
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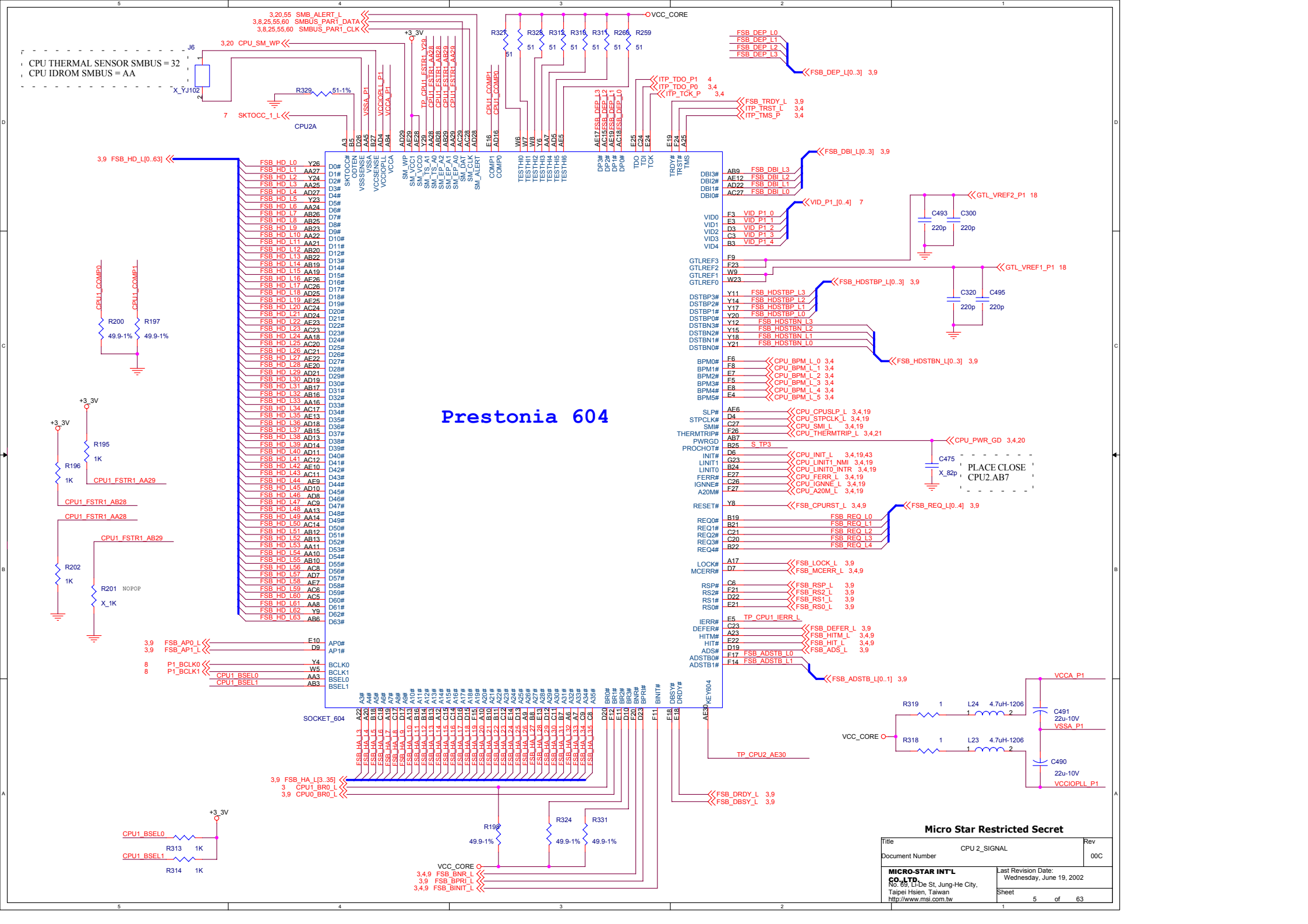


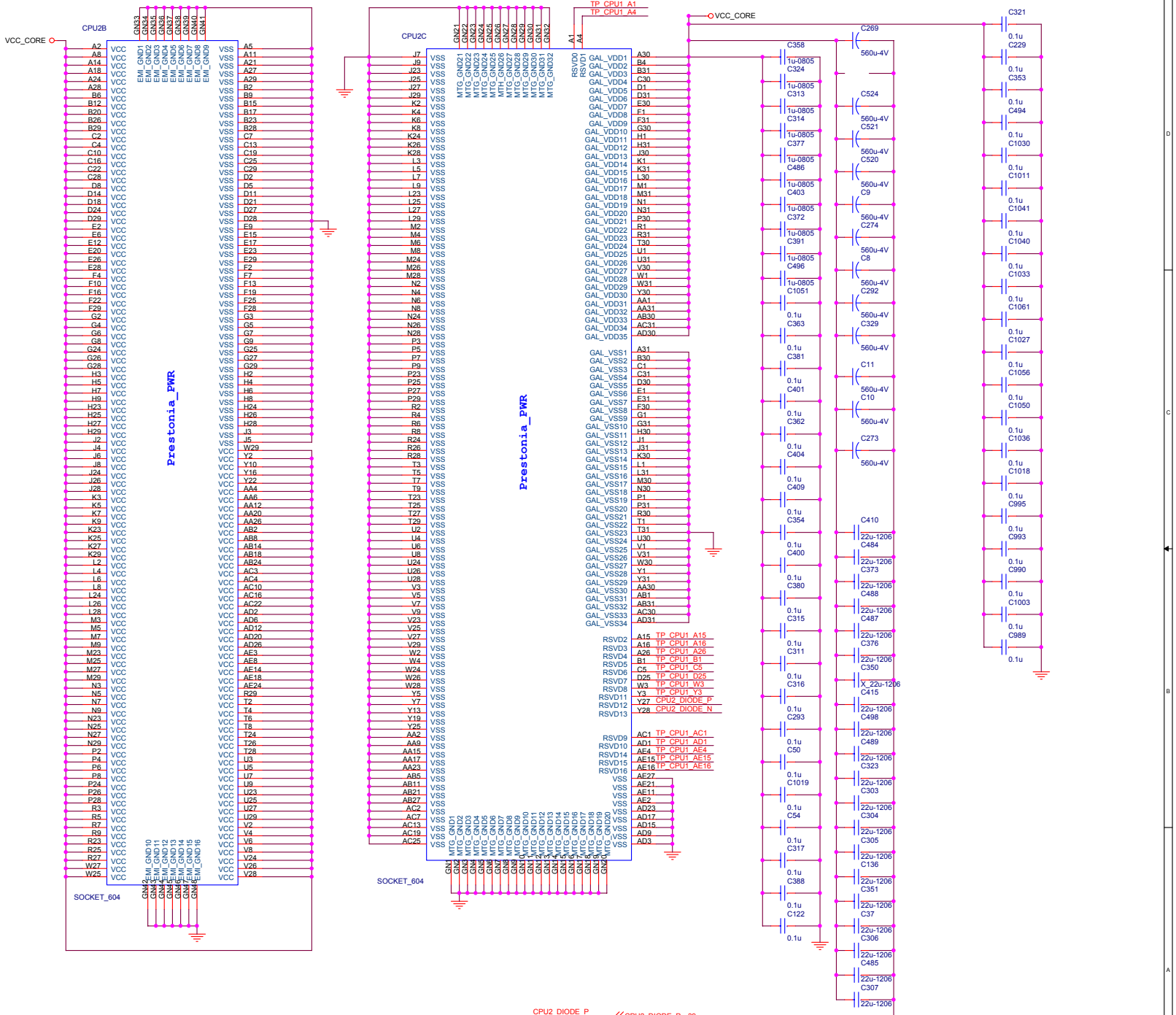
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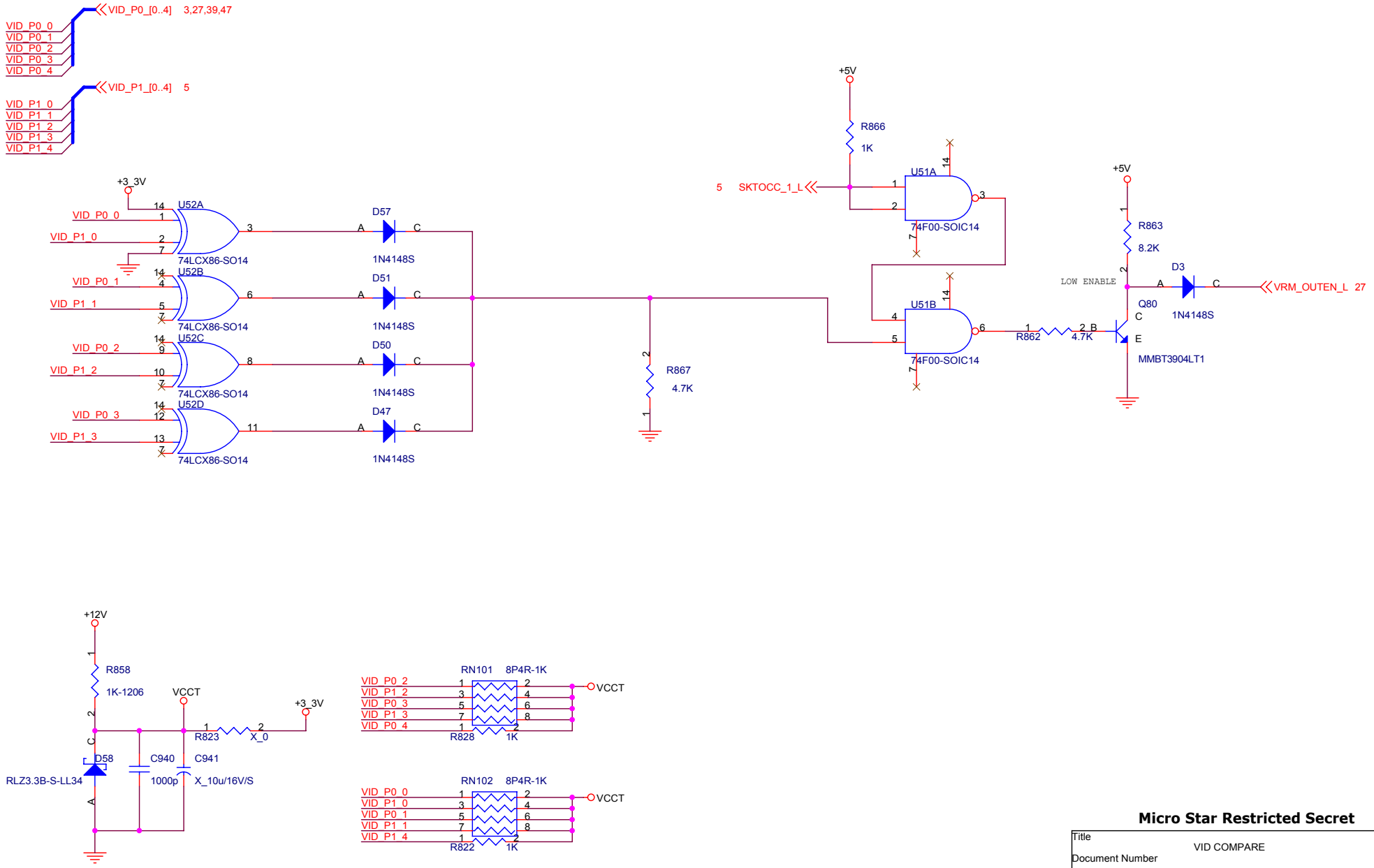
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Document Number		
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CPU THERMAL SENSOR SMBUS = 30
CPU IDROM SMBUS = A8
Prestonia 604
CPU1A
SOCKET_604
PLACE CLOSE CPU1.AB7
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Title
CPU 1_SIGNAL
Document Number
Rev
00C
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CPU1B
Prestonia_PWR
SOCKET_604
CPU1C
Prestonia_PWR
SOCKET_604
VCC_CORE
TP_CPU0_A1
TP_CPU0_A4
CPU1_DIODE_P
CPU1_DIODE_N
FOR PRESTONIA 604 ONLY
INTEL recommand:22u*20+1u*8
INTEL OS-CON *9~10
Micro Star Restricted Secret
Title
CPU 1_POWER
Document Number
Rev
00C
MICRO-STAR INT'L CO.,LTD.
No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan
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CPU THERMAL SENSOR SMBUS = 32
CPU IDROM SMBUS = AA
Prestonia 604
SOCKET_604
CPU2A
PLACE CLOSE CPU2.AB7
Micro Star Restricted Secret
Title
CPU 2_SIGNAL
Document Number
Rev
00C
MICRO-STAR INT'L CO.,LTD.
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CPU2B
CPU2C
Prestonia_PWR
SOCKET_604
VCC_CORE
TP_CPU1_A1
TP_CPU1_A4
TP_CPU1_A15
TP_CPU1_A16
TP_CPU1_A26
TP_CPU1_B1
TP_CPU1_C5
TP_CPU1_D25
TP_CPU1_W3
TP_CPU1_Y3
CPU2_DIODE_P
CPU2_DIODE_N
TP_CPU1_AC1
TP_CPU1_AD1
TP_CPU1_AE4
TP_CPU1_AE15
TP_CPU1_AE16
C269
C358
C321
C524
C521
C520
C9
C274
C8
C292
C329
C11
C10
C273
C410
C484
C373
C488
C487
C376
C350
X_22u-1206
C415
C498
C489
C323
C303
C304
C305
C136
C351
C37
C306
C485
C307
C324
C313
C314
C377
C486
C403
C372
C391
C496
C1051
C363
C381
C401
C362
C404
C409
C354
C400
C380
C315
C311
C316
C293
C50
C1019
C54
C317
C388
C122
C321
C229
C353
C494
C1030
C1011
C1041
C1040
C1033
C1061
C1027
C1056
C1050
C1036
C1018
C995
C993
C990
C1003
C989
CPU2_DIODE_P
CPU2_DIODE_N
FOR PRESTONIA 604 ONLY
Micro Star Restricted Secret
Title
CPU 2_POWER
Document Number
Rev
00C
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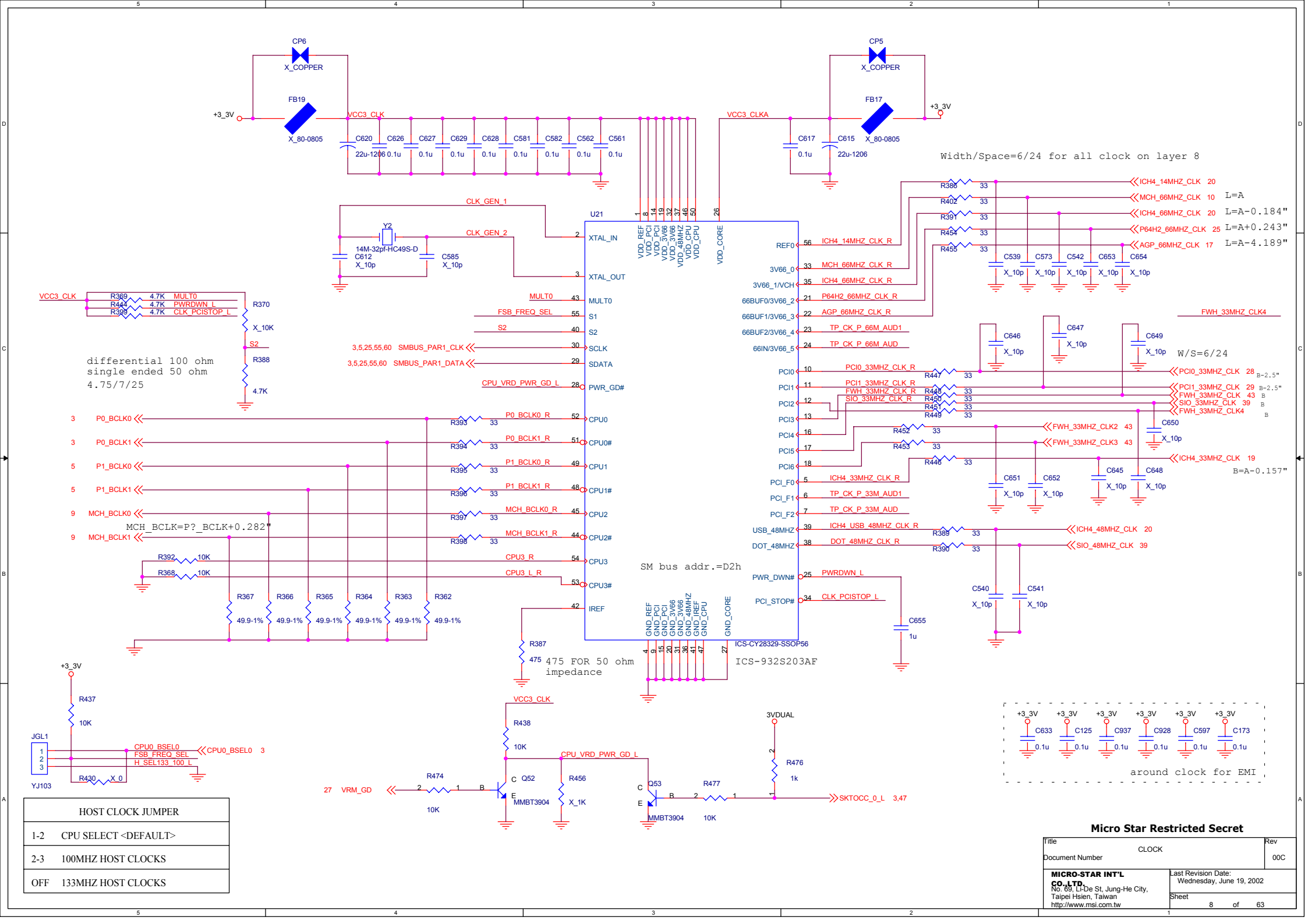
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Title	VID COMPARE	Rev 00C
Document Number		

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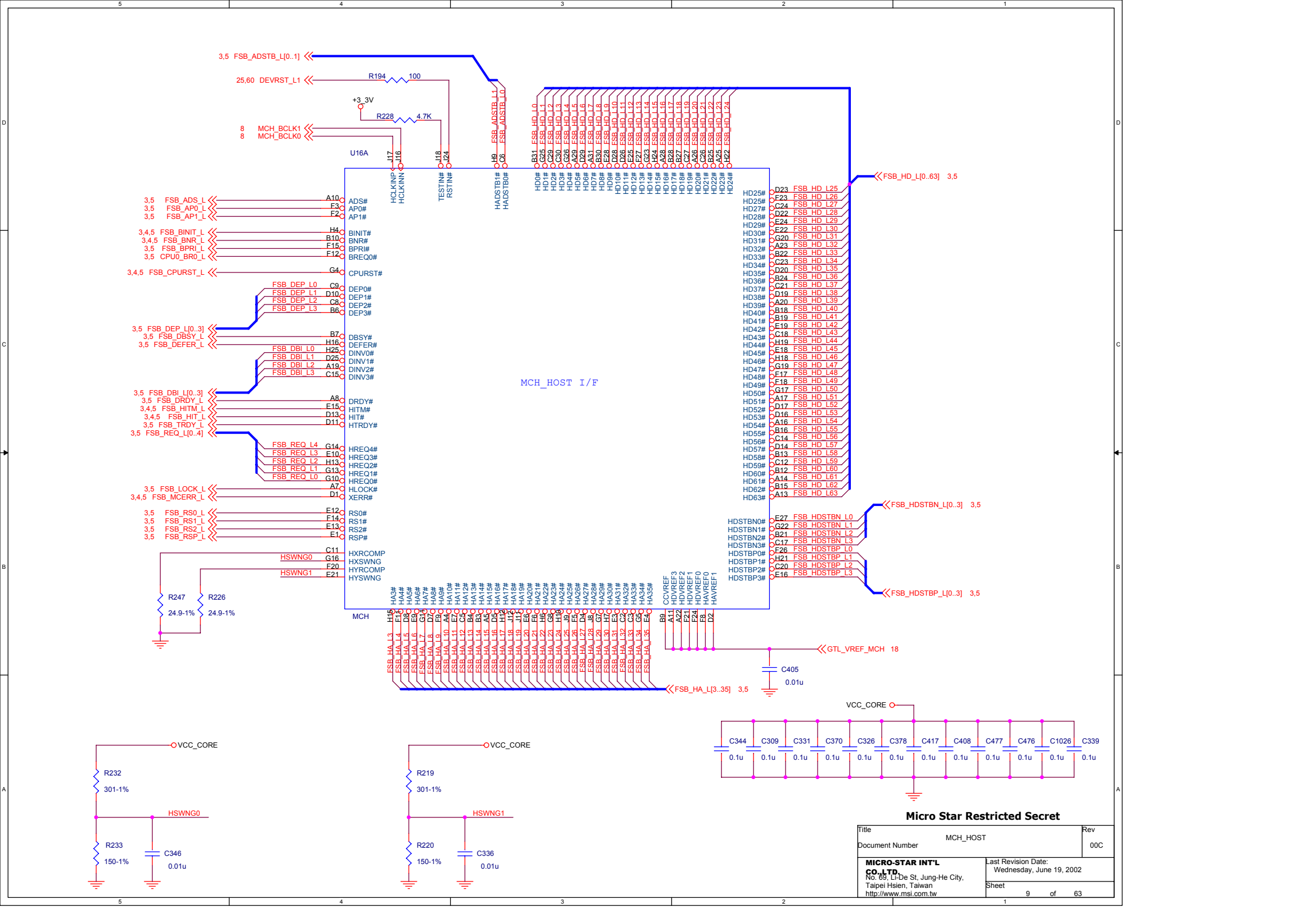
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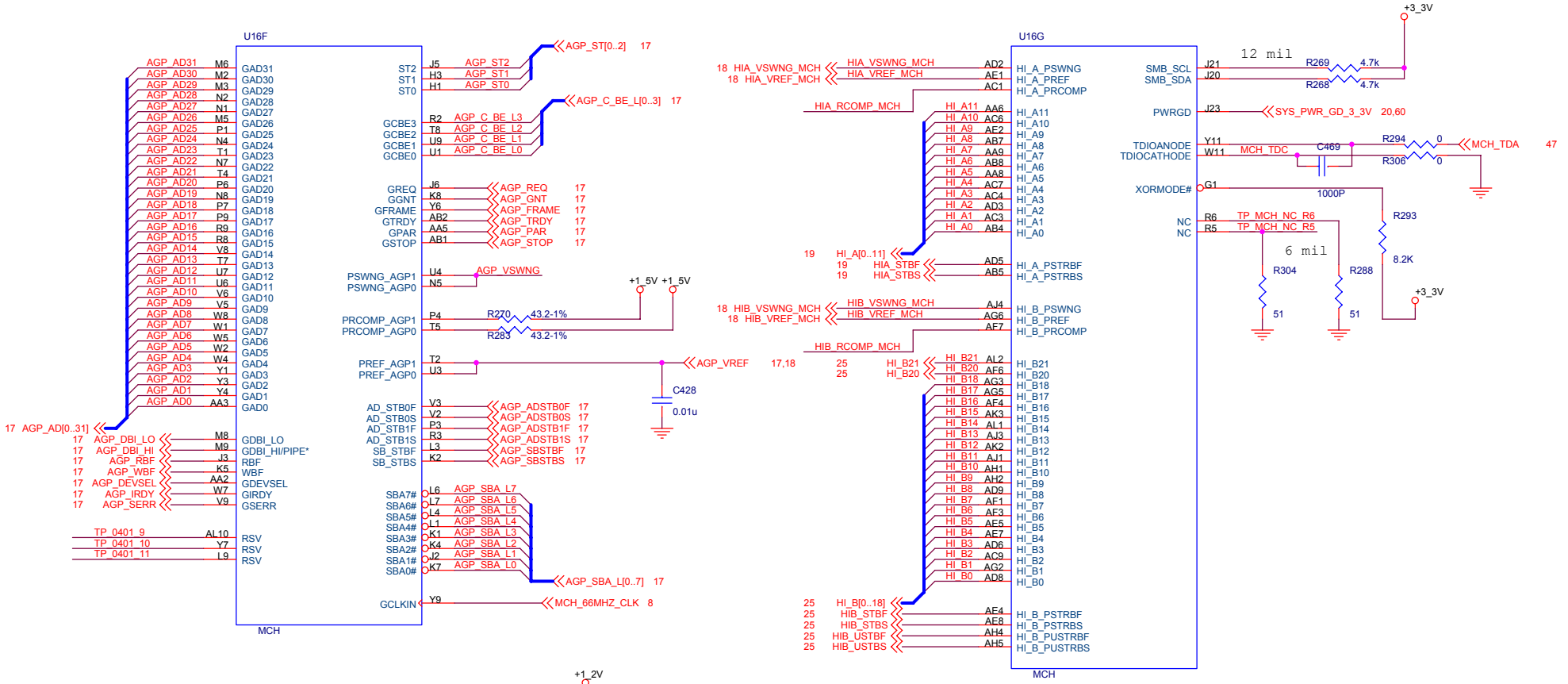


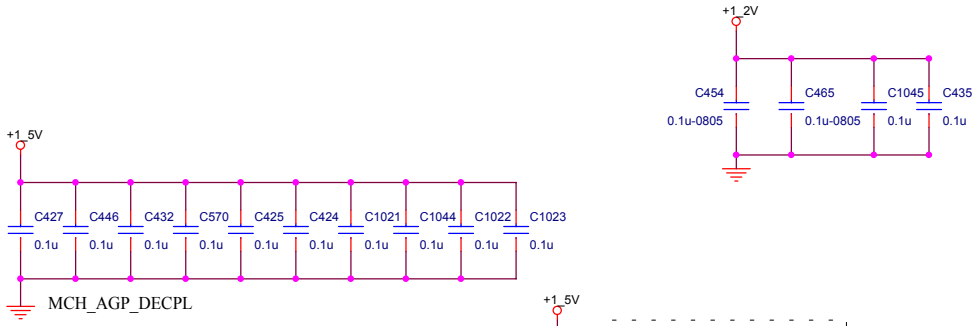
HOST CLOCK JUMPER	
1-2	CPU SELECT <DEFAULT>
2-3	100MHZ HOST CLOCKS
OFF	133MHZ HOST CLOCKS

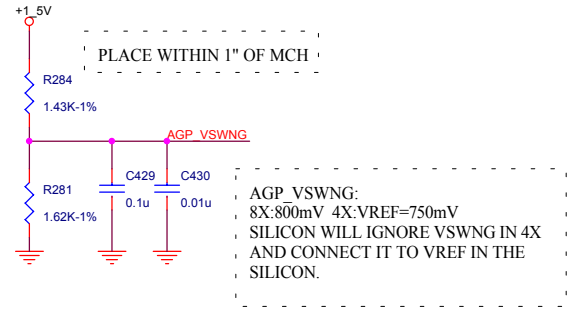
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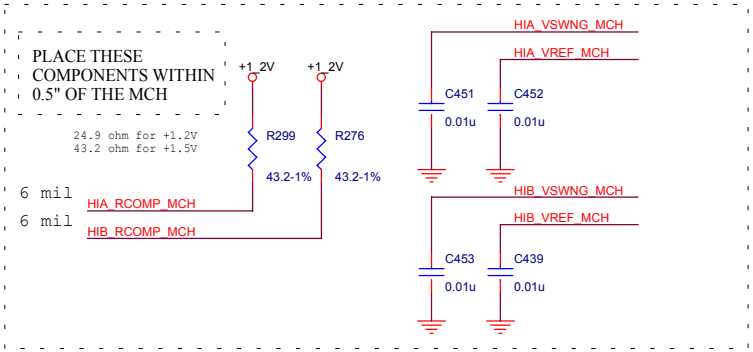
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Document Number		
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MCH_HOST I/F
U16A
MCH
3,5 FSB_ADSTB_L[0..1]
25,60 DEVRST_L1
R194 100
+3_3V
R228 4.7K
8 MCH_BCLK1
8 MCH_BCLK0
FSB_HD_L[0..63] 3,5
FSB_HDSTBN_L[0..3] 3,5
FSB_HDSTBP_L[0..3] 3,5
FSB_HA_L[3..35] 3,5
GTL_VREF_MCH 18
C405 0.01u
R247 24.9-1% R226 24.9-1%
VCC_CORE
C344 C309 C331 C370 C326 C378 C417 C408 C477 C476 C1026 C339
0.1u
R232 301-1% R233 150-1% C346 0.01u HSWNG0
R219 301-1% R220 150-1% C336 0.01u HSWNG1
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Title MCH_HOST
Document Number
Rev 00C
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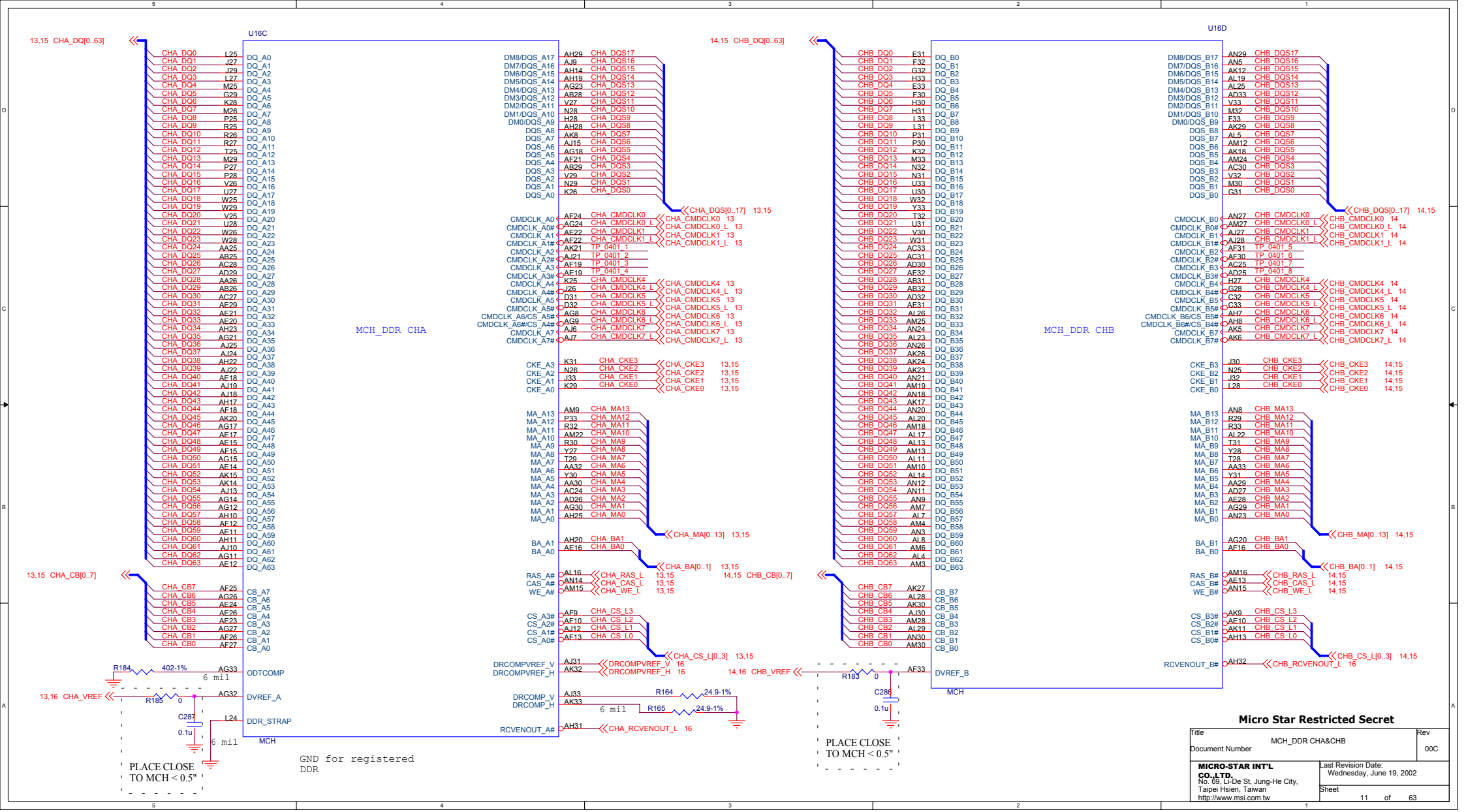


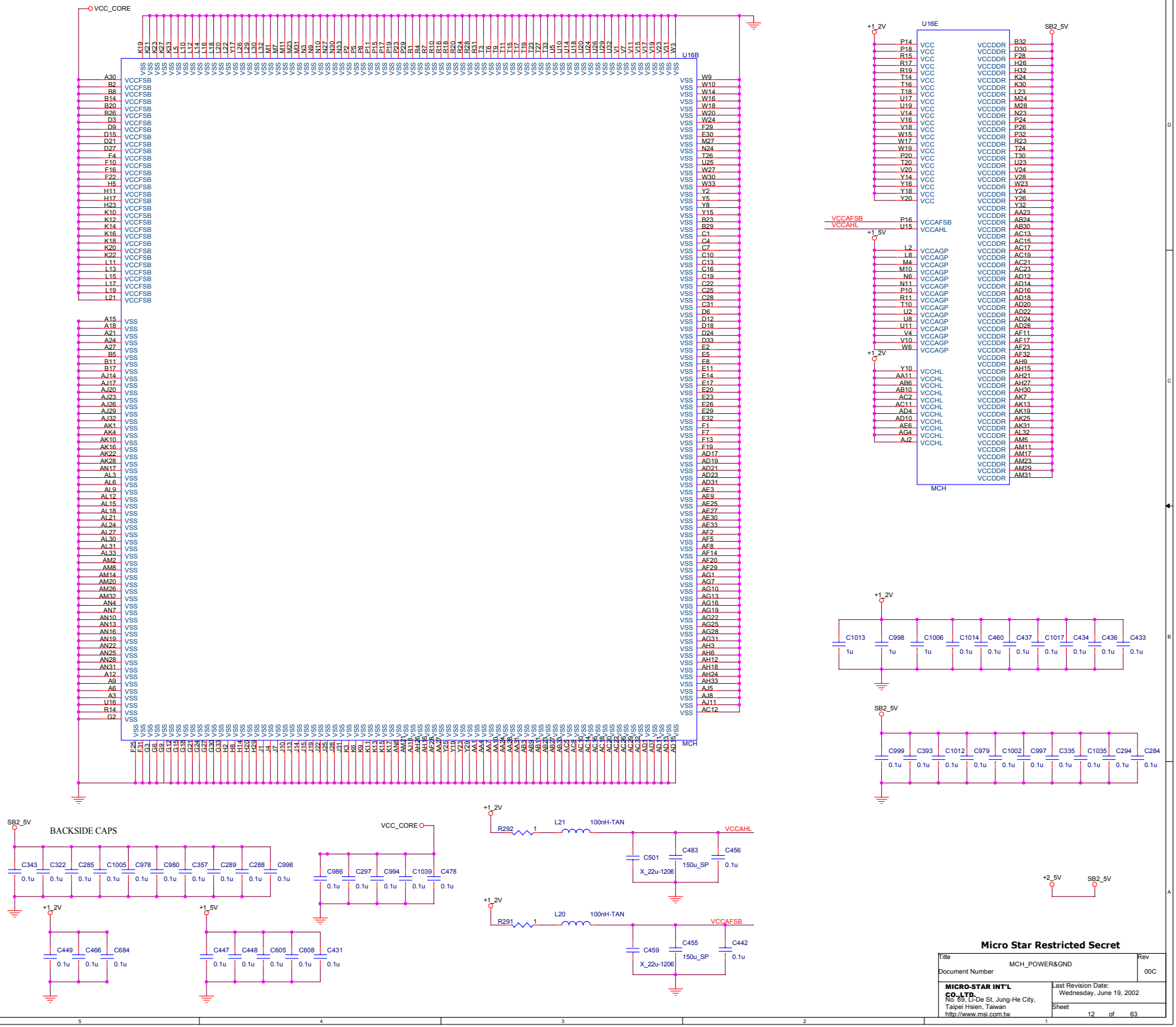




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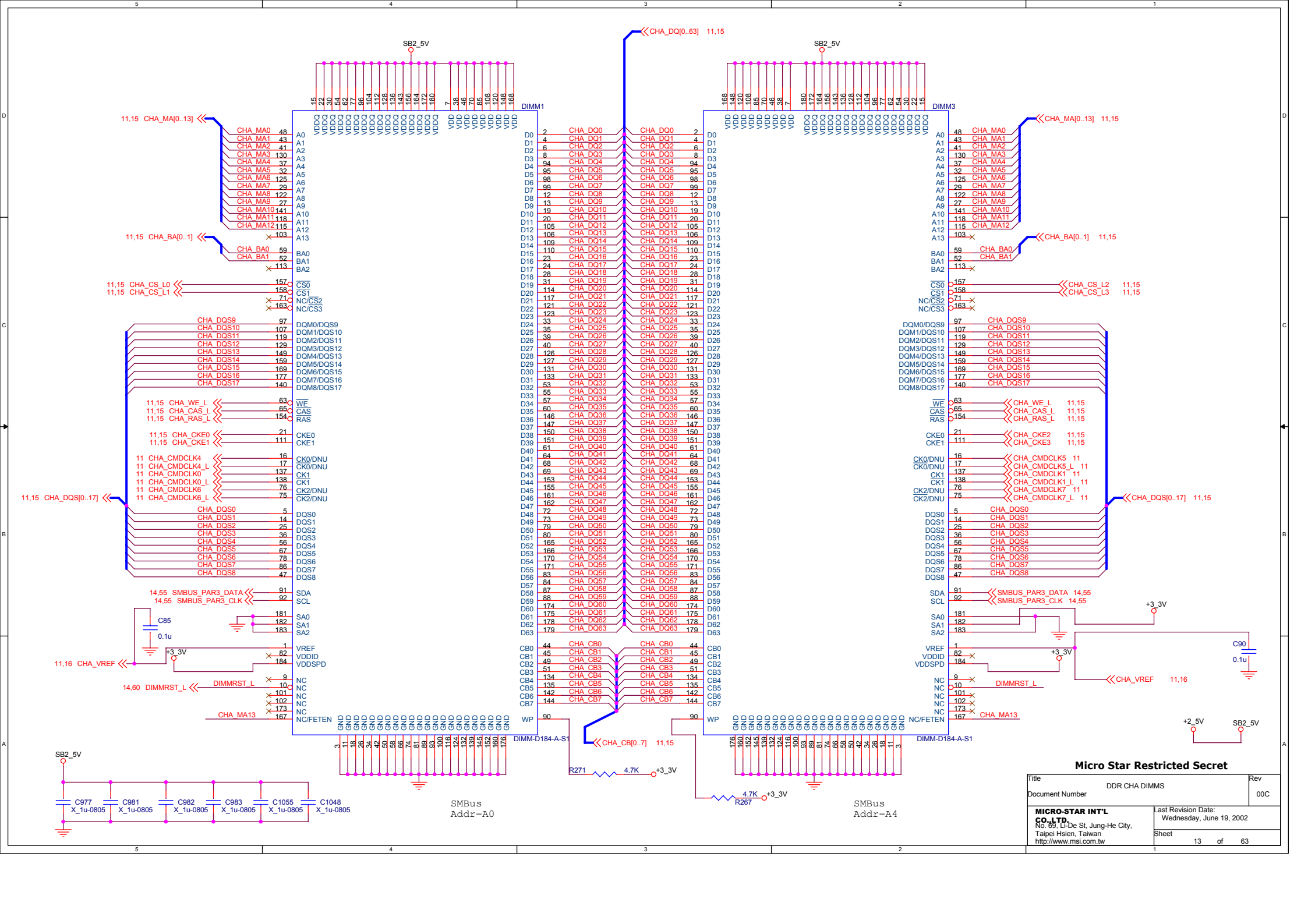
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Document Number		00C
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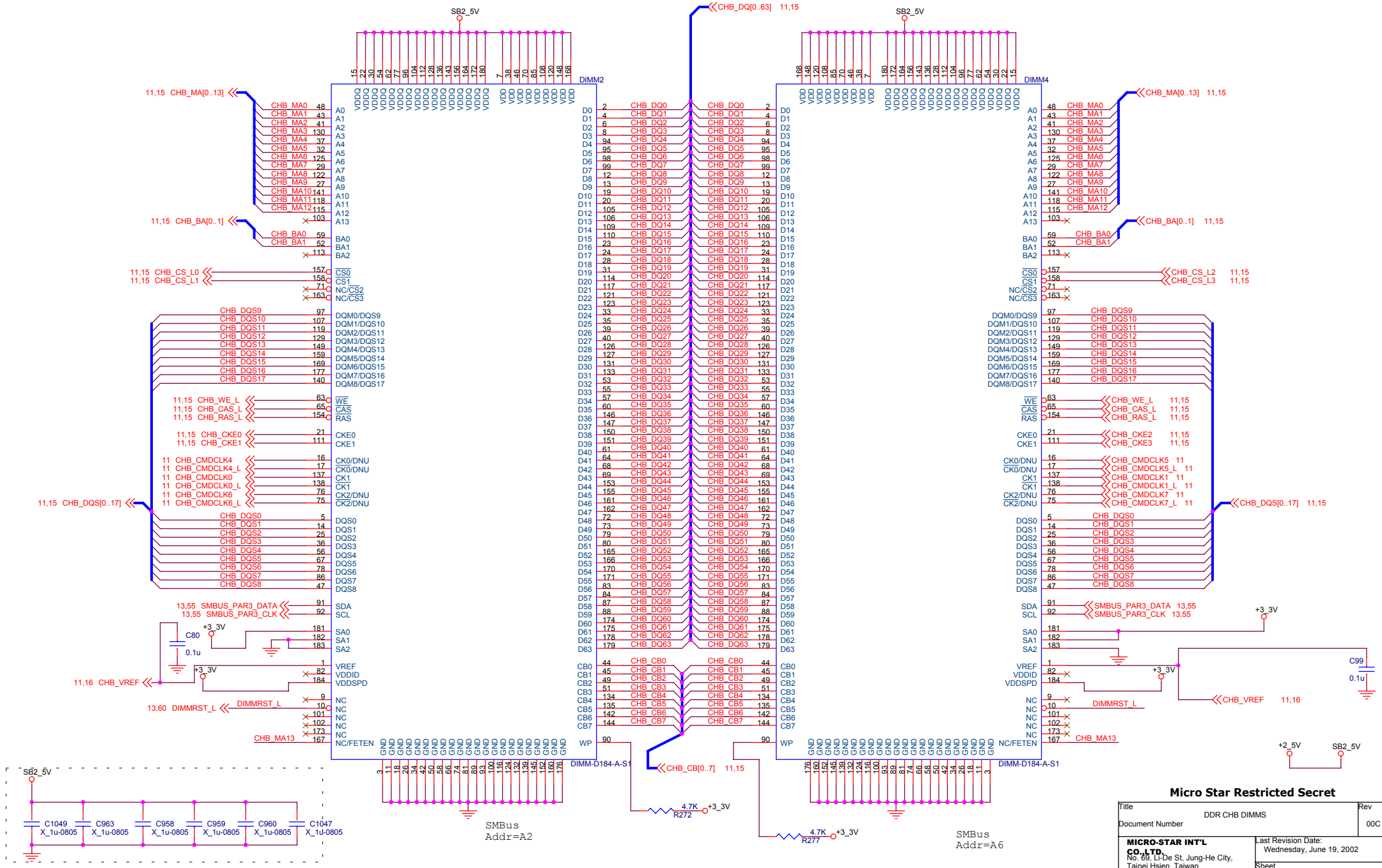

U16C
MCH_DDR CHA
MCH
13,15 CHA_DQ[0..63]
14,15 CHB_DQ[0..63]
13,15 CHA_CB[0..7]
CHA_DQ0 L25
CHA_DQ1 J27
CHA_DQ2 J29
CHA_DQ3 L27
CHA_DQ4 M25
CHA_DQ5 G29
CHA_DQ6 K28
CHA_DQ7 M26
CHA_DQ8 P25
CHA_DQ9 R25
CHA_DQ10 R26
CHA_DQ11 R27
CHA_DQ12 T25
CHA_DQ13 M29
CHA_DQ14 P27
CHA_DQ15 P28
CHA_DQ16 V26
CHA_DQ17 U27
CHA_DQ18 W25
CHA_DQ19 W29
CHA_DQ20 V25
CHA_DQ21 U28
CHA_DQ22 W26
CHA_DQ23 W28
CHA_DQ24 AA25
CHA_DQ25 AB25
CHA_DQ26 AC28
CHA_DQ27 AD29
CHA_DQ28 AA26
CHA_DQ29 AB26
CHA_DQ30 AC27
CHA_DQ31 AE29
CHA_DQ32 AE21
CHA_DQ33 AE20
CHA_DQ34 AH23
CHA_DQ35 AG21
CHA_DQ36 AJ25
CHA_DQ37 AJ24
CHA_DQ38 AH22
CHA_DQ39 AJ22
CHA_DQ40 AE18
CHA_DQ41 AJ19
CHA_DQ42 AJ18
CHA_DQ43 AH17
CHA_DQ44 AF18
CHA_DQ45 AK20
CHA_DQ46 AG17
CHA_DQ47 AE17
CHA_DQ48 AE15
CHA_DQ49 AF15
CHA_DQ50 AG15
CHA_DQ51 AE14
CHA_DQ52 AK15
CHA_DQ53 AK14
CHA_DQ54 AJ13
CHA_DQ55 AG14
CHA_DQ56 AG12
CHA_DQ57 AH10
CHA_DQ58 AF12
CHA_DQ59 AE11
CHA_DQ60 AH11
CHA_DQ61 AJ10
CHA_DQ62 AG11
CHA_DQ63 AE12
CHA_CB7 AF25
CHA_CB6 AG26
CHA_CB5 AE24
CHA_CB4 AE26
CHA_CB3 AE23
CHA_CB2 AG27
CHA_CB1 AF26
CHA_CB0 AF27
DQ_A0 DQ_A1 DQ_A2 DQ_A3 DQ_A4 DQ_A5 DQ_A6 DQ_A7 DQ_A8 DQ_A9 DQ_A10 DQ_A11 DQ_A12 DQ_A13 DQ_A14 DQ_A15 DQ_A16 DQ_A17 DQ_A18 DQ_A19 DQ_A20 DQ_A21 DQ_A22 DQ_A23 DQ_A24 DQ_A25 DQ_A26 DQ_A27 DQ_A28 DQ_A29 DQ_A30 DQ_A31 DQ_A32 DQ_A33 DQ_A34 DQ_A35 DQ_A36 DQ_A37 DQ_A38 DQ_A39 DQ_A40 DQ_A41 DQ_A42 DQ_A43 DQ_A44 DQ_A45 DQ_A46 DQ_A47 DQ_A48 DQ_A49 DQ_A50 DQ_A51 DQ_A52 DQ_A53 DQ_A54 DQ_A55 DQ_A56 DQ_A57 DQ_A58 DQ_A59 DQ_A60 DQ_A61 DQ_A62 DQ_A63
CB_A7 CB_A6 CB_A5 CB_A4 CB_A3 CB_A2 CB_A1 CB_A0
R184 402-1% AG33 ODTCOMP
6 mil
13,16 CHA_VREF AG32 DVREF_A
R185 0
C287 0.1u
L24 DDR_STRAP
6 mil
PLACE CLOSE TO MCH < 0.5"
GND for registered DDR
DM8/DQS_A17 DM7/DQS_A16 DM6/DQS_A15 DM5/DQS_A14 DM4/DQS_A13 DM3/DQS_A12 DM2/DQS_A11 DM1/DQS_A10 DM0/DQS_A9 DQS_A8 DQS_A7 DQS_A6 DQS_A5 DQS_A4 DQS_A3 DQS_A2 DQS_A1 DQS_A0
AH29 CHA_DQS17
AJ9 CHA_DQS16
AH14 CHA_DQS15
AH19 CHA_DQS14
AG23 CHA_DQS13
AB28 CHA_DQS12
V27 CHA_DQS11
N28 CHA_DQS10
H28 CHA_DQS9
AH28 CHA_DQS8
AK8 CHA_DQS7
AJ15 CHA_DQS6
AG18 CHA_DQS5
AF21 CHA_DQS4
AB29 CHA_DQS3
V29 CHA_DQS2
N29 CHA_DQS1
K26 CHA_DQS0
CHA_DQS[0..17] 13,15
CMDCLK_A0 CMDCLK_A0# CMDCLK_A1 CMDCLK_A1# CMDCLK_A2 CMDCLK_A2# CMDCLK_A3 CMDCLK_A3# CMDCLK_A4 CMDCLK_A4# CMDCLK_A5 CMDCLK_A5# CMDCLK_A6/CS_A5# CMDCLK_A6#/CS_A4# CMDCLK_A7 CMDCLK_A7#
AF24 CHA_CMDCLK0 CHA_CMDCLK0 13
AG24 CHA_CMDCLK0_L CHA_CMDCLK0_L 13
AF22 CHA_CMDCLK1 CHA_CMDCLK1 13
AF22 CHA_CMDCLK1_L CHA_CMDCLK1_L 13
AK21 TP_0401_1
AJ21 TP_0401_2
AF19 TP_0401_3
AE19 TP_0401_4
K25 CHA_CMDCLK4 CHA_CMDCLK4 13
J26 CHA_CMDCLK4_L CHA_CMDCLK4_L 13
D31 CHA_CMDCLK5 CHA_CMDCLK5 13
D32 CHA_CMDCLK5_L CHA_CMDCLK5_L 13
AG8 CHA_CMDCLK6 CHA_CMDCLK6 13
AG9 CHA_CMDCLK6_L CHA_CMDCLK6_L 13
AJ6 CHA_CMDCLK7 CHA_CMDCLK7 13
AJ7 CHA_CMDCLK7_L CHA_CMDCLK7_L 13
CKE_A3 CKE_A2 CKE_A1 CKE_A0
K31 CHA_CKE3 CHA_CKE3 13,15
N26 CHA_CKE2 CHA_CKE2 13,15
J33 CHA_CKE1 CHA_CKE1 13,15
K29 CHA_CKE0 CHA_CKE0 13,15
MA_A13 MA_A12 MA_A11 MA_A10 MA_A9 MA_A8 MA_A7 MA_A6 MA_A5 MA_A4 MA_A3 MA_A2 MA_A1 MA_A0
AM9 CHA_MA13
P33 CHA_MA12
R32 CHA_MA11
AM22 CHA_MA10
R30 CHA_MA9
Y27 CHA_MA8
T29 CHA_MA7
AA32 CHA_MA6
Y30 CHA_MA5
AA30 CHA_MA4
AC24 CHA_MA3
AD26 CHA_MA2
AG30 CHA_MA1
AH25 CHA_MA0
CHA_MA[0..13] 13,15
BA_A1 BA_A0
AH20 CHA_BA1
AE16 CHA_BA0
CHA_BA[0..1] 13,15
RAS_A# CAS_A# WE_A#
AL16 CHA_RAS_L 13,15
AN14 CHA_CAS_L 13,15
AM15 CHA_WE_L 13,15
CS_A3# CS_A2# CS_A1# CS_A0#
AF9 CHA_CS_L3
AF10 CHA_CS_L2
AJ12 CHA_CS_L1
AF13 CHA_CS_L0
CHA_CS_L[0..3] 13,15
DRCOMPVREF_V DRCOMPVREF_H
AJ31 DRCOMPVREF_V 16
AK32 DRCOMPVREF_H 16
DRCOMP_V DRCOMP_H
AJ33 AK33
R164 24.9-1%
6 mil
R165 24.9-1%
RCVENOUT_A#
AH31 CHA_RCVENOUT_L 16
U16D
MCH_DDR CHB
MCH
14,16 CHB_VREF
CHB_DQ0 F31
CHB_DQ1 F32
CHB_DQ2 G32
CHB_DQ3 H33
CHB_DQ4 E33
CHB_DQ5 F30
CHB_DQ6 H30
CHB_DQ7 H31
CHB_DQ8 L33
CHB_DQ9 L31
CHB_DQ10 P31
CHB_DQ11 P30
CHB_DQ12 K32
CHB_DQ13 M33
CHB_DQ14 N32
CHB_DQ15 N31
CHB_DQ16 U33
CHB_DQ17 U30
CHB_DQ18 W32
CHB_DQ19 Y33
CHB_DQ20 T32
CHB_DQ21 U31
CHB_DQ22 V30
CHB_DQ23 W31
CHB_DQ24 AC33
CHB_DQ25 AC31
CHB_DQ26 AD30
CHB_DQ27 AE32
CHB_DQ28 AB31
CHB_DQ29 AB32
CHB_DQ30 AD32
CHB_DQ31 AE31
CHB_DQ32 AL26
CHB_DQ33 AM25
CHB_DQ34 AN24
CHB_DQ35 AL23
CHB_DQ36 AN26
CHB_DQ37 AK26
CHB_DQ38 AK24
CHB_DQ39 AK23
CHB_DQ40 AN21
CHB_DQ41 AM19
CHB_DQ42 AN18
CHB_DQ43 AK17
CHB_DQ44 AN20
CHB_DQ45 AL20
CHB_DQ46 AM18
CHB_DQ47 AL17
CHB_DQ48 AL13
CHB_DQ49 AM13
CHB_DQ50 AL11
CHB_DQ51 AM10
CHB_DQ52 AL14
CHB_DQ53 AN12
CHB_DQ54 AN11
CHB_DQ55 AN9
CHB_DQ56 AM7
CHB_DQ57 AL7
CHB_DQ58 AM4
CHB_DQ59 AN3
CHB_DQ60 AL8
CHB_DQ61 AM6
CHB_DQ62 AL4
CHB_DQ63 AM3
CHB_CB7 AK27
CHB_CB6 AL28
CHB_CB5 AK30
CHB_CB4 AJ30
CHB_CB3 AM28
CHB_CB2 AL29
CHB_CB1 AN30
CHB_CB0 AM30
DQ_B0 DQ_B1 DQ_B2 DQ_B3 DQ_B4 DQ_B5 DQ_B6 DQ_B7 DQ_B8 DQ_B9 DQ_B10 DQ_B11 DQ_B12 DQ_B13 DQ_B14 DQ_B15 DQ_B16 DQ_B17 DQ_B18 DQ_B19 DQ_B20 DQ_B21 DQ_B22 DQ_B23 DQ_B24 DQ_B25 DQ_B26 DQ_B27 DQ_B28 DQ_B29 DQ_B30 DQ_B31 DQ_B32 DQ_B33 DQ_B34 DQ_B35 DQ_B36 DQ_B37 DQ_B38 DQ_B39 DQ_B40 DQ_B41 DQ_B42 DQ_B43 DQ_B44 DQ_B45 DQ_B46 DQ_B47 DQ_B48 DQ_B49 DQ_B50 DQ_B51 DQ_B52 DQ_B53 DQ_B54 DQ_B55 DQ_B56 DQ_B57 DQ_B58 DQ_B59 DQ_B60 DQ_B61 DQ_B62 DQ_B63
CB_B7 CB_B6 CB_B5 CB_B4 CB_B3 CB_B2 CB_B1 CB_B0
AF33 DVREF_B
R183 0
C286 0.1u
PLACE CLOSE TO MCH < 0.5"
DM8/DQS_B17 DM7/DQS_B16 DM6/DQS_B15 DM5/DQS_B14 DM4/DQS_B13 DM3/DQS_B12 DM2/DQS_B11 DM1/DQS_B10 DM0/DQS_B9 DQS_B8 DQS_B7 DQS_B6 DQS_B5 DQS_B4 DQS_B3 DQS_B2 DQS_B1 DQS_B0
AN29 CHB_DQS17
AN5 CHB_DQS16
AK12 CHB_DQS15
AL19 CHB_DQS14
AL25 CHB_DQS13
AD33 CHB_DQS12
V33 CHB_DQS11
M32 CHB_DQS10
F33 CHB_DQS9
AK29 CHB_DQS8
AL5 CHB_DQS7
AM12 CHB_DQS6
AK18 CHB_DQS5
AM24 CHB_DQS4
AC30 CHB_DQS3
V32 CHB_DQS2
M30 CHB_DQS1
G31 CHB_DQS0
CHB_DQS[0..17] 14,15
CMDCLK_B0 CMDCLK_B0# CMDCLK_B1 CMDCLK_B1# CMDCLK_B2 CMDCLK_B2# CMDCLK_B3 CMDCLK_B3# CMDCLK_B4 CMDCLK_B4# CMDCLK_B5 CMDCLK_B5# CMDCLK_B6/CS_B5# CMDCLK_B6#/CS_B4# CMDCLK_B7 CMDCLK_B7#
AN27 CHB_CMDCLK0 CHB_CMDCLK0 14
AM27 CHB_CMDCLK0_L CHB_CMDCLK0_L 14
AJ27 CHB_CMDCLK1 CHB_CMDCLK1 14
AJ28 CHB_CMDCLK1_L CHB_CMDCLK1_L 14
AF31 TP_0401_5
AE30 TP_0401_6
AG25 TP_0401_7
AD25 TP_0401_8
H27 CHB_CMDCLK4 CHB_CMDCLK4 14
G28 CHB_CMDCLK4_L CHB_CMDCLK4_L 14
C32 CHB_CMDCLK5 CHB_CMDCLK5 14
C33 CHB_CMDCLK5_L CHB_CMDCLK5_L 14
AH7 CHB_CMDCLK6 CHB_CMDCLK6 14
AH8 CHB_CMDCLK6_L CHB_CMDCLK6_L 14
AK5 CHB_CMDCLK7 CHB_CMDCLK7 14
AK6 CHB_CMDCLK7_L CHB_CMDCLK7_L 14
CKE_B3 CKE_B2 CKE_B1 CKE_B0
J30 CHB_CKE3 CHB_CKE3 14,15
N25 CHB_CKE2 CHB_CKE2 14,15
J32 CHB_CKE1 CHB_CKE1 14,15
L28 CHB_CKE0 CHB_CKE0 14,15
MA_B13 MA_B12 MA_B11 MA_B10 MA_B9 MA_B8 MA_B7 MA_B6 MA_B5 MA_B4 MA_B3 MA_B2 MA_B1 MA_B0
AN8 CHB_MA13
R29 CHB_MA12
R33 CHB_MA11
AL22 CHB_MA10
T31 CHB_MA9
Y28 CHB_MA8
T28 CHB_MA7
AA33 CHB_MA6
Y31 CHB_MA5
AA29 CHB_MA4
AD27 CHB_MA3
AE28 CHB_MA2
AG29 CHB_MA1
AN23 CHB_MA0
CHB_MA[0..13] 14,15
BA_B1 BA_B0
AG20 CHB_BA1
AF16 CHB_BA0
CHB_BA[0..1] 14,15
RAS_B# CAS_B# WE_B#
AM16 CHB_RAS_L 14,15
AE13 CHB_CAS_L 14,15
AN15 CHB_WE_L 14,15
CS_B3# CS_B2# CS_B1# CS_B0#
AK9 CHB_CS_L3
AE10 CHB_CS_L2
AK11 CHB_CS_L1
AH13 CHB_CS_L0
CHB_CS_L[0..3] 14,15
RCVENOUT_B#
AH32 CHB_RCVENOUT_L 16
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Title
MCH_DDR CHA&CHB
Document Number
Rev
00C
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CHA_DQ[0..63] 11,15
SB2_5V
DIMM1
DIMM3
DIMM-D184-A-S1
11,15 CHA_MA[0..13]
11,15 CHA_BA[0..1]
11,15 CHA_CS_L0
11,15 CHA_CS_L1
CHA_CS_L2 11,15
CHA_CS_L3 11,15
CHA_DQS[0..17] 11,15
11,15 CHA_WE_L
11,15 CHA_CAS_L
11,15 CHA_RAS_L
11,15 CHA_CKE0
11,15 CHA_CKE1
CHA_CKE2 11,15
CHA_CKE3 11,15
11 CHA_CMDCLK4
11 CHA_CMDCLK4_L
11 CHA_CMDCLK0
11 CHA_CMDCLK0_L
11 CHA_CMDCLK6
11 CHA_CMDCLK6_L
CHA_CMDCLK5 11
CHA_CMDCLK5_L 11
CHA_CMDCLK1 11
CHA_CMDCLK1_L 11
CHA_CMDCLK7 11
CHA_CMDCLK7_L 11
14,55 SMBUS_PAR3_DATA
14,55 SMBUS_PAR3_CLK
SMBUS_PAR3_DATA 14,55
SMBUS_PAR3_CLK 14,55
11,16 CHA_VREF
CHA_VREF 11,16
14,60 DIMMRST_L
DIMMRST_L
CHA_MA13
CHA_CB[0..7] 11,15
C85
0.1u
C90
0.1u
+3_3V
+2_5V
R271 4.7K
R267 4.7K
C977 X_1u-0805
C981 X_1u-0805
C982 X_1u-0805
C983 X_1u-0805
C1055 X_1u-0805
C1048 X_1u-0805
SMBus
Addr=A0
SMBus
Addr=A4
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DDR CHA DIMMS
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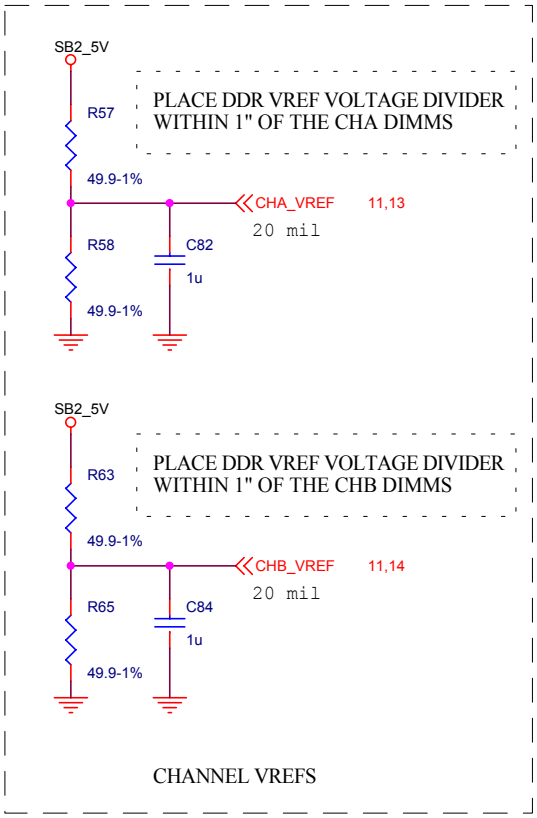
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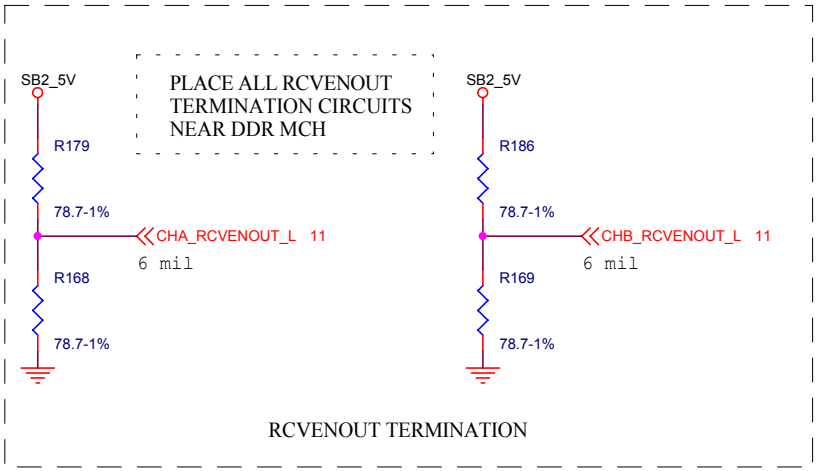
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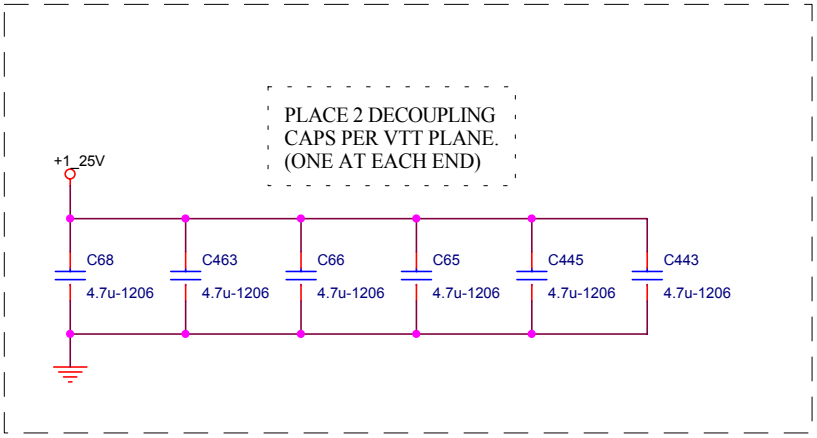


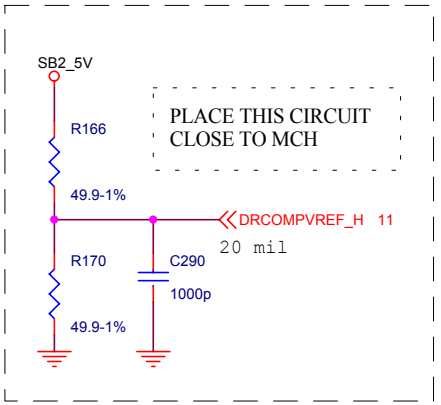
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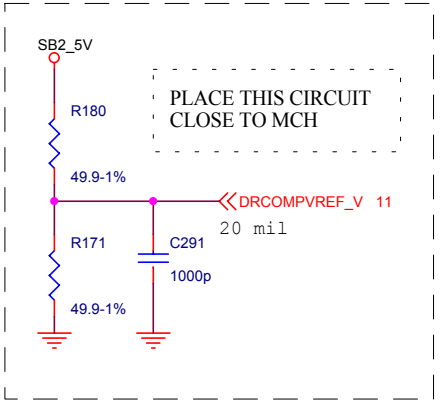
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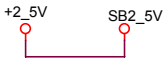






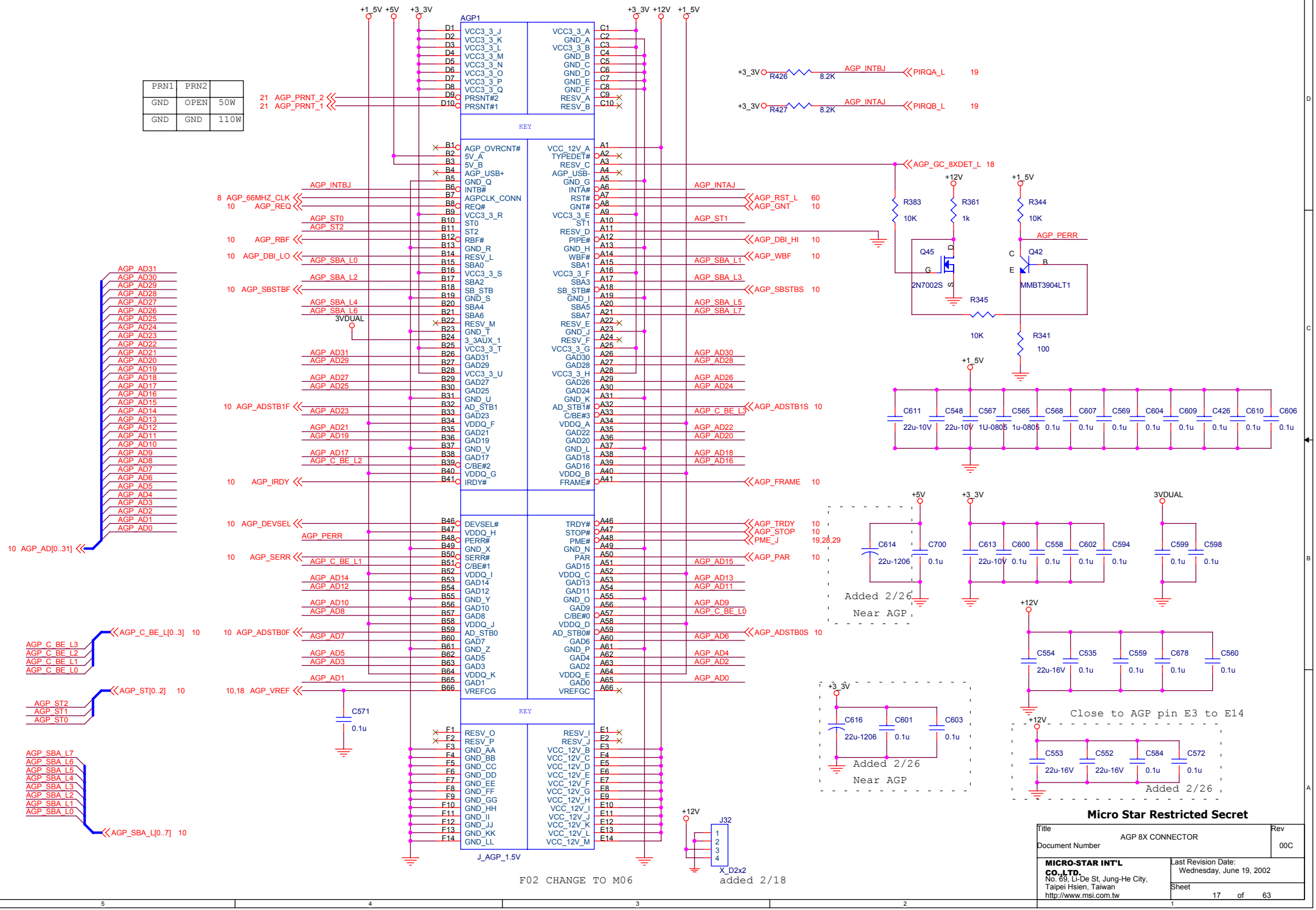


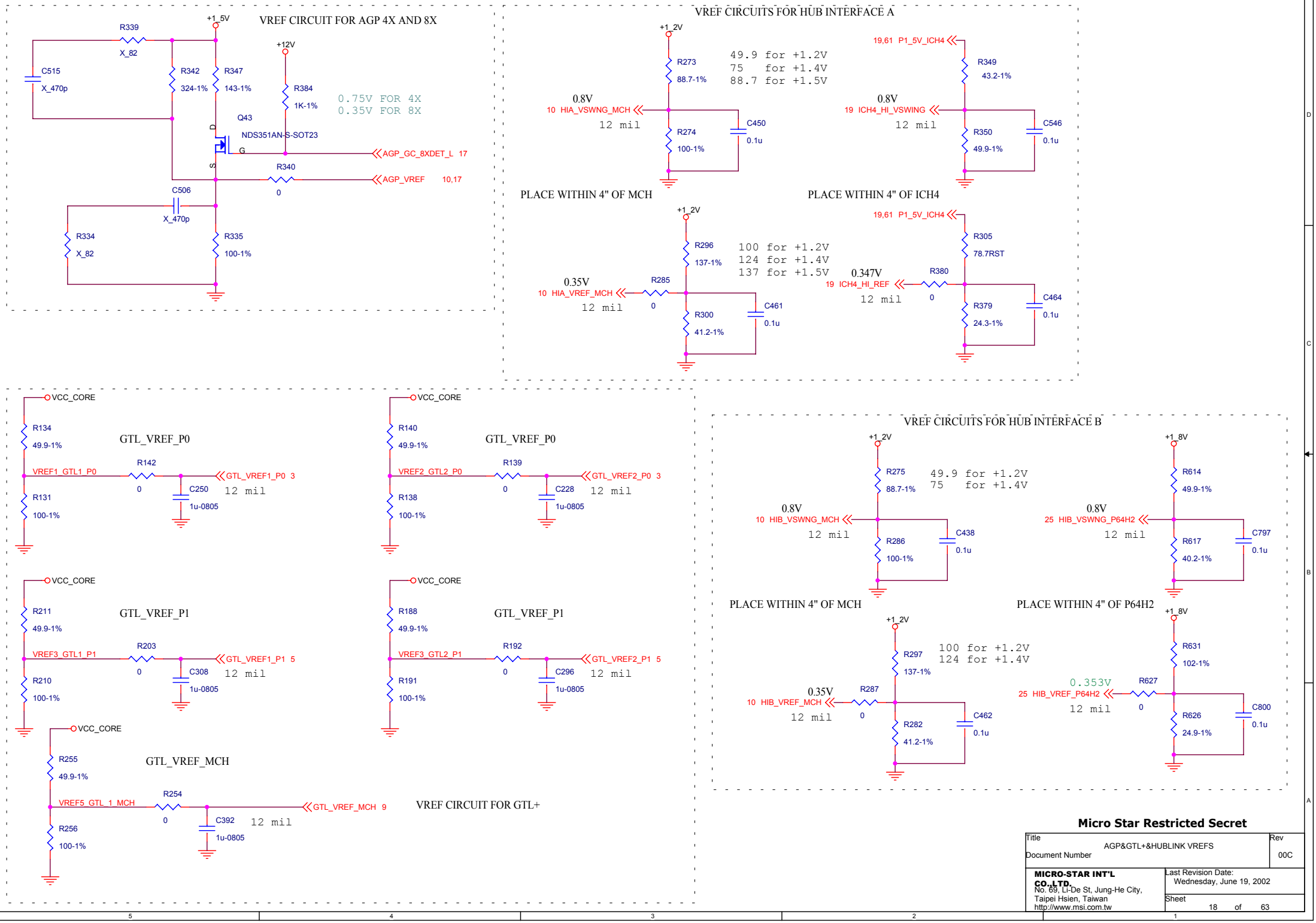




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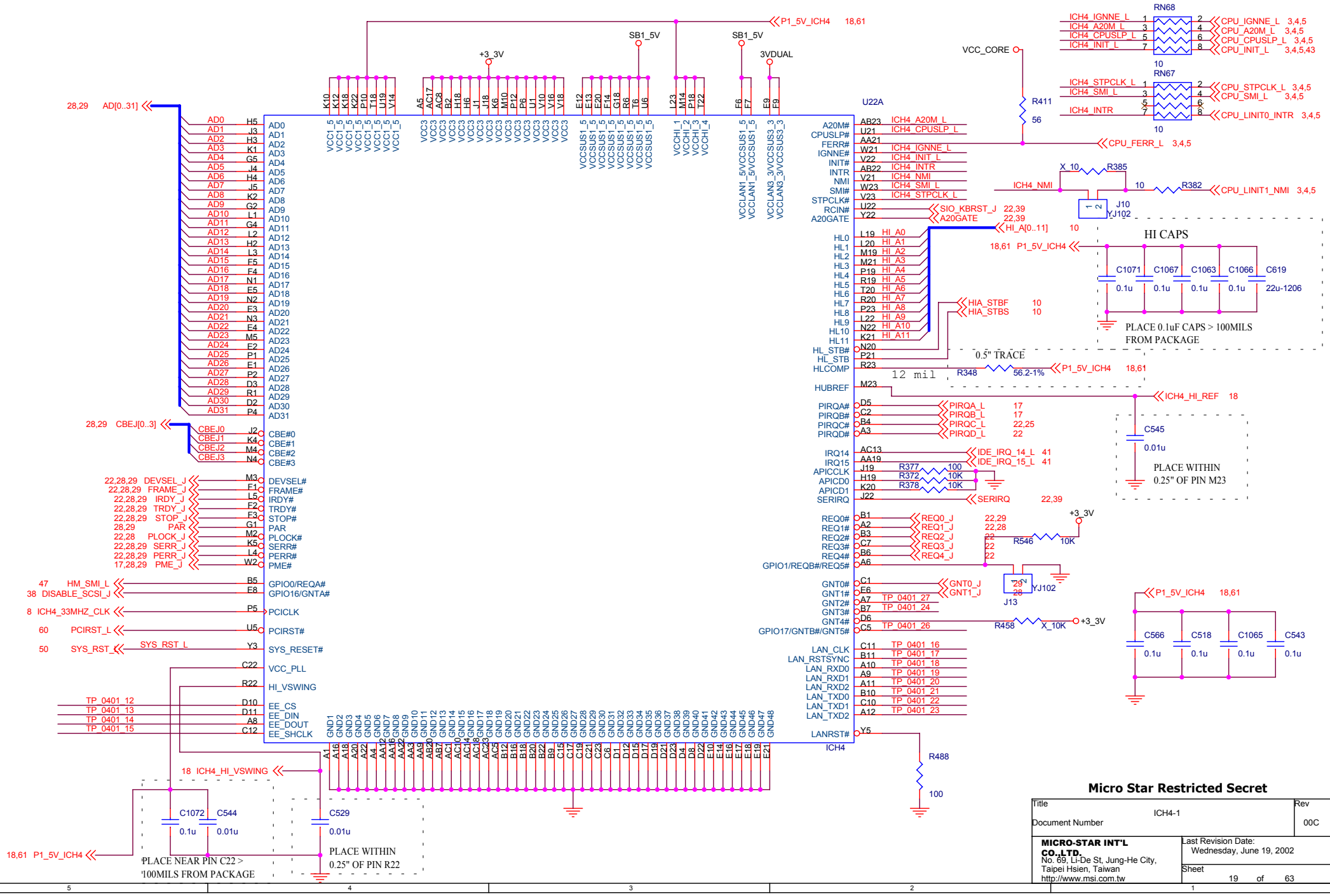
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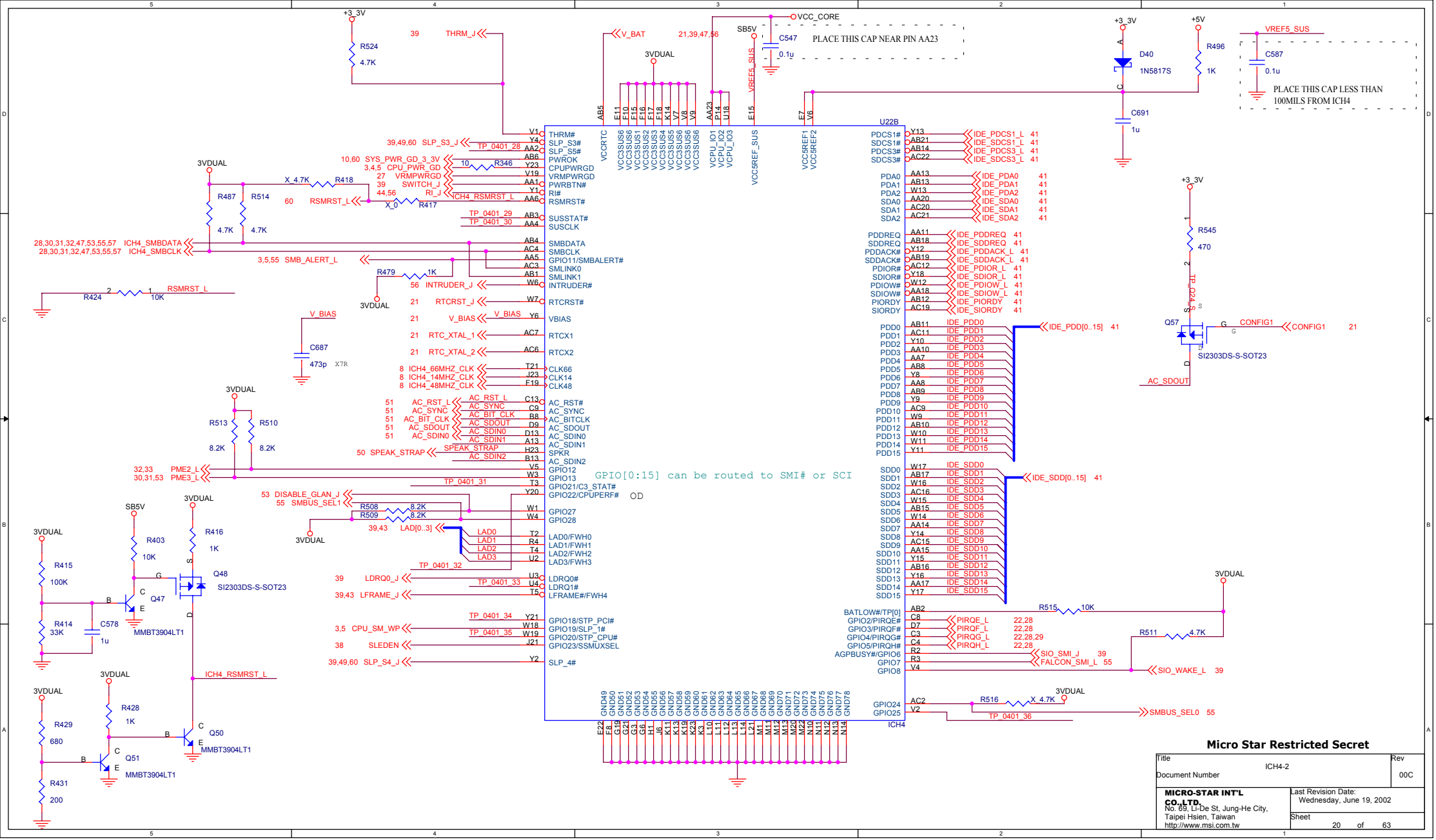

AGP1
PRN1 PRN2
GND OPEN 50W
GND GND 110W
KEY
3VDUAL
F02 CHANGE TO M06
J_AGP_1.5V
X_D2x2
added 2/18
Added 2/26
Near AGP
Close to AGP pin E3 to E14
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AGP 8X CONNECTOR
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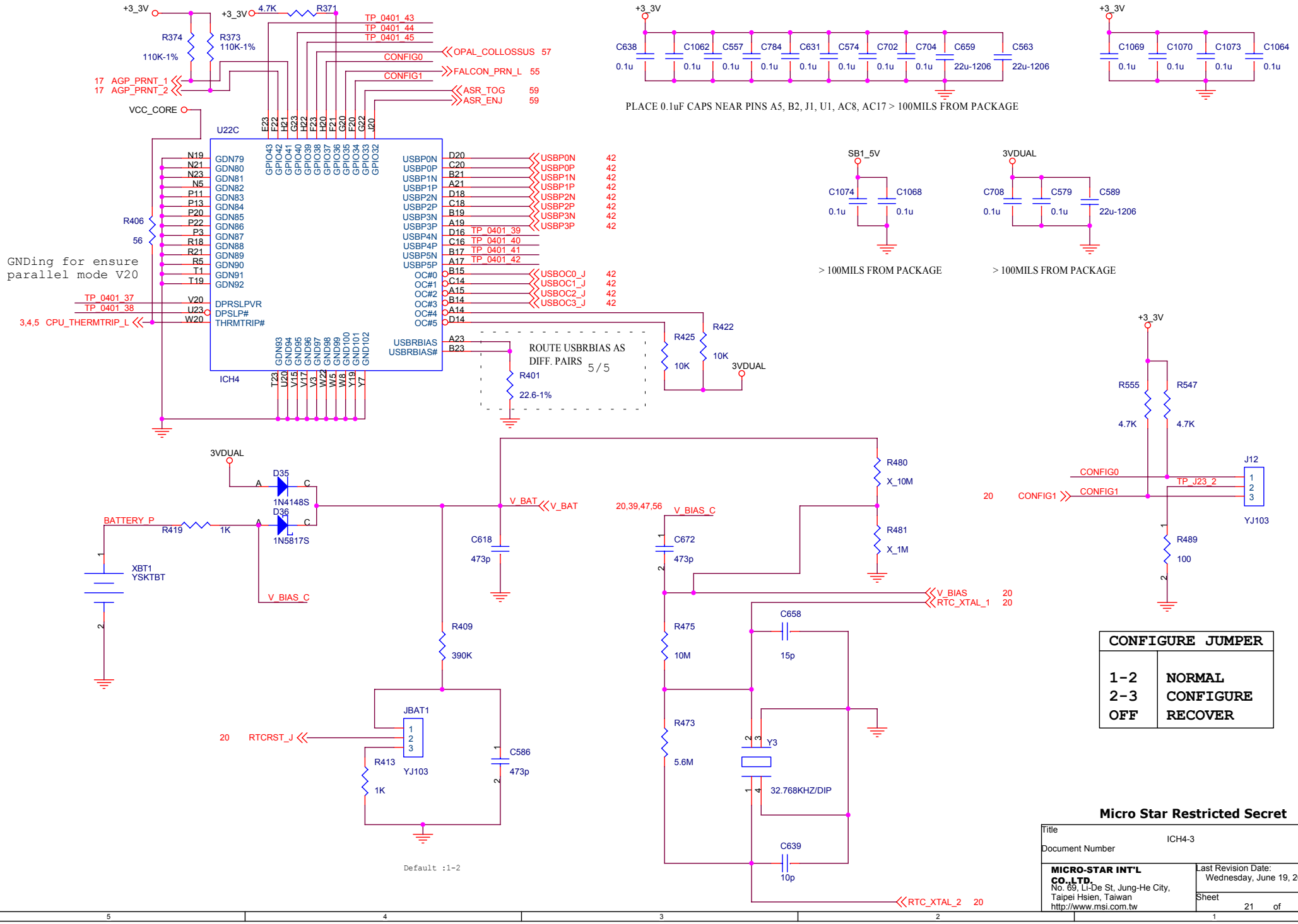


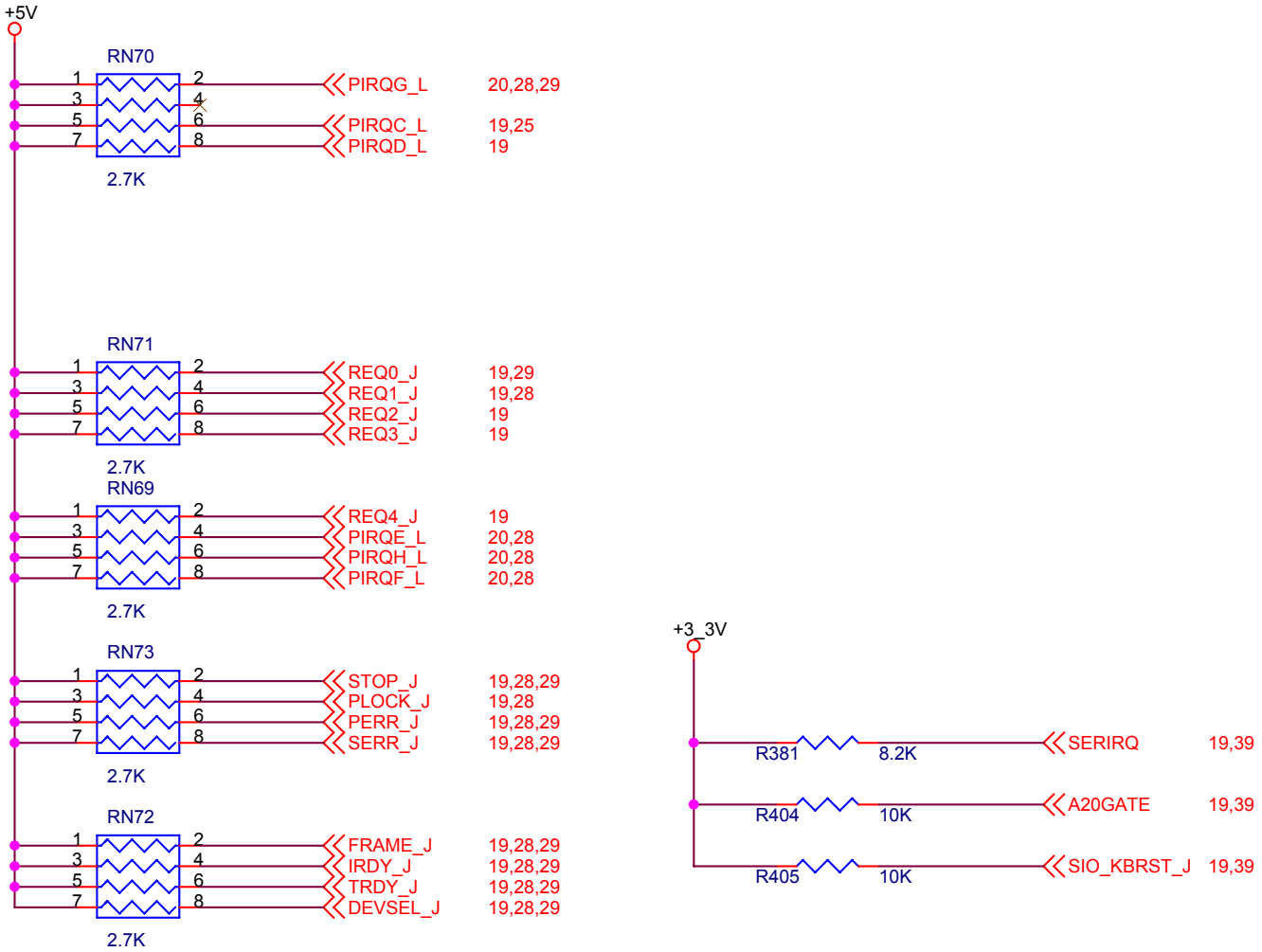
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Title	AGP&GTL+&HUBLINK VREFS	Rev 00C
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U22A
AD[0..31]
CBEJ[0..3]
DEVSEL_J
FRAME_J
IRDY_J
TRDY_J
STOP_J
PAR
PLOCK_J
SERR_J
PERR_J
PME_J
HM_SMI_L
DISABLE_SCSI_J
ICH4_33MHZ_CLK
PCIRST_L
SYS_RST_L
SYS_RST_
HI_VSWING
EE_CS
EE_DIN
EE_DOUT
EE_SHCLK
ICH4_HI_VSWING
P1_5V_ICH4
SB1_5V
+3_3V
3VDUAL
VCC_CORE
R411
56
CPU_FERR_L
RN68
ICH4_IGNNE_L
ICH4_A20M_L
ICH4_CPUSLP_L
ICH4_INIT_L
CPU_IGNNE_L
CPU_A20M_L
CPU_CPUSLP_L
CPU_INIT_L
RN67
ICH4_STPCLK_L
ICH4_SMI_L
ICH4_INTR
CPU_STPCLK_L
CPU_SMI_L
CPU_LINIT0_INTR
ICH4_NMI
R385
R382
CPU_LINIT1_NMI
J10
YJ102
SIO_KBRST_J
A20GATE
HI_A[0..11]
HI CAPS
C1071
C1067
C1063
C1066
C619
0.1u
22u-1206
PLACE 0.1uF CAPS > 100MILS
FROM PACKAGE
HIA_STBF
HIA_STBS
0.5" TRACE
12 mil
R348
56.2-1%
HUBREF
ICH4_HI_REF
C545
0.01u
PLACE WITHIN
0.25" OF PIN M23
PIRQA_L
PIRQB_L
PIRQC_L
PIRQD_L
IDE_IRQ_14_L
IDE_IRQ_15_L
R377
R372
R378
100
10K
SERIRQ
REQ0_J
REQ1_J
REQ2_J
REQ3_J
REQ4_J
R546
GNT0_J
GNT1_J
J13
R458
X_10K
C566
C518
C1065
C543
LANRST#
ICH4
R488
100
C1072
C544
C529
PLACE NEAR PIN C22 >
100MILS FROM PACKAGE
PLACE WITHIN
0.25" OF PIN R22
Micro Star Restricted Secret
Title
ICH4-1
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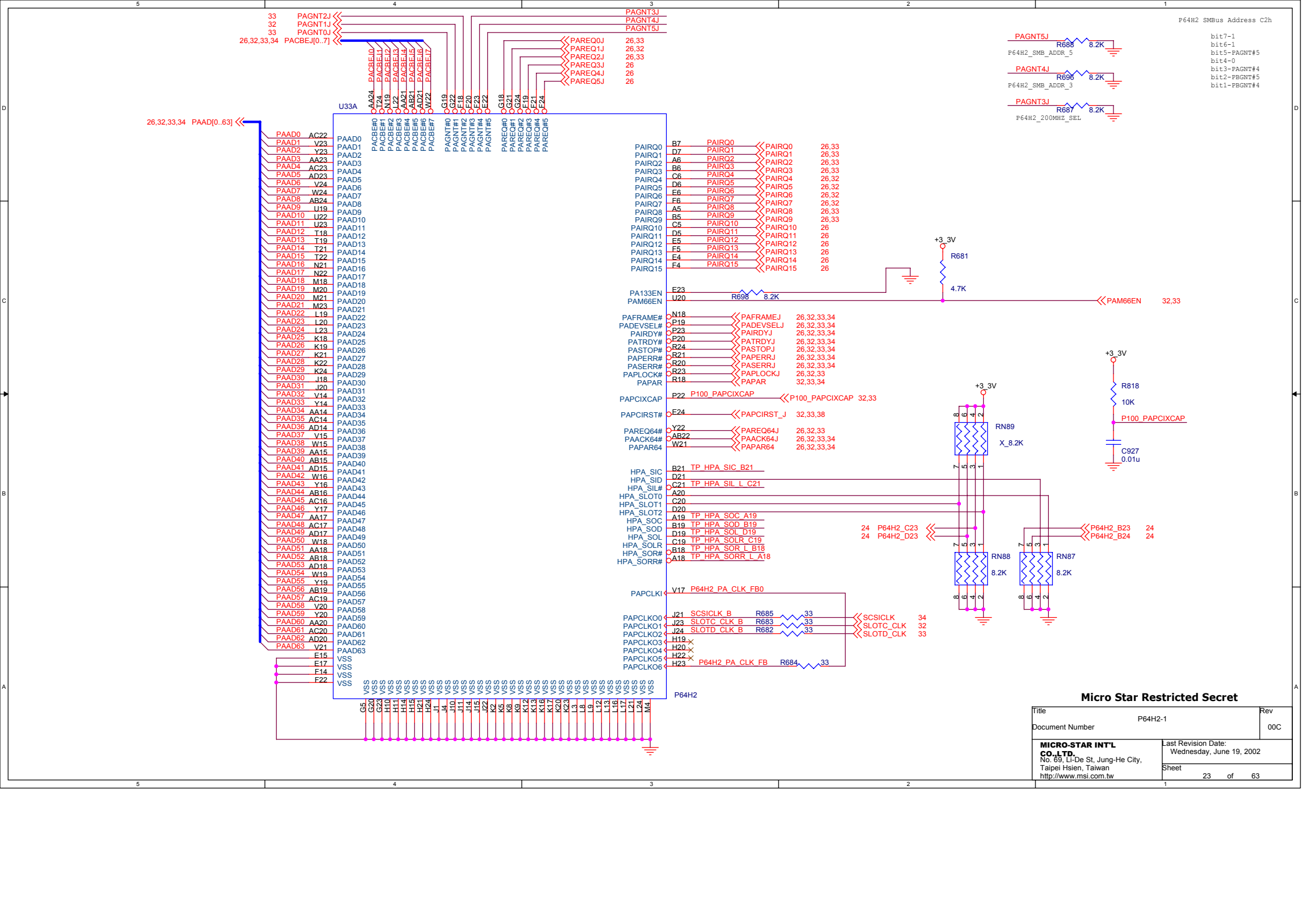

GPIO[0:15] can be routed to SMI# or SCI
PLACE THIS CAP NEAR PIN AA23
PLACE THIS CAP LESS THAN 100MILS FROM ICH4
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Title
ICH4-2
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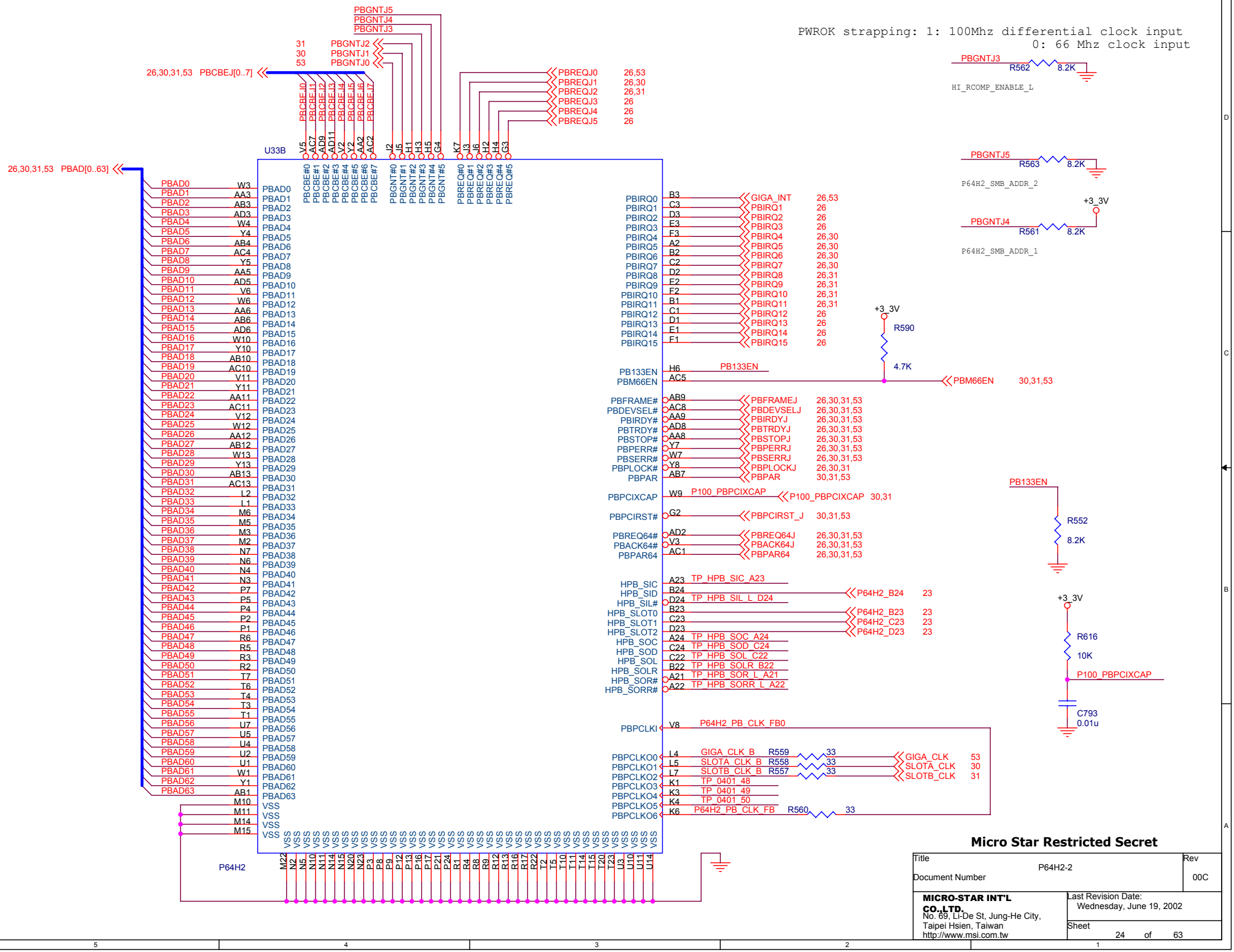

+3_3V
R374
110K-1%
R373
110K-1%
+3_3V
4.7K
R371
TP_0401_43
TP_0401_44
TP_0401_45
CONFIG0
OPAL_COLLOSSUS 57
CONFIG1
FALCON_PRN_L 55
17 AGP_PRNT_1
17 AGP_PRNT_2
ASR_TOG 59
ASR_ENJ 59
VCC_CORE
U22C
ICH4
GNDing for ensure
parallel mode V20
R406
56
TP_0401_37
TP_0401_38
3,4,5 CPU_THERMTRIP_L
DPRSLPVR
DPSLP#
THRMTRIP#
USBP0N 42
USBP0P 42
USBP1N 42
USBP1P 42
USBP2N 42
USBP2P 42
USBP3N 42
USBP3P 42
TP_0401_39
TP_0401_40
TP_0401_41
TP_0401_42
USBOC0_J 42
USBOC1_J 42
USBOC2_J 42
USBOC3_J 42
USBRBIAS
USBRBIAS#
ROUTE USBRBIAS AS
DIFF. PAIRS
5/5
R401
22.6-1%
R425
10K
R422
10K
3VDUAL
+3_3V
C638
C1062
C557
C784
C631
C574
C702
C704
C659
C563
0.1u
22u-1206
PLACE 0.1uF CAPS NEAR PINS A5, B2, J1, U1, AC8, AC17 > 100MILS FROM PACKAGE
+3_3V
C1069
C1070
C1073
C1064
SB1_5V
C1074
C1068
> 100MILS FROM PACKAGE
3VDUAL
C708
C579
C589
> 100MILS FROM PACKAGE
+3_3V
R555
R547
4.7K
CONFIG0
CONFIG1
20
TP_J23_2
J12
YJ103
R489
100
3VDUAL
D35
1N4148S
D36
1N5817S
BATTERY_P
R419
1K
XBT1
YSKTBT
V_BAT
V_BIAS_C
C618
473p
R409
390K
JBAT1
20 RTCRST_J
R413
1K
YJ103
C586
473p
Default :1-2
20,39,47,56
V_BIAS_C
C672
473p
R480
X_10M
R481
X_1M
V_BIAS 20
RTC_XTAL_1 20
R475
10M
C658
15p
R473
5.6M
Y3
32.768KHZ/DIP
C639
10p
RTC_XTAL_2 20
CONFIGURE JUMPER
1-2 NORMAL
2-3 CONFIGURE
OFF RECOVER
Micro Star Restricted Secret
Title
ICH4-3
Rev
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Document Number
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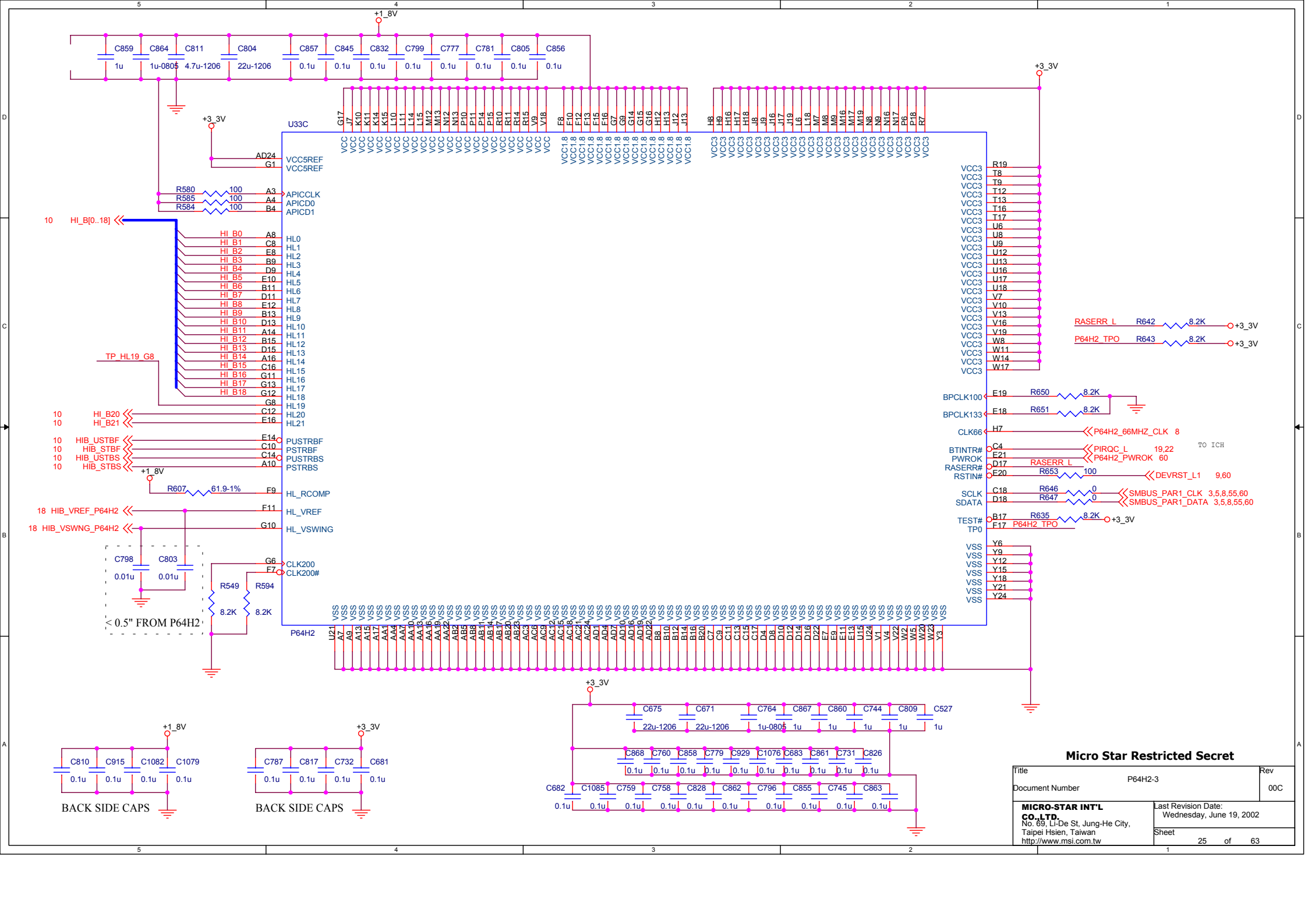


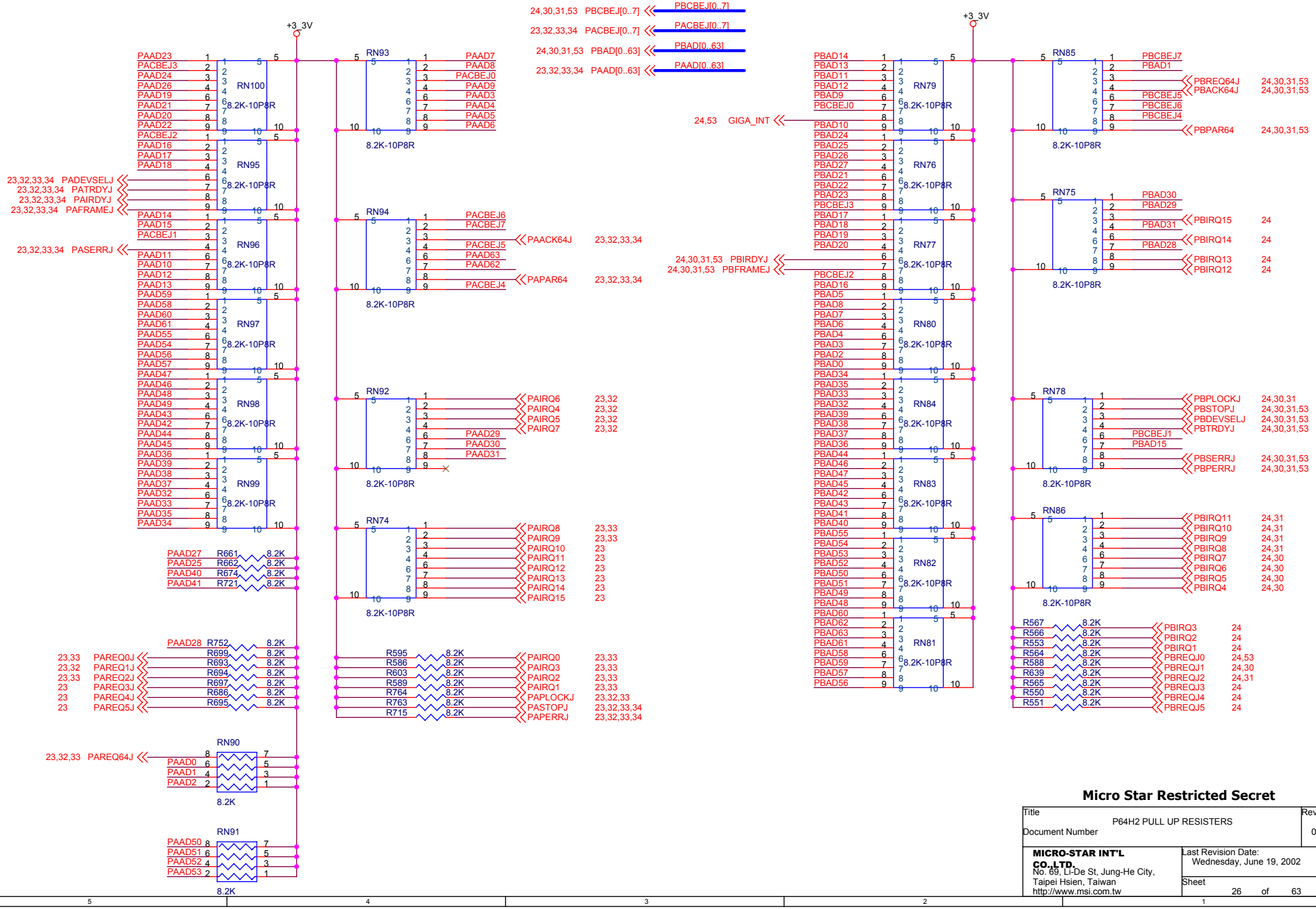
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Title: ICH4 PULL UP		Rev: 00C
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P64H2 SMBus Address C2h
bit7-1
bit6-1
bit5-PAGNT#5
bit4-0
bit3-PAGNT#4
bit2-PBGNT#5
bit1-PBGNT#4
PAGNT5J
R688 8.2K
P64H2_SMB_ADDR_5
PAGNT4J
R696 8.2K
P64H2_SMB_ADDR_3
PAGNT3J
R687 8.2K
P64H2_200MHZ_SEL
33 PAGNT2J
32 PAGNT1J
33 PAGNT0J
26,32,33,34 PACBEJ[0..7]
PAGNT3J
PAGNT4J
PAGNT5J
PAREQ0J 26,33
PAREQ1J 26,32
PAREQ2J 26,33
PAREQ3J 26
PAREQ4J 26
PAREQ5J 26
U33A
26,32,33,34 PAAD[0..63]
P64H2
+3_3V
R681
4.7K
R695 8.2K
PAM66EN 32,33
R818
10K
P100_PAPCIXCAP
C927
0.01u
RN89
X_8.2K
RN88
8.2K
RN87
8.2K
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Title
P64H2-1
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PWROK strapping: 1: 100Mhz differential clock input
0: 66 Mhz clock input
U33B
P64H2
PBGNTJ5
PBGNTJ4
PBGNTJ3
PBGNTJ2
PBGNTJ1
PBGNTJ0
26,30,31,53 PBCBEJ[0..7]
26,30,31,53 PBAD[0..63]
PBREQJ0 26,53
PBREQJ1 26,30
PBREQJ2 26,31
PBREQJ3 26
PBREQJ4 26
PBREQJ5 26
R562 8.2K
HI_RCOMP_ENABLE_L
R563 8.2K
P64H2_SMB_ADDR_2
+3_3V
R561 8.2K
P64H2_SMB_ADDR_1
GIGA_INT 26,53
PBIRQ1 26
PBIRQ2 26
PBIRQ3 26
PBIRQ4 26,30
PBIRQ5 26,30
PBIRQ6 26,30
PBIRQ7 26,30
PBIRQ8 26,31
PBIRQ9 26,31
PBIRQ10 26,31
PBIRQ11 26,31
PBIRQ12 26
PBIRQ13 26
PBIRQ14 26
PBIRQ15 26
R590
4.7K
PB133EN
PBM66EN 30,31,53
PBFRAMEJ 26,30,31,53
PBDEVSELJ 26,30,31,53
PBIRDYJ 26,30,31,53
PBTRDYJ 26,30,31,53
PBSTOPJ 26,30,31,53
PBPERRJ 26,30,31,53
PBSERRJ 26,30,31,53
PBPLOCKJ 26,30,31
PBPAR 30,31,53
P100_PBPCIXCAP 30,31
PBPCIRST_J 30,31,53
PBREQ64J 26,30,31,53
PBACK64J 26,30,31,53
PBPAR64 26,30,31,53
R552
8.2K
TP_HPB_SIC_A23
P64H2_B24 23
TP_HPB_SIL_L_D24
P64H2_B23 23
P64H2_C23 23
P64H2_D23 23
TP_HPB_SOC_A24
TP_HPB_SOD_C24
TP_HPB_SOL_C22
TP_HPB_SOLR_B22
TP_HPB_SOR_L_A21
TP_HPB_SORR_L_A22
R616
10K
C793
0.01u
P64H2_PB_CLK_FB0
GIGA_CLK_B R559 33 GIGA_CLK 53
SLOTA_CLK_B R558 33 SLOTA_CLK 30
SLOTB_CLK_B R557 33 SLOTB_CLK 31
TP_0401_48
TP_0401_49
TP_0401_50
P64H2_PB_CLK_FB R560 33
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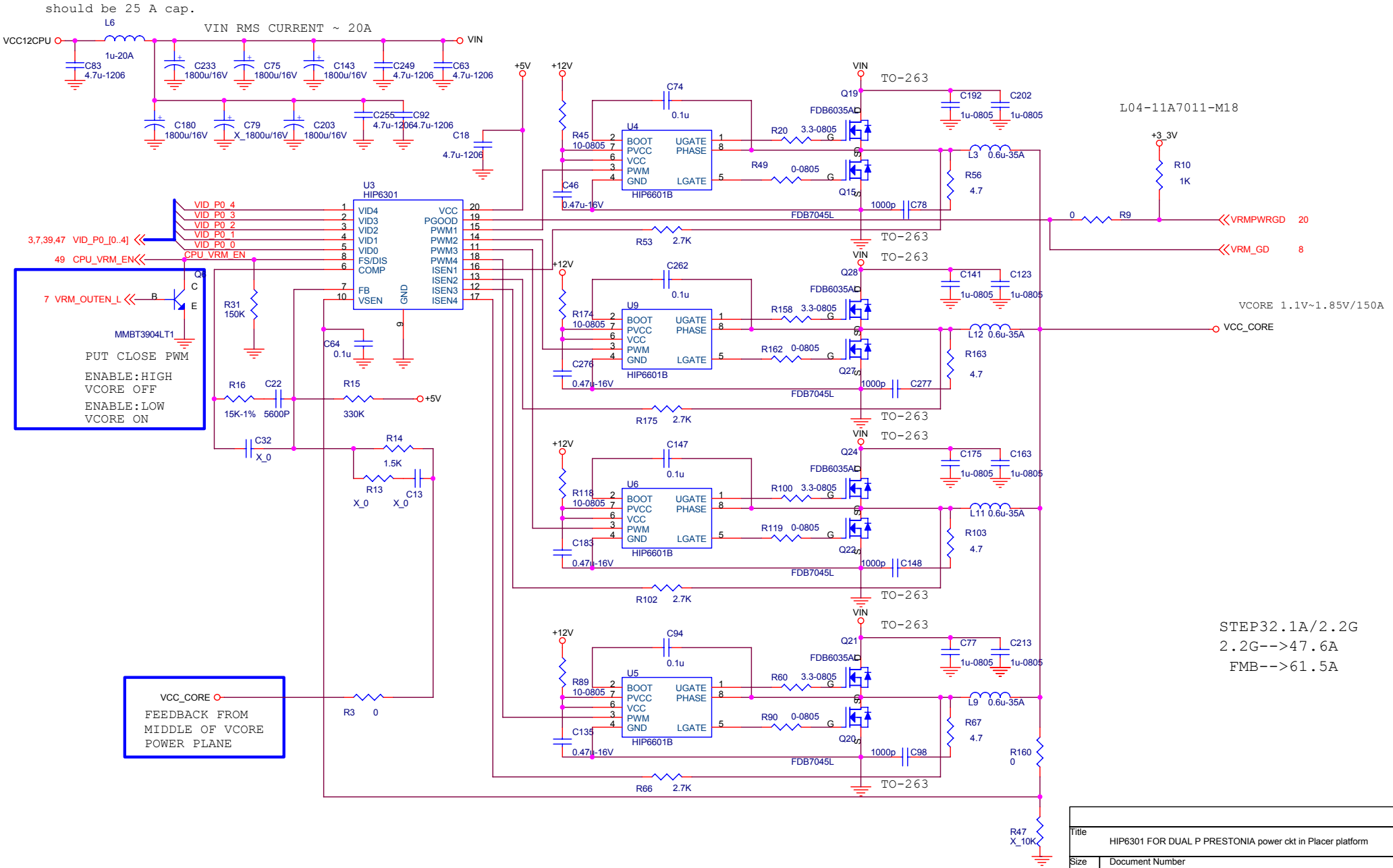

+1_8V
C859 1u
C864 1u-0805
C811 4.7u-1206
C804 22u-1206
C857 0.1u
C845 0.1u
C832 0.1u
C799 0.1u
C777 0.1u
C781 0.1u
C805 0.1u
C856 0.1u
+3_3V
U33C
AD24 VCC5REF
G1 VCC5REF
R580 100 A3 APICCLK
R585 100 A4 APICD0
R584 100 B4 APICD1
10 HI_B[0..18]
HI_B0 A8 HL0
HI_B1 C8 HL1
HI_B2 E8 HL2
HI_B3 B9 HL3
HI_B4 D9 HL4
HI_B5 E10 HL5
HI_B6 B11 HL6
HI_B7 D11 HL7
HI_B8 E12 HL8
HI_B9 B13 HL9
HI_B10 D13 HL10
HI_B11 A14 HL11
HI_B12 B15 HL12
HI_B13 D15 HL13
HI_B14 A16 HL14
HI_B15 C16 HL15
HI_B16 G11 HL16
HI_B17 G13 HL17
HI_B18 G12 HL18
TP_HL19_G8 G8 HL19
10 HI_B20 C12 HL20
10 HI_B21 E16 HL21
10 HIB_USTBF E14 PUSTRBF
10 HIB_STBF C10 PSTRBF
10 HIB_USTBS C14 PUSTRBS
10 HIB_STBS A10 PSTRBS
+1_8V
R607 61.9-1% F9 HL_RCOMP
18 HIB_VREF_P64H2 F11 HL_VREF
18 HIB_VSWNG_P64H2 G10 HL_VSWING
C798 0.01u
C803 0.01u
R549 8.2K
R594 8.2K
< 0.5" FROM P64H2
G6 CLK200
F7 CLK200#
P64H2
+3_3V
R19 R8 R9 R12 R13 R16 R17 U6 U8 U9 U12 U13 U16 U17 U18 V7 V10 V13 V16 V19 W8 W11 W14 W17
RASERR_L R642 8.2K +3_3V
P64H2_TPO R643 8.2K +3_3V
BPCLK100 E19 R650 8.2K
BPCLK133 E18 R651 8.2K
CLK66 H7 P64H2_66MHZ_CLK 8
BTINTR# C4 PIRQC_L 19,22
PWROK E21 P64H2_PWROK 60
RASERR# D17 RASERR_L
RSTIN# E20 R653 100 DEVRST_L1 9,60
TO ICH
SCLK C18 R646 0 SMBUS_PAR1_CLK 3,5,8,55,60
SDATA D18 R647 0 SMBUS_PAR1_DATA 3,5,8,55,60
TEST# B17 R635 8.2K +3_3V
TP0 F17 P64H2_TPO
Y6 Y9 Y12 Y15 Y18 Y21 Y24
+3_3V
C675 22u-1206
C671 22u-1206
C764 1u-0805
C867 1u
C860 1u
C744 1u
C809 1u
C527 1u
C868 C760 C858 C779 C929 C1076 C683 C861 C731 C826 0.1u
C682 C1085 C759 C758 C828 C862 C796 C855 C745 C863 0.1u
+1_8V
C810 C915 C1082 C1079 0.1u
BACK SIDE CAPS
+3_3V
C787 C817 C732 C681 0.1u
BACK SIDE CAPS
Micro Star Restricted Secret
Title P64H2-3
Rev 00C
Document Number
MICRO-STAR INT'L CO.,LTD.
No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan
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Title	P64H2 PULL UP RESISTERS
Document Number	Rev 00C
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw	Last Revision Date: Wednesday, June 19, 2002
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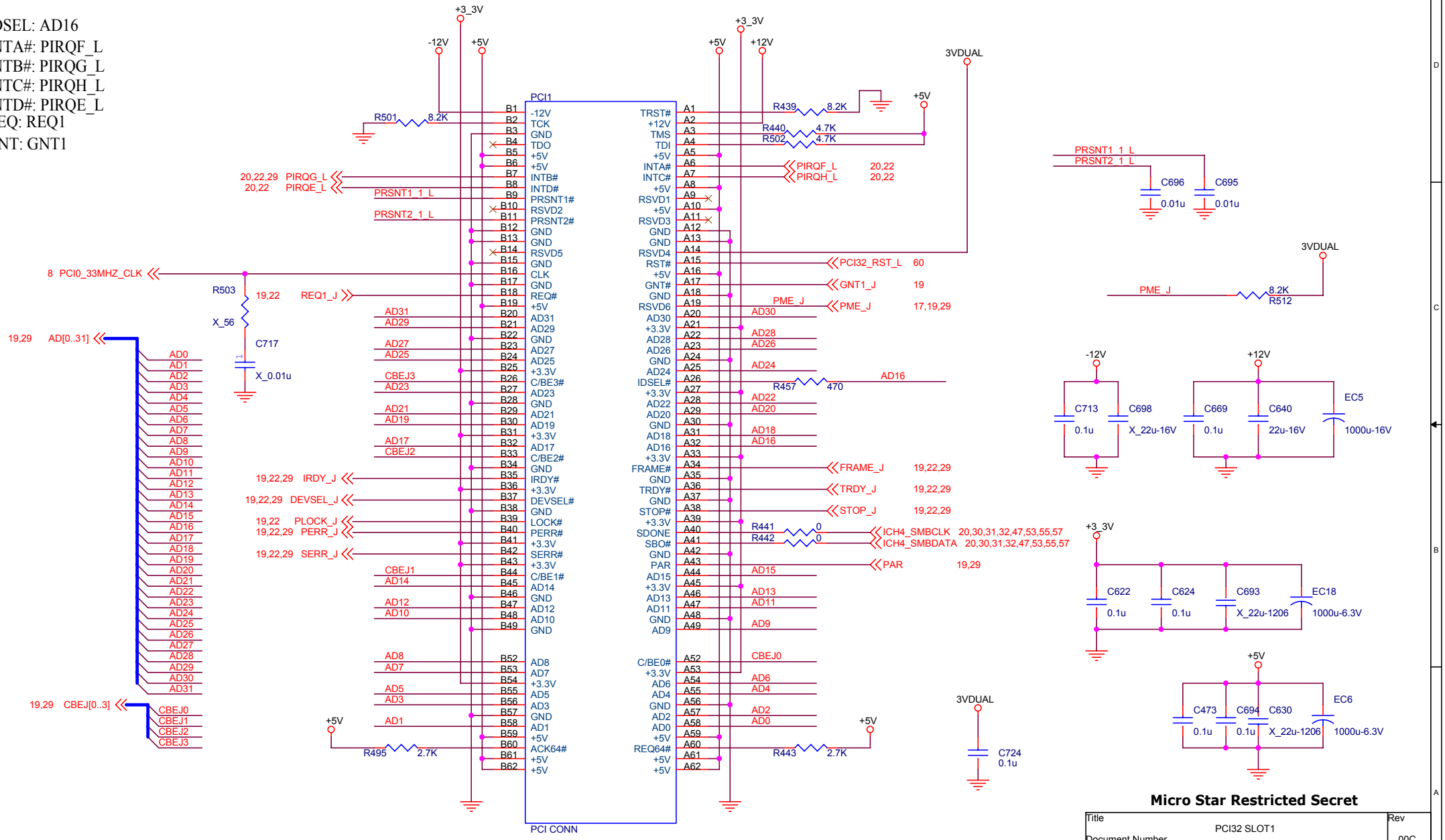
HIP6301 FOR INTEL Dual P PRESTONIA POWER CKT



Title	HIP6301 FOR DUAL P PRESTONIA power ckt in Placer platform	
Size B	Document Number 2002/06/06	Rev 00C
Date: Wednesday, June 19, 2002	Sheet 27 of 63	

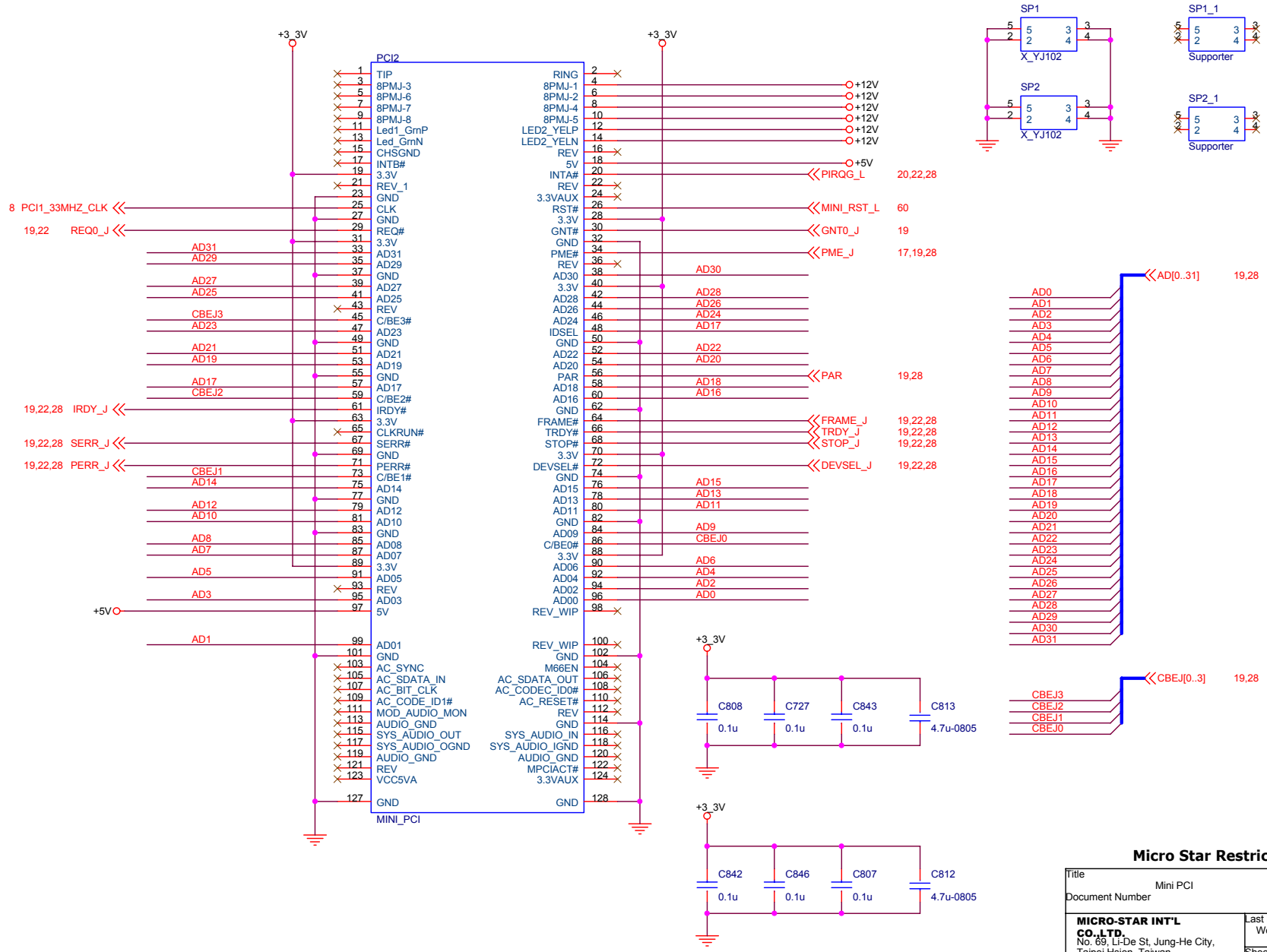
PCI32 SLOT

IDSEL: AD16
INTA#: PIRQF_L
INTB#: PIRQG_L
INTC#: PIRQH_L
INTD#: PIRQE_L
REQ: REQ1
GNT: GNT1



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Title	PCI32 SLOT1	Rev 00C
Document Number		
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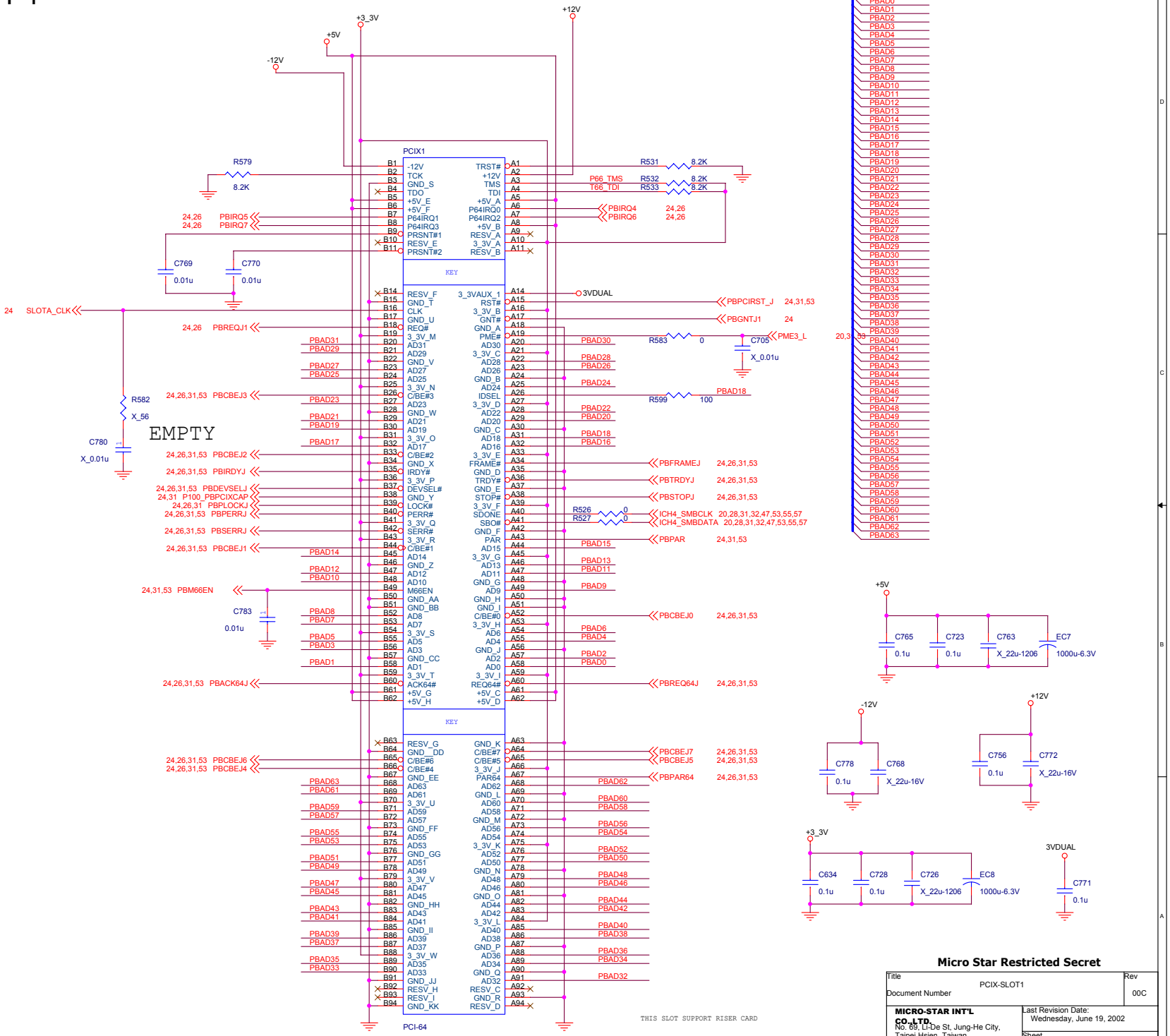


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Title: Mini PCI	Rev: 00C
Document Number:	
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw	Last Revision Date: Wednesday, June 19, 2002 Sheet 29 of 63

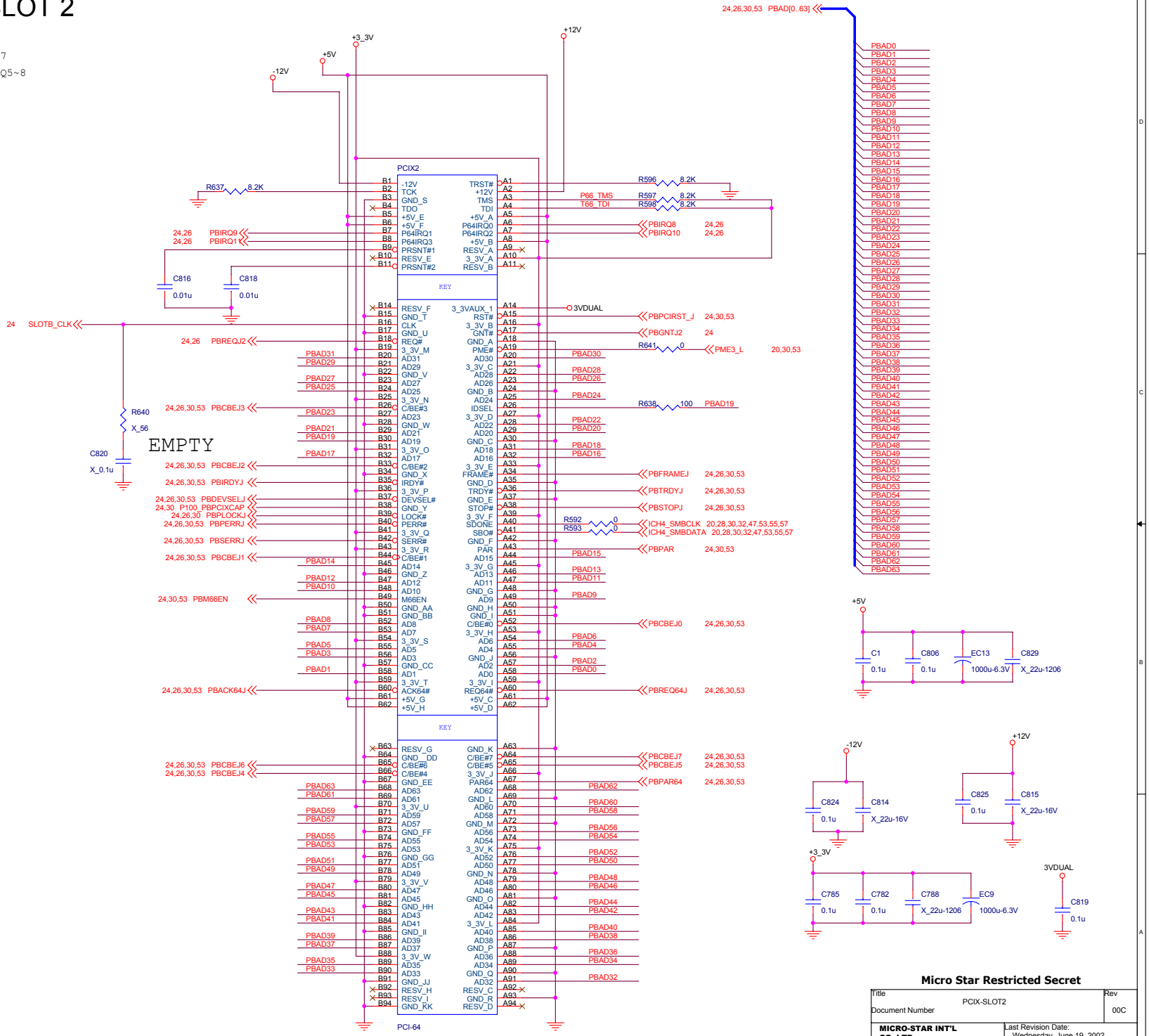
PCIX SLOT 1

IDSEL: PBAD26
INTA~D : PBIRQ1~4
REQ: PBREQ1
GNT: PBGNT1



PCIX SLOT 2

IDSEL: PBAD27
INTA~D : PBIRQ5~8
REQ: PBREQ2
GNT: PBGNT2

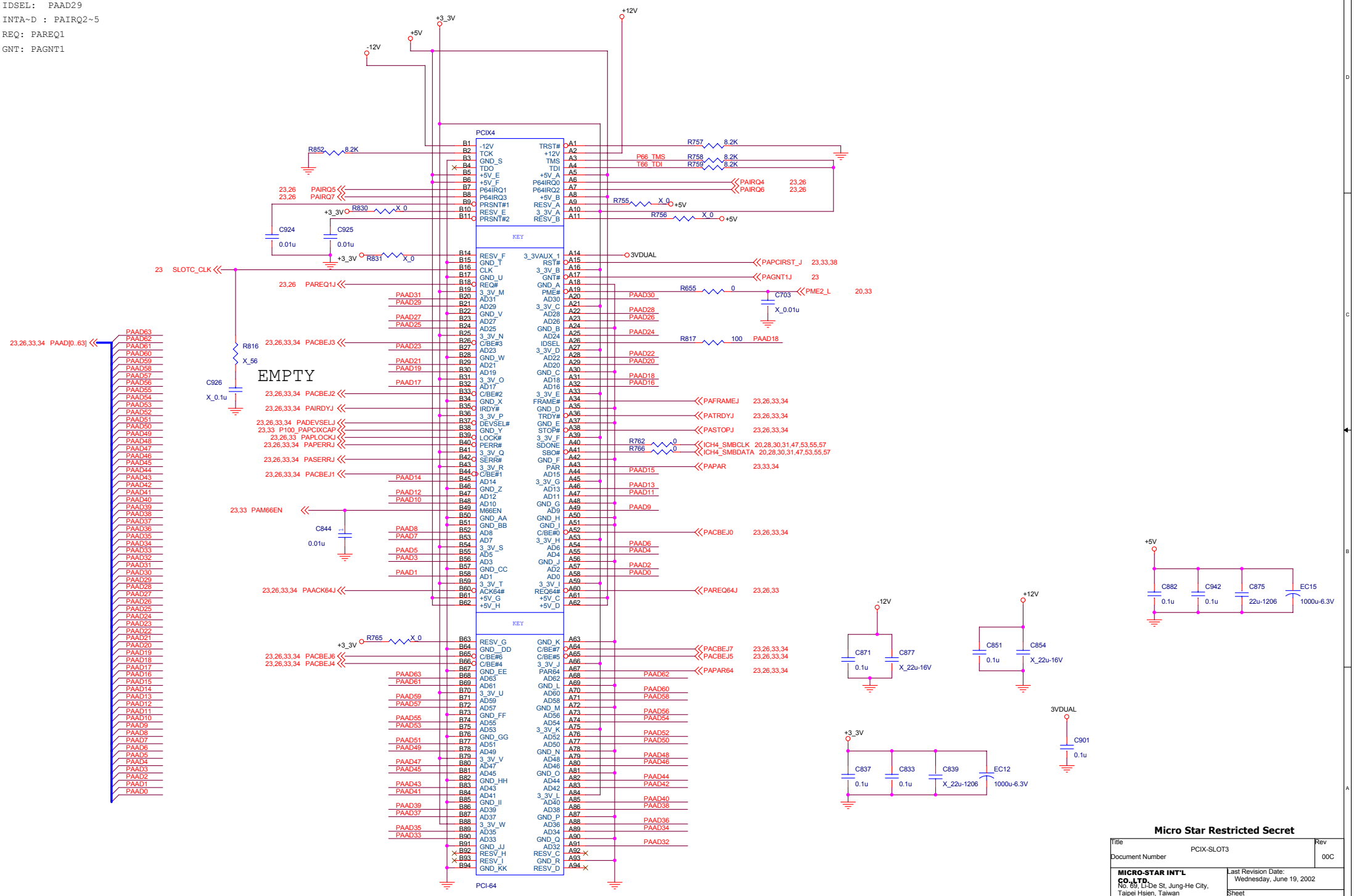


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Document Number		
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PCIX SLOT 3

IDSEL: PAAD29
INTA~D : PAIRQ2~5
REQ: PAREQ1
GNT: PAGNT1



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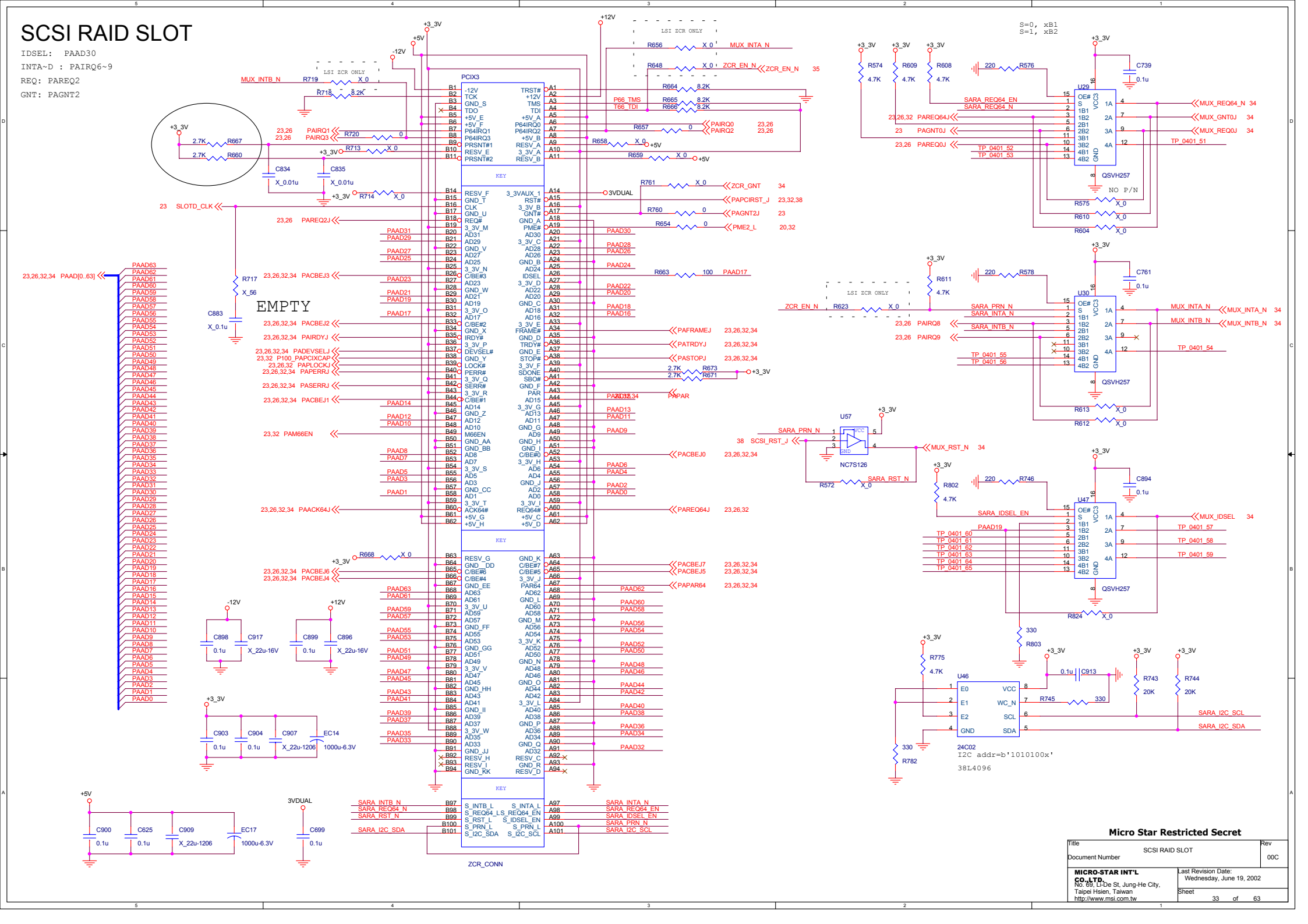
SCSI RAID SLOT

IDSEL: PAAD30

INTA~D : PAIRQ6~9

REQ: PAREQ2

GNT: PAGNT2



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Title	SCSI RAID SLOT	Rev 00C
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SCSI

IDSEL: PAAD28

INTA,B : PAIRQ0,1

REQ: PAREQ0

GNT: PAGNT0

SCSI_VCC3

LSI ZCR ONLY

R675 X_4.7K

R624 X_4.7K

R605 X_4.7K

ZCR_GNT

R672 0

R644 100

SCSICLK

MUX_RST_N

MUX_GNT0J

MUX_IDSEL

U34B

53C1030

CLK

RST#

GNT#

IDSEL

M66EN

TCK_CHIP

TMS_CHIP

TDI_CHIP

TDO_CHIP

PCI

INTA#

INTB#

ALT_INTA#

ALT_INTB#

REQ#

MUX_INTA_N

MUX_INTB_N

MUX_REQ0J

TP_SS10

TP_SS11

TP_SS12

TP_SS13

TP_SS14

TP_SS15

23,26,32,33 PAAD[0..63]

ROUTE PCIX 50~60 Ohm

PAAD63 PAAD62 PAAD61 PAAD60 PAAD59 PAAD58 PAAD57 PAAD56 PAAD55 PAAD54 PAAD53 PAAD52 PAAD51 PAAD50 PAAD49 PAAD48 PAAD47 PAAD46 PAAD45 PAAD44 PAAD43 PAAD42 PAAD41 PAAD40 PAAD39 PAAD38 PAAD37 PAAD36 PAAD35 PAAD34 PAAD33 PAAD32 PAAD31 PAAD30 PAAD29 PAAD28 PAAD27 PAAD26 PAAD25 PAAD24 PAAD23 PAAD22 PAAD21 PAAD20 PAAD19 PAAD18 PAAD17 PAAD16 PAAD15 PAAD14 PAAD13 PAAD12 PAAD11 PAAD10 PAAD9 PAAD8 PAAD7 PAAD6 PAAD5 PAAD4 PAAD3 PAAD2 PAAD1 PAAD0

AD63 AD62 AD61 AD60 AD59 AD58 AD57 AD56 AD55 AD54 AD53 AD52 AD51 AD50 AD49 AD48 AD47 AD46 AD45 AD44 AD43 AD42 AD41 AD40 AD39 AD38 AD37 AD36 AD35 AD34 AD33 AD32 AD31 AD30 AD29 AD28 AD27 AD26 AD25 AD24 AD23 AD22 AD21 AD20 AD19 AD18 AD17 AD16 AD15 AD14 AD13 AD12 AD11 AD10 AD09 AD08 AD07 AD06 AD05 AD04 AD03 AD02 AD01 AD00

ACK64# REQ64# PAR64 FRAME# IRDY# PAR DEVSEL# TRDY# STOP# PERR# SERR#

PAACK64J PAREQ64_N PAPAR64 PAFRAMEJ PAIRDYJ PAPAR PADEVSELJ PATRDYJ PASTOPJ PAPERRJ PASERRJ

23,26,32,33

CBE7# CBE6# CBE5# CBE4# CBE3# CBE2# CBE1# CBE0#

PACBEJ[0..7]

PACBEJ7 PACBEJ6 PACBEJ5 PACBEJ4 PACBEJ3 PACBEJ2 PACBEJ1 PACBEJ0

GPIO0 GPIO1 GPIO2 GPIO3 GPIO4 GPIO5 GPIO6 GPIO7

TP_SS16 TP_SS17 TP_SS8 TP_SS18 TP_SS19 TP_SS9 TP_SS7 TP_SS20

SerialCLK SerialDATA

SERIALCLK SERIALDATA

SCSI_VCC3

R804 220

D45 GNx2-S-GN

added 2/18

HBLED#

MEMORY

MAD15 MAD14 MAD13 MAD12 MAD11 MAD10 MAD9 MAD8 MAD7 MAD6 MAD5 MAD4 MAD3 MAD2 MAD1 MAD0

Enable 133 PCI-X
Enable 64 bit
Enable 66 PCI-X
RSV
ID control[1]
ID control[0]
RSV
RSV
Enable EEPROM auto-download
Enable IOP boot/?
PCI function and SCSI Channel configuration
RSV
ROM size configuration
RSV

FLSHALE1# FLSHALE0# FLSHCE MPAR1 MPAR0 ADV# ADSC# BWE1# BWE0# MOE# RAMCE# MCLK

FLSHALE1_L FLSHALE0_L

MPAR1 MPAR0

Enable PCI-X

TP_SS2 TP_SS3 TP_SS4 TP_SS6 TP_SS5

NVRAM_WE_L NVRAM_OE_L NVRAM_CE_L

NC R600 X_4.7K SCSI_VCC3

SCSI_VCC3

FB22 80-0805 added 2/18

OSC1 VCC OUT GND NC 80MHZ

C757 0.1u

R577 22

SCLK

53C1030

MAD7 MAD6 MAD5 MAD4 MAD3 MAD2 MAD1 MAD0

MAD[0..7] 38

R704 R770 R767 R768 R701 R739 R702 R735 R738 R703 R705 R771 R769 R772 R736 R737 R709 R710

X_4.7K 4.7K

U34C

53C1030

PCI5VBIAS8 PCI5VBIAS7 PCI5VBIAS6 PCI5VBIAS5 PCI5VBIAS4 PCI5VBIAS3 PCI5VBIAS2 PCI5VBIAS1 PCI5VBIAS0

VDDC12 VDDC11 VDDC10 VDDC9 VDDC8 VDDC7 VDDC6 VDDC5 VDDC4 VDDC3 VDDC2 VDDC1 VDDC0

VSSC13 VSSC12 VSSC11 VSSC10 VSSC9 VSSC8 VSSC7 VSSC6 VSSC5 VSSC4 VSSC3 VSSC2 VSSC1 VSSC0

VDD-IO45 VDD-IO44 VDD-IO43 VDD-IO42 VDD-IO41 VDD-IO40 VDD-IO39 VDD-IO38 VDD-IO37 VDD-IO36 VDD-IO35 VDD-IO34 VDD-IO33 VDD-IO32 VDD-IO31 VDD-IO30 VDD-IO29 VDD-IO28 VDD-IO27 VDD-IO26 VDD-IO25 VDD-IO24 VDD-IO23 VDD-IO22 VDD-IO21 VDD-IO20 VDD-IO19 VDD-IO18 VDD-IO17 VDD-IO16 VDD-IO15 VDD-IO14 VDD-IO13 VDD-IO12 VDD-IO11 VDD-IO10 VDD-IO9 VDD-IO8 VDD-IO7 VDD-IO6 VDD-IO5 VDD-IO4 VDD-IO3 VDD-IO2 VDD-IO1 VDD-IO0

GND0 GND1 GND2 GND3 GND4 GND5 GND6 GND7 GND8 GND9 GND10 GND11 GND12 GND13 GND14 GND15 GND16 GND17 GND18 GND19 GND20 GND21 GND22 GND23 GND24 GND25 GND26 GND27 GND28 GND29 GND30 GND31 GND32 GND33 GND34 GND35 GND36 GND37 GND38 GND39 GND40 GND41 GND42 GND43 GND44 GND45 GND46 GND47 GND48 GND49 GND50 GND51 GND52 GND53 GND54 GND55 GND56 GND57 GND58 GND59 GND60 GND61 GND62 GND63 GND64 GND65 GND66 GND67 GND68 GND69 GND70 GND71 GND72 GND73 GND74 GND75 GND76 GND77 GND78 GND79 GND80 GND81

PWR/ GND

+3_3V

SCSI_VDDC

SCSI_VCC3

53C1030

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PLACE CLOSE THE SCSI CONTROLLER

36 DIFFSENSEA — R570 22K — A_DIFFSENS
R569 220K — C748 0.1u

37 DIFFSENSEB — R740 22K — B_DIFFSENS
R773 220K — C890 0.1u

U34A 53C1030

SCSI

LVSCDAP15	W5	A_SD15+	A_SD15-	Y1	LVSCDAM15
LVSCDAP14	Y2	A_SD14+	A_SD14-	AA2	LVSCDAM14
LVSCDAP13	AA3	A_SD13+	A_SD13-	AB2	LVSCDAM13
LVSCDAP12	AC1	A_SD12+	A_SD12-	AD1	LVSCDAM12
LVSCDAP11	D1	A_SD11+	A_SD11-	F2	LVSCDAM11
LVSCDAP10	G1	A_SD10+	A_SD10-	G2	LVSCDAM10
LVSCDAP9	H4	A_SD9+	A_SD9-	J4	LVSCDAM9
LVSCDAP8	H2	A_SD8+	A_SD8-	H1	LVSCDAM8
LVSCDAP7	P3	A_SD7+	A_SD7-	R4	LVSCDAM7
LVSCDAP6	R5	A_SD6+	A_SD6-	T5	LVSCDAM6
LVSCDAP5	R2	A_SD5+	A_SD5-	T2	LVSCDAM5
LVSCDAP4	T4	A_SD4+	A_SD4-	U2	LVSCDAM4
LVSCDAP3	U4	A_SD3+	A_SD3-	U5	LVSCDAM3
LVSCDAP2	U3	A_SD2+	A_SD2-	V2	LVSCDAM2
LVSCDAP1	V5	A_SD1+	A_SD1-	V4	LVSCDAM1
LVSCDAP0	V3	A_SD0+	A_SD0-	W4	LVSCDAM0
36 LVCSDAPHP	W1	A_SDP1+	A_SDP1-	W2	36 LVCSDAPHM
36 LVCSDAPLP	P5	A_SDP0+	A_SDP0-	P4	36 LVCSDAPLM
36 LVATNAP	N5	A_SATN+	A_SATN-	M4	36 LVATNAM
36 LVBSYAP	N4	A_SBSY+	A_SBSY-	N3	36 LVBSYAM
36 LVACKAP	L5	A_SACK+	A_SACK-	M5	36 LVACKAM
36 LVRSTAP	M2	A_SRST+	A_SRST-	M1	36 LVRSTAM
36 LVMSGAP	L1	A_SMSG+	A_SMSG-	L2	36 LVMSGAM
36 LVSELAP	K2	A_SSEL+	A_SSEL-	L4	36 LVSELAM
36 LVCDAP	K4	A_SC_D+	A_SC_D-	K3	36 LVCDAM
36 LVREQAP	J3	A_SREQ+	A_SREQ-	J2	36 LVREQAM
36 LVIOAP	J5	A_SI_O+	A_SI_O-	K5	36 LVIOAM
LVSCDBP15	B7	B_SD15+	B_SD15-	D8	LVSCDBM15
LVSCDBP14	B6	B_SD14+	B_SD14-	E8	LVSCDBM14
LVSCDBP13	B5	B_SD13+	B_SD13-	C6	LVSCDBM13
LVSCDBP12	A4	B_SD12+	B_SD12-	A3	LVSCDBM12
LVSCDBP11	A23	B_SD11+	B_SD11-	B21	LVSCDBM11
LVSCDBP10	B20	B_SD10+	B_SD10-	D19	LVSCDBM10
LVSCDBP9	A20	B_SD9+	B_SD9-	E18	LVSCDBM9
LVSCDBP8	B19	B_SD8+	B_SD8-	A19	LVSCDBM8
LVSCDBP7	E12	B_SD7+	B_SD7-	E13	LVSCDBM7
LVSCDBP6	B12	B_SD6+	B_SD6-	A12	LVSCDBM6
LVSCDBP5	D11	B_SD5+	B_SD5-	B11	LVSCDBM5
LVSCDBP4	C10	B_SD4+	B_SD4-	B10	LVSCDBM4
LVSCDBP3	E11	B_SD3+	B_SD3-	D10	LVSCDBM3
LVSCDBP2	E10	B_SD2+	B_SD2-	C9	LVSCDBM2
LVSCDBP1	B9	B_SD1+	B_SD1-	D9	LVSCDBM1
LVSCDBP0	A8	B_SD0+	B_SD0-	B8	LVSCDBM0
37 LVCSDBPHP	E9	B_SDP1+	B_SDP1-	A7	37 LVCSDBPHM
37 LVCSDBPLP	C13	B_SDP0+	B_SDP0-	D12	37 LVCSDBPLM
37 LVATNBP	D13	B_SATN+	B_SATN-	B14	37 LVATNBM
37 LVBSYBP	B15	B_SBSY+	B_SBSY-	A15	37 LVBSYBM
37 LVACKBP	E14	B_SACK+	B_SACK-	D14	37 LVACKBM
37 LVRSTBP	A16	B_SRST+	B_SRST-	E15	37 LVRSTBM
37 LVMSGBP	D16	B_SMSG+	B_SMSG-	B16	37 LVMSGBM
37 LVSELBP	C17	B_SSEL+	B_SSEL-	B17	37 LVSELBM
37 LVCDBP	D17	B_SC_D+	B_SC_D-	E16	37 LVCDBM
37 LVREQBP	C18	B_SREQ+	B_SREQ-	B18	37 LVREQBM
37 LVIOBP	D18	B_SI_O+	B_SI_O-	E17	37 LVIOBM

MISC

A_DIFFSENS	E2	A_DIFFSENS	TRACECLK	B3	SCSCI_TP3
B_DIFFSENS	B22	B_DIFFSENS	NC	A24	
			DIS_SCSI_FSN	AC4	
			TN	C5	SCSCI_TP4
SCSI_VCC3 — R571 X_0 — R568 10K-1%	R1	A_RBIAS			
	T1	A_VDDBIAS	A_LED#	J23	SCSILED1 38
SCSI_VCC3 — R618 X_0 — R622 10K-1%	A11	B_RBIAS	B_LED#	K23	SCSILED2 38
	B13	B_VDDBIAS			
SCSCI_TP1 — TP_SS25	Y4	IDDTN	PIPESTAT0	D5	SCSCI_TP5
	AA22	CLKMODE_0	PIPESTAT1	E6	SCSCI_TP6
SCSCI_TP2 — R601 220 — R615 220	AC2	CLKMODE_1	PIPESTAT2	C3	SCSCI_TP7
	D7	TESTCLKEN	TRACESYNC	E5	SCSCI_TP8
	E7	SCANMODE			
			TRACEPKT0	D4	SCSCI_TP9
SCSI_VCC3 — R581 X_4.7K	AD5	TST_RST#	TRACEPKT1	B2	SCSCI_TP10
			TRACEPKT2	F5	SCSCI_TP11
TP_SS1	AA5	RTCK_ICE	TRACEPKT3	F4	SCSCI_TP12
	AA4	TCK_ICE	TRACEPKT4	C2	SCSCI_TP13
TP_SS24	AB3	TDI_ICE	TRACEPKT5	E3	SCSCI_TP14
TP_SCSI_1	AD2	TDO_ICE	TRACEPKT6	G5	SCSCI_TP15
TP_SCSI_2	Y5	TMS_ICE	TRACEPKT7	F4	SCSCI_TP16
TP_SCSI_3	AB4	TRST_ICE			
TP_SCSI_4	N22	SCANEN	PVT1	AE5	R587 49.9-1%
TP_SS23	AE2	TESTHCLK			
TP_SS22	AB6	TESTACLK	PVT2	AF4	
TP_SS21			NC	N23	ZCR_EN_N 33
SCSI_VDDC — FB21 FB23 — FB120S FB120S	C1	VDDA0	VSSA0	H5	FB20 FB24 FB120S
	AB21	VDDA1	VSSA1	AD24	

53C1030

C838 X_0.1u — C752 X_0.1u

LVSCDAM[15..0] — LVSCDAM[15..0] 36
LVSCDAP[15..0] — LVSCDAP[15..0] 36

LVSCDBM[15..0] — LVSCDBM[15..0] 37
LVSCDBP[15..0] — LVSCDBP[15..0] 37

SCSI_VCC3: C870 0.1u, C840 0.1u
SCSI_VDDC: C823 0.1u, C869 0.1u

SCSI_VCC3: C872 0.1u, C853 0.1u, C762 0.1u, C790 0.1u
SCSI_VDDC: C791 0.1u, C746 0.1u, C827 0.1u, C754 0.1u
Place at soldering side

+5V — SCSI_VDDC: C789 0.1u, C749 0.1u, C751 0.1u, C887 0.1u, EC10 10u-1206 — SCSI_VCC3: EC16 10u-1206

SCSI_VCC3: C747 0.1u, C821 0.1u, EC11 10u-1206

SCSI_VCC3: C891 0.1u, C1098 0.1u, C1083 0.1u, C1097 0.1u

SCSI_VCC3: C1093 0.1u, C1095 0.1u, C1091 0.1u, C1078 0.1u

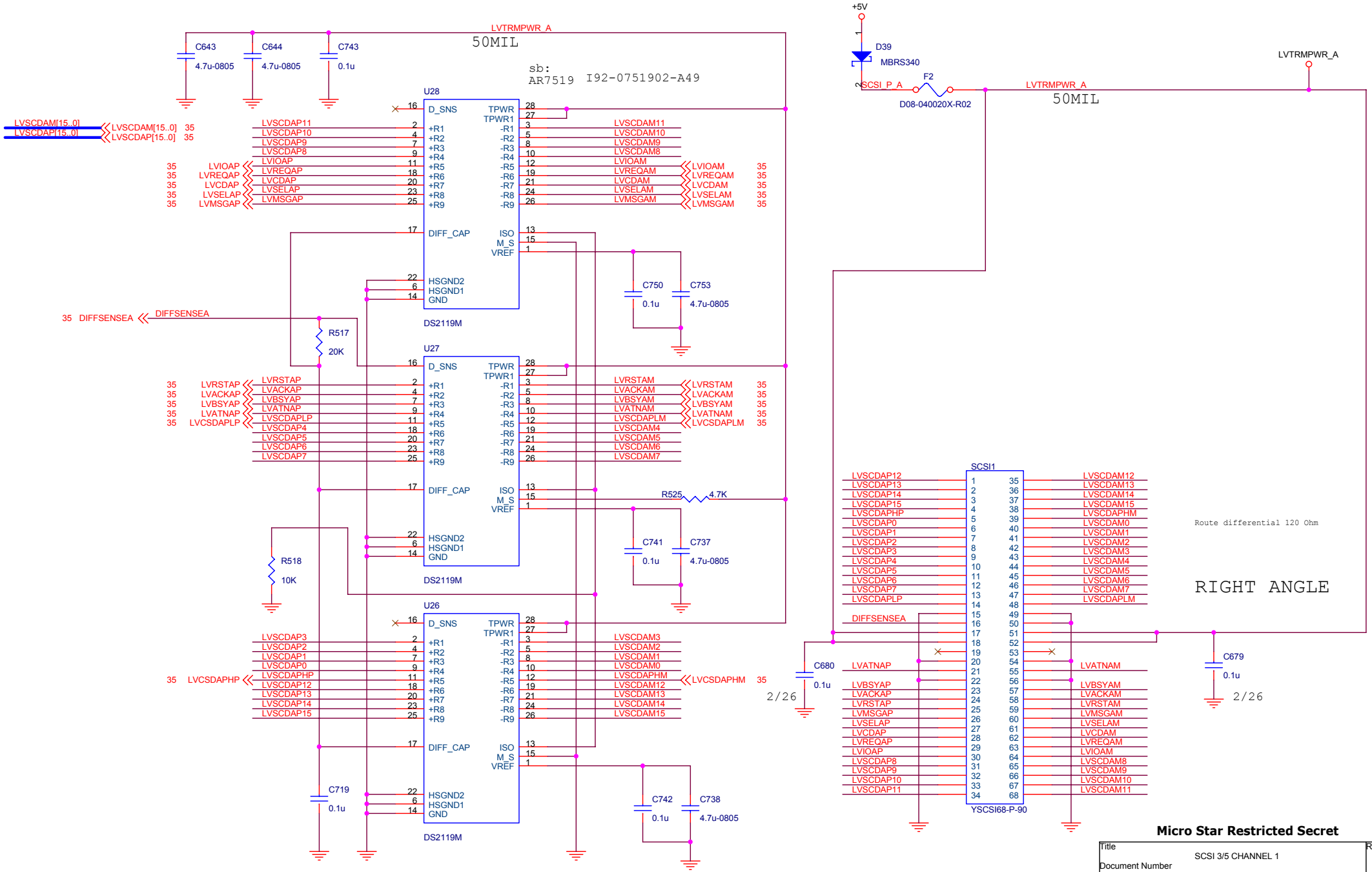
SCSI_VCC3: C1080 0.1u, C1084 0.1u, C1094 0.1u, C1088 0.1u

SCSI_VCC3 — R680 4.7K — SCSCI_TP3
SCSI_VCC3 — R548 X_220 — SCSCI_TP4
SCSI_VCC3 — R700 4.7K — ZCR_EN_N 33

SCSI_VDDC: C1081 0.1u, C1100 0.1u, C1087 0.1u, C1086 0.1u
SCSI_VDDC: C1096 0.1u, C1099 0.1u, C1092 0.1u, C1077 0.1u
SCSI_VDDC: C1089 0.1u, C1090 0.1u, C897 0.1u, C1075 0.1u

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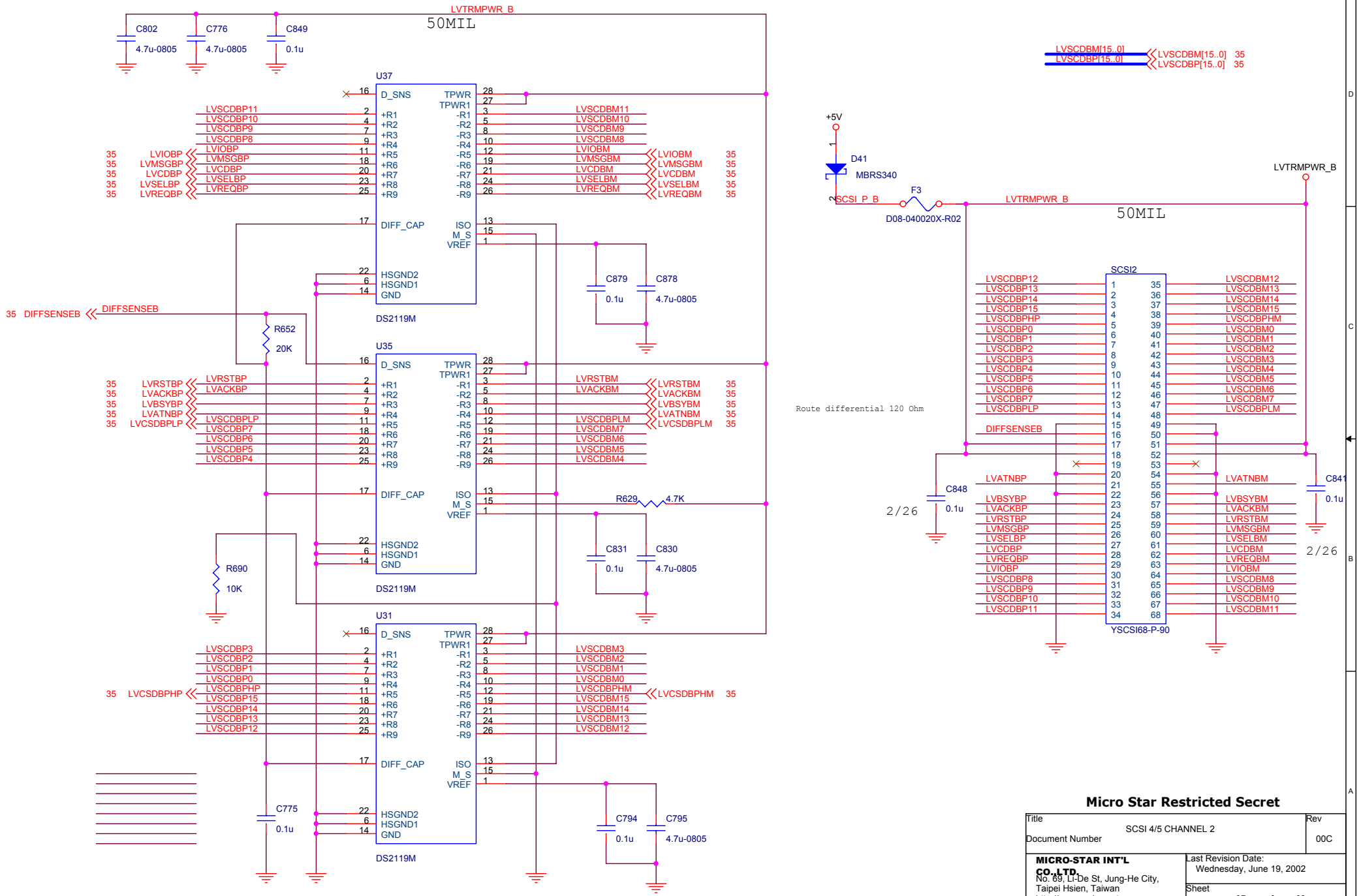
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Document Number		
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw	Last Revision Date: Wednesday, June 19, 2002	Sheet 35 of 63



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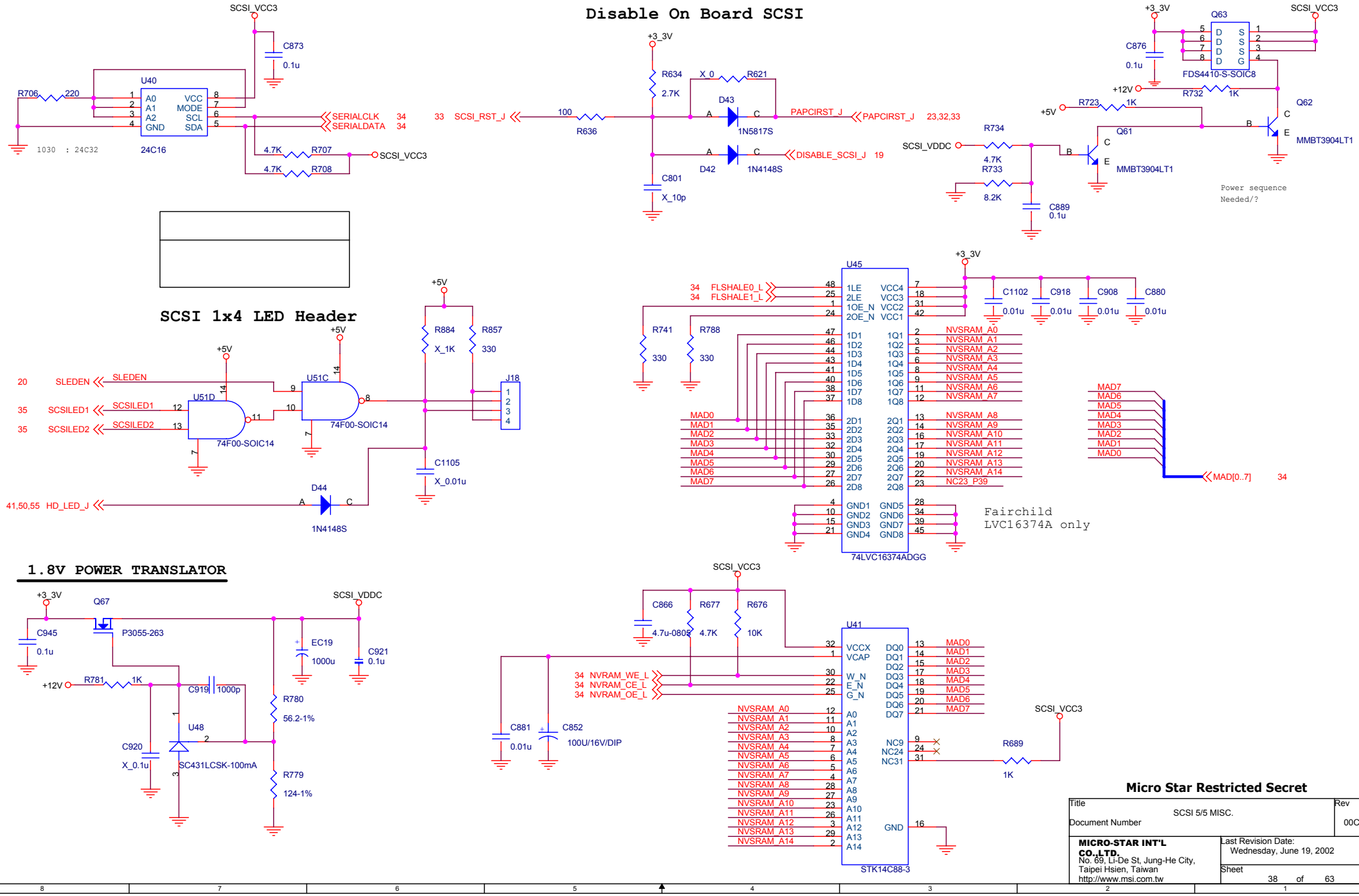
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Document Number	
Rev	00C
Last Revision Date:	Wednesday, June 19, 2002
Sheet	36 of 63

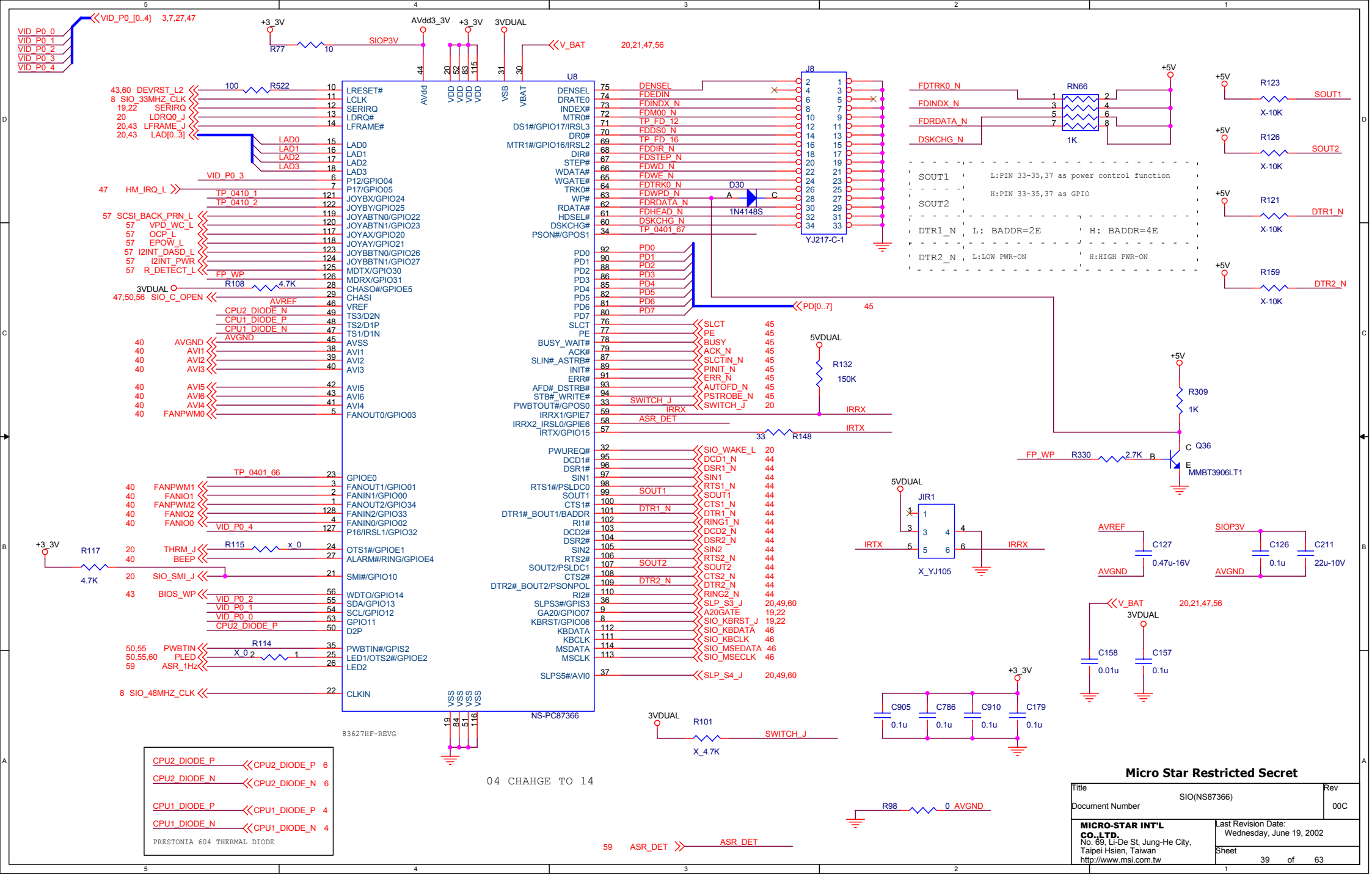
MICRO-STAR INT'L CO.,LTD.
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Taipei Hsien, Taiwan
http://www.msi.com.tw



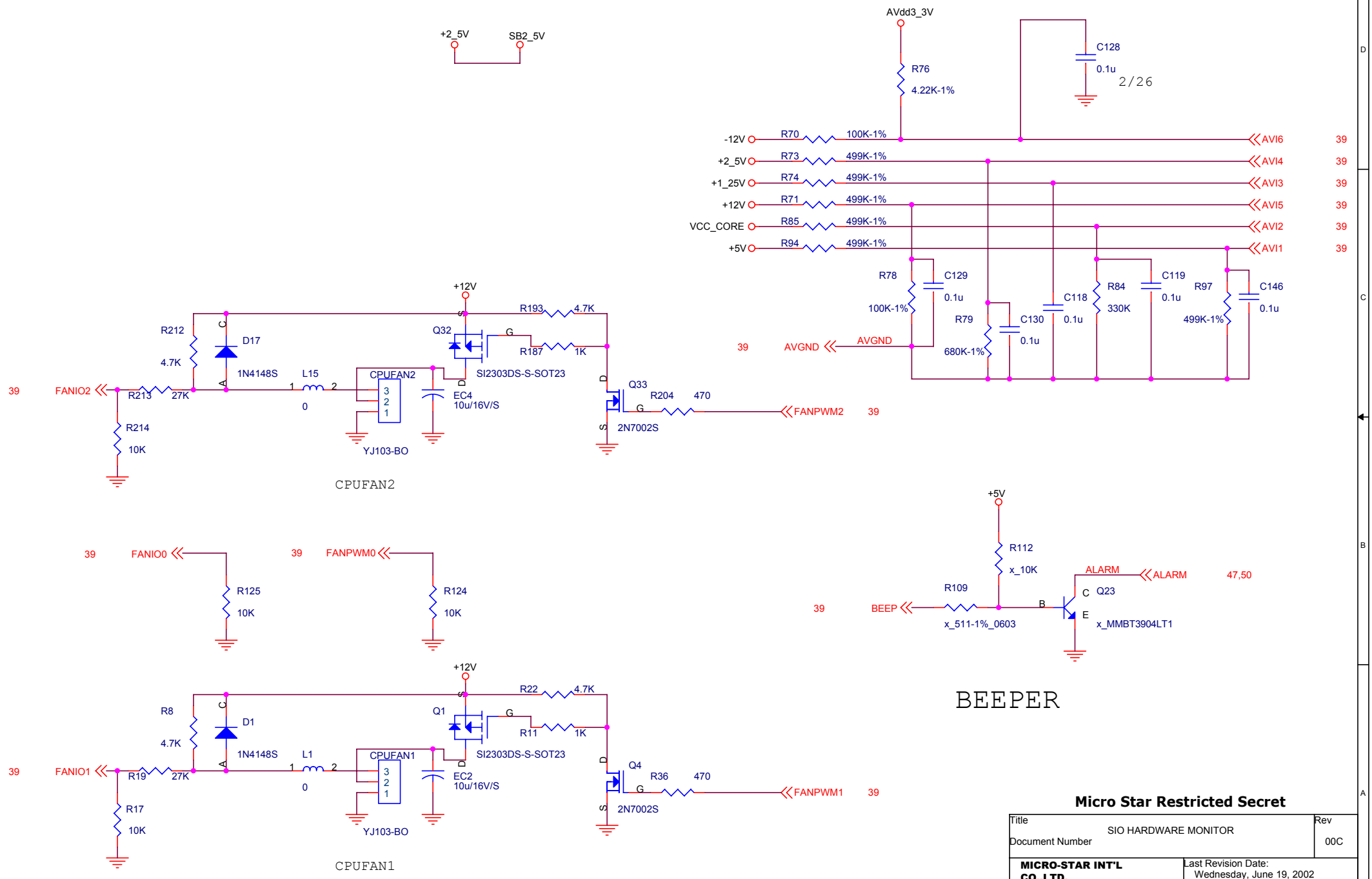
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Title	SCSI 4/5 CHANNEL 2
Document Number	Rev 00C
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Disable On Board SCSI
SCSI 1x4 LED Header
1.8V POWER TRANSLATOR
Fairchild
LVC16374A only
Power sequence
Needed/?
Micro Star Restricted Secret
Title
SCSI 5/5 MISC.
Document Number
00C
Rev
MICRO-STAR INT'L CO.,LTD.
No. 69, Li-De St, Jung-He City,
Taipei Hsien, Taiwan
http://www.msi.com.tw
Last Revision Date:
Wednesday, June 19, 2002
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U8
NS-PC87366
J8
YJ217-C-1
D30
1N4148S
RN66
1K
JIR1
X_YJ105
Q36
MMBT3906LT1
SOUT1 L:PIN 33-35,37 as power control function
H:PIN 33-35,37 as GPIO
SOUT2
DTR1_N L: BADDR=2E H: BADDR=4E
DTR2_N L:LOW PWR-ON H:HIGH PWR-ON
PRESTONIA 604 THERMAL DIODE
83627HF-REVG
04 CHAHGE TO 14
Micro Star Restricted Secret
Title SIO(NS87366)
Document Number
Rev 00C
MICRO-STAR INT'L CO.,LTD.
No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan
http://www.msi.com.tw
Last Revision Date: Wednesday, June 19, 2002
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LPC Hardware Monitor



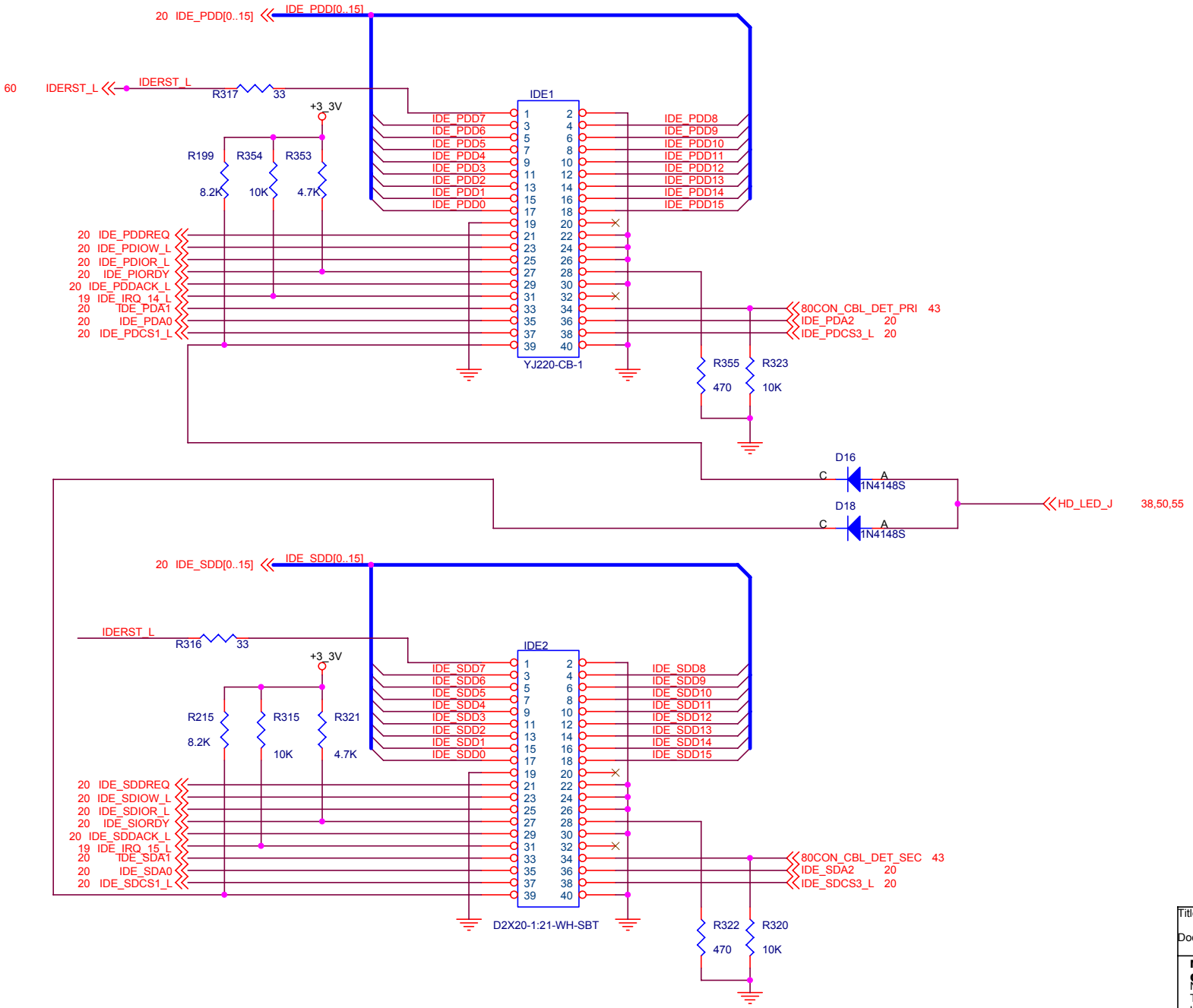
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Title	SIO HARDWARE MONITOR	Rev
Document Number		00C

MICRO-STAR INT'L CO.,LTD.
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Taipei Hsien, Taiwan
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Last Revision Date: Wednesday, June 19, 2002

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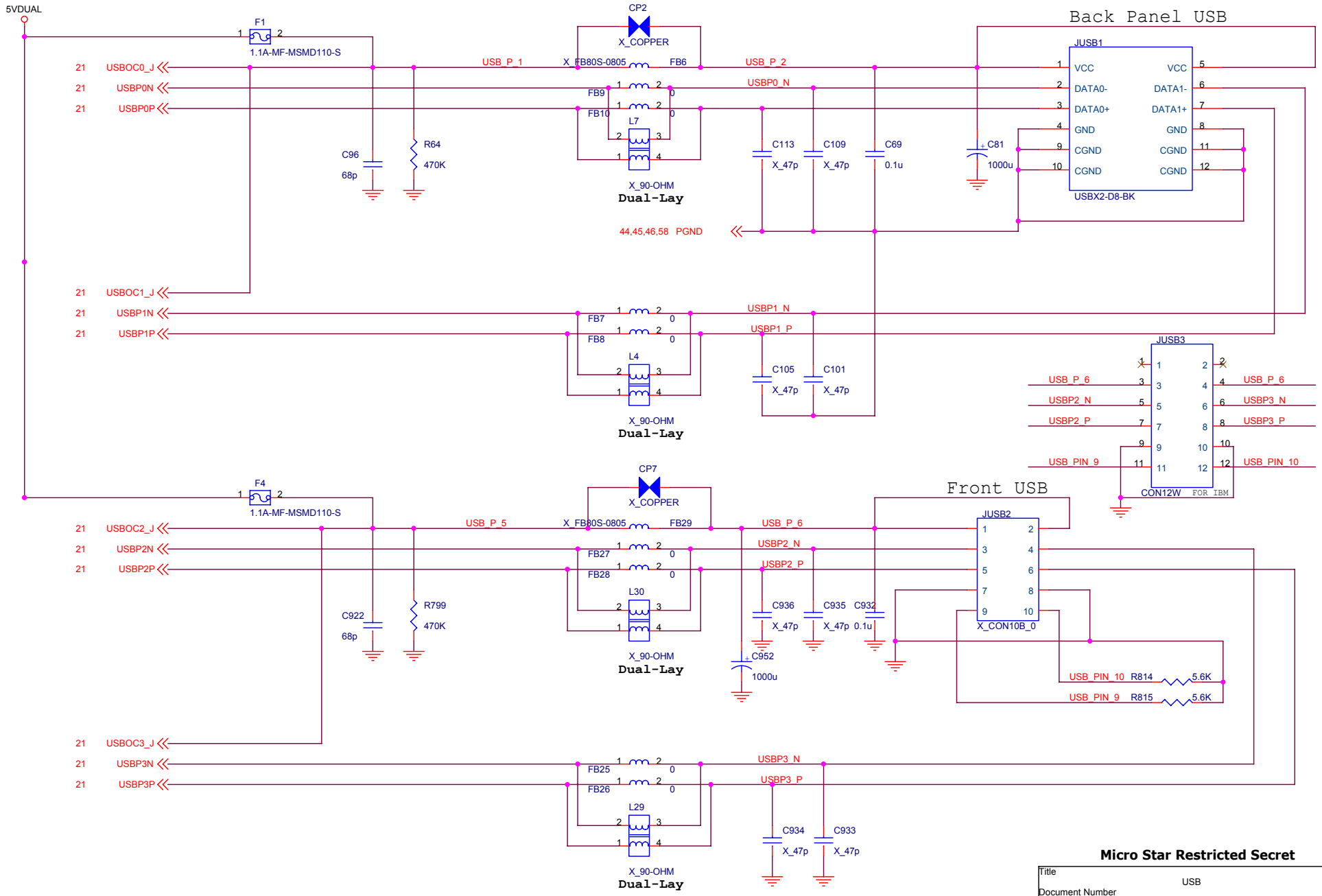


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Title	IDE	Rev 00C
Document Number		

MICRO-STAR INT'L CO.,LTD.
No. 69, Li-De St, Jung-He City,
Taipei Hsien, Taiwan
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Last Revision Date: Wednesday, June 19, 2002



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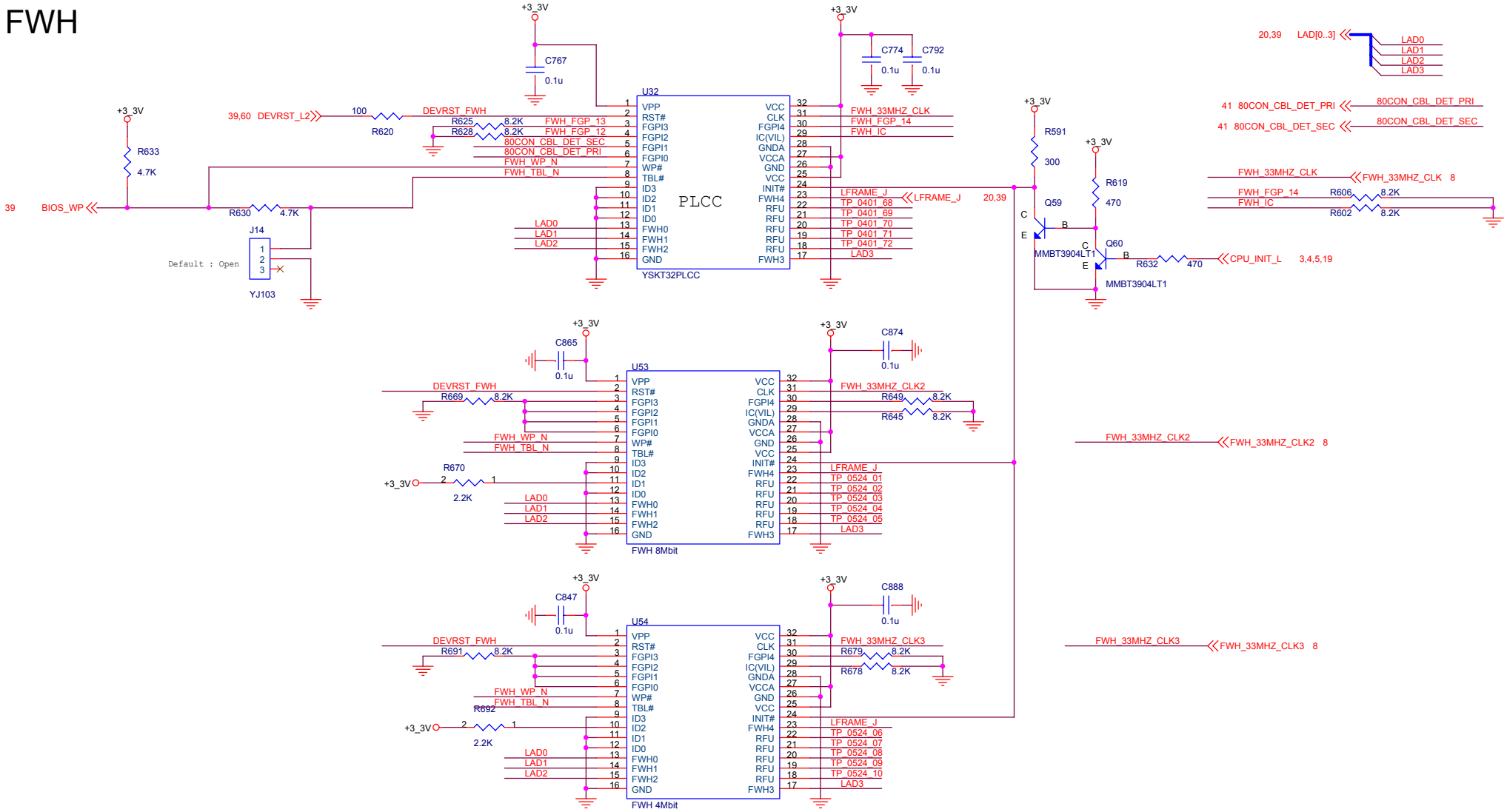
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Document Number	

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Taipei Hsien, Taiwan
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Last Revision Date: Wednesday, June 19, 2002

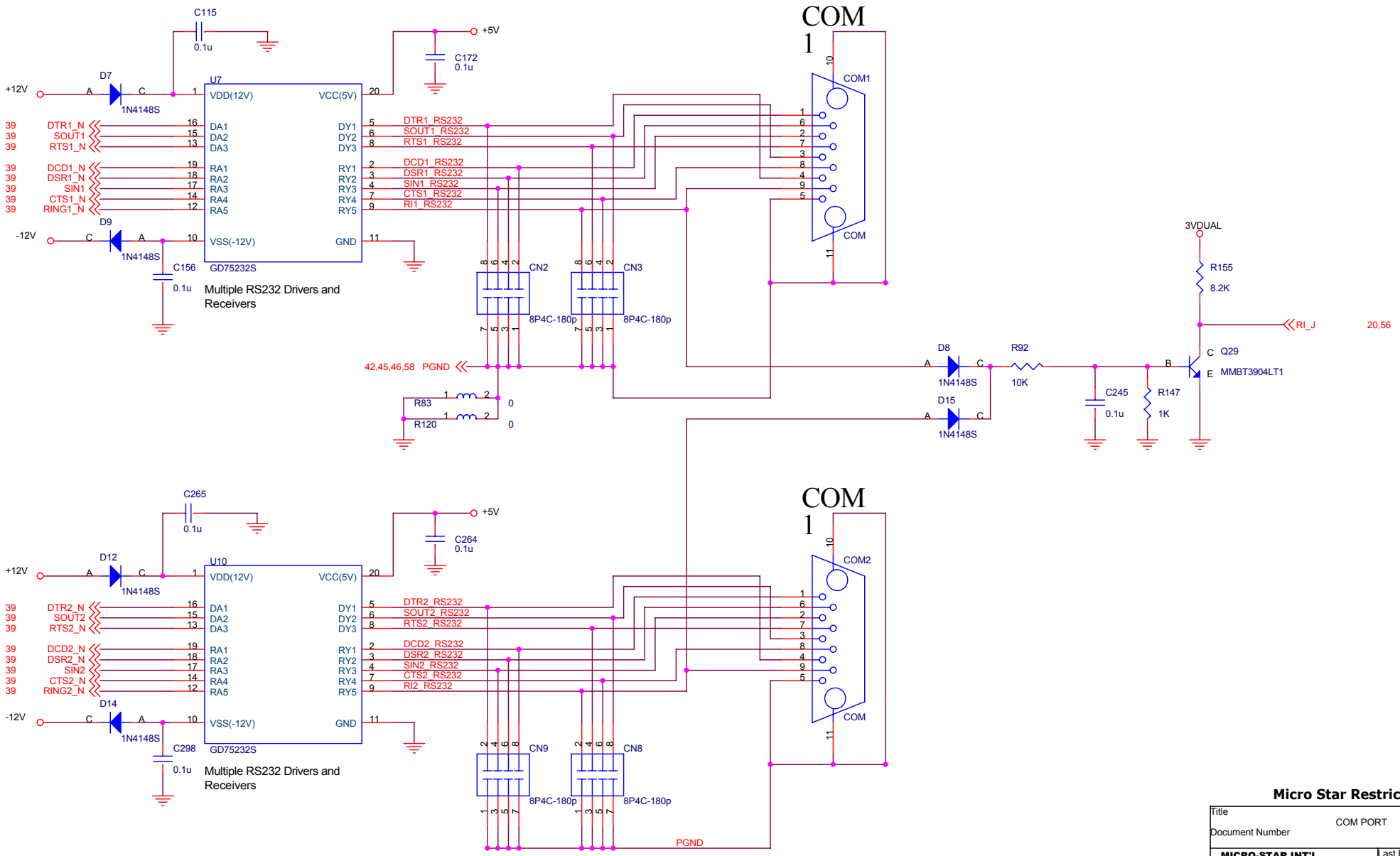
Sheet 42 of 63

FWH



Micro Star Restricted Secret

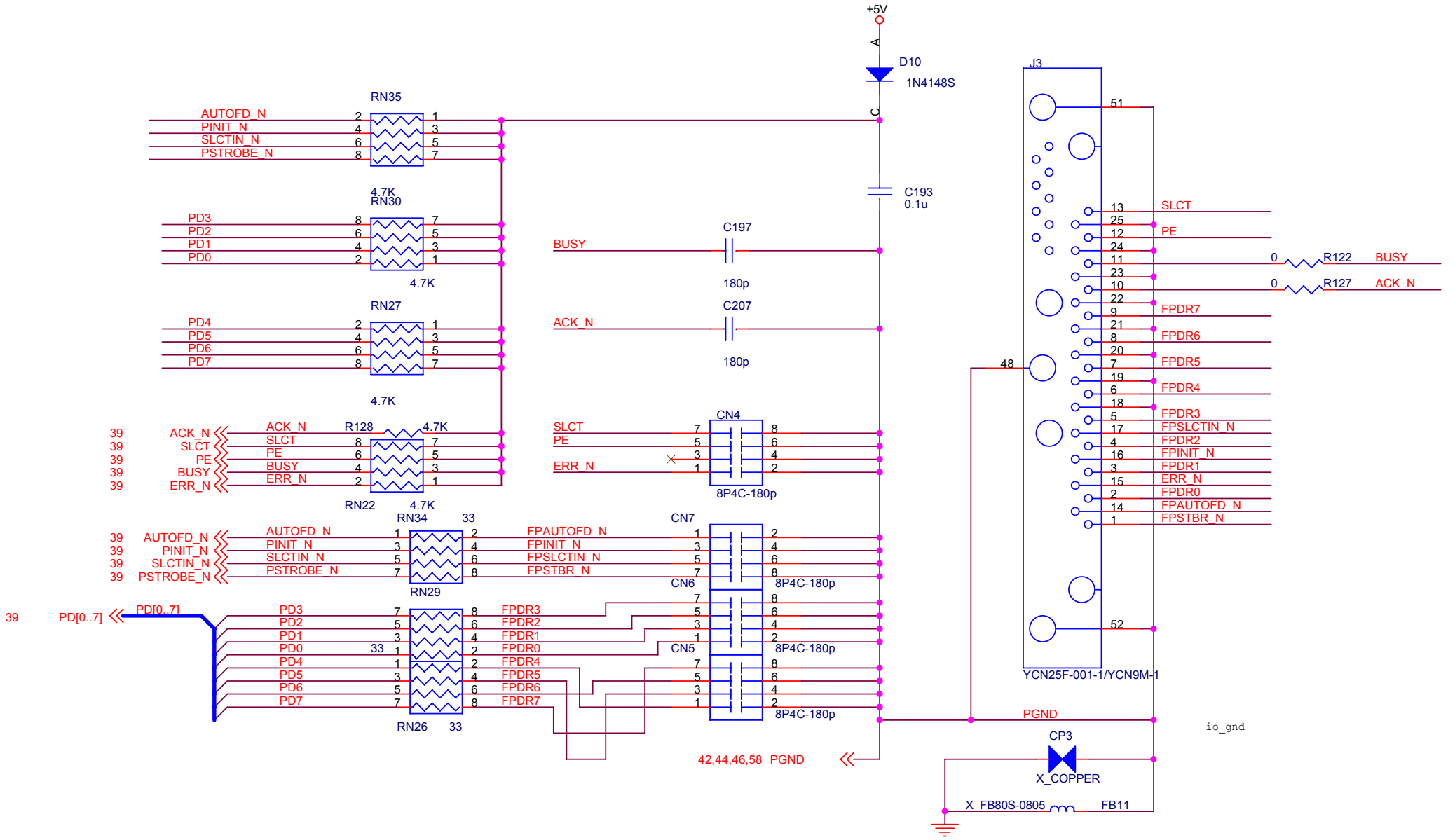
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Document Number	
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw	Last Revision Date: Wednesday, June 19, 2002
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Title	COM PORT
Document Number	
Rev	00C
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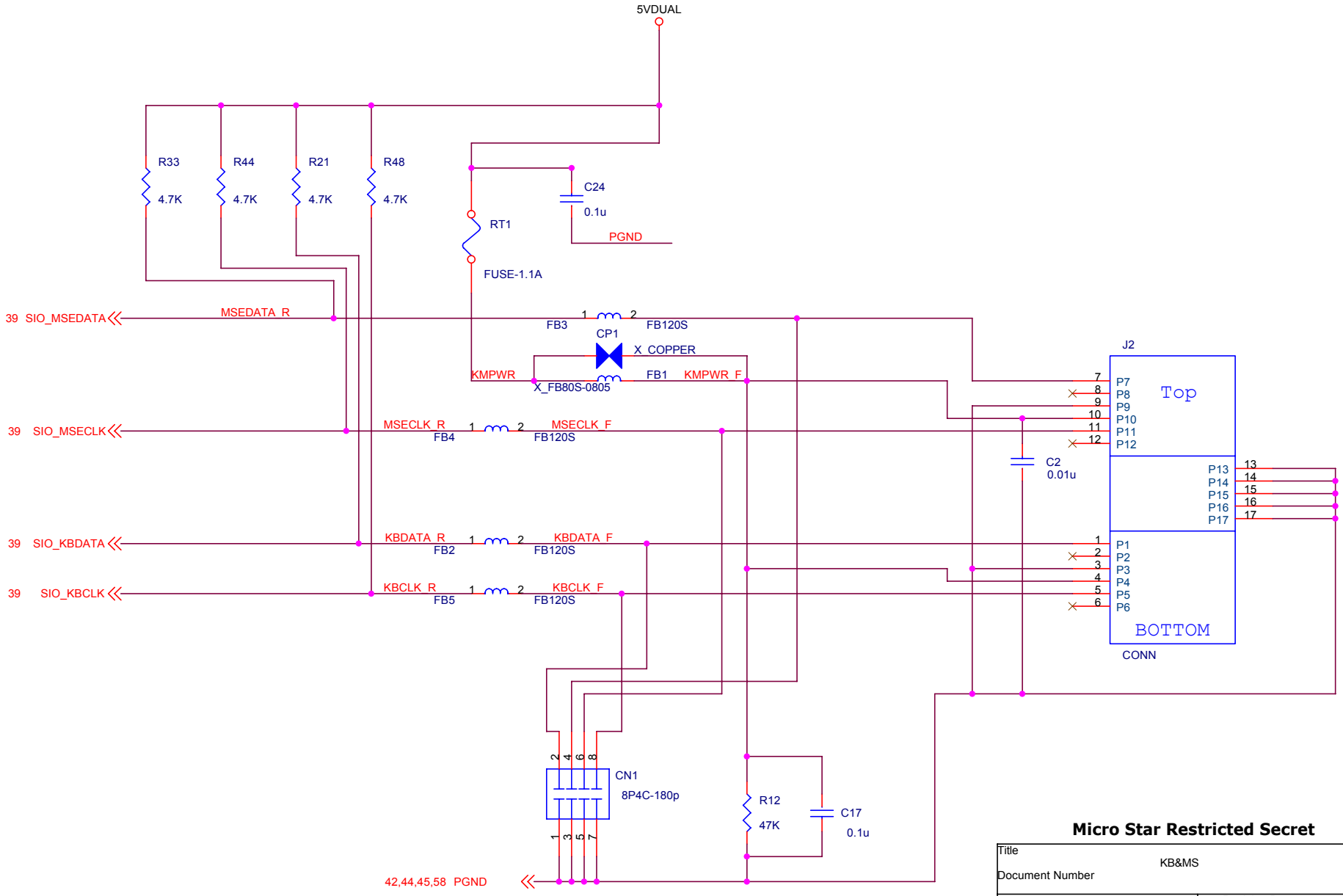
Micro Star Restricted Secret

Title	PRINTER PORT	Rev 00C
Document Number		

MICRO-STAR INT'L CO.,LTD.
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Taipei Hsien, Taiwan
http://www.msi.com.tw

Last Revision Date: Wednesday, June 19, 2002

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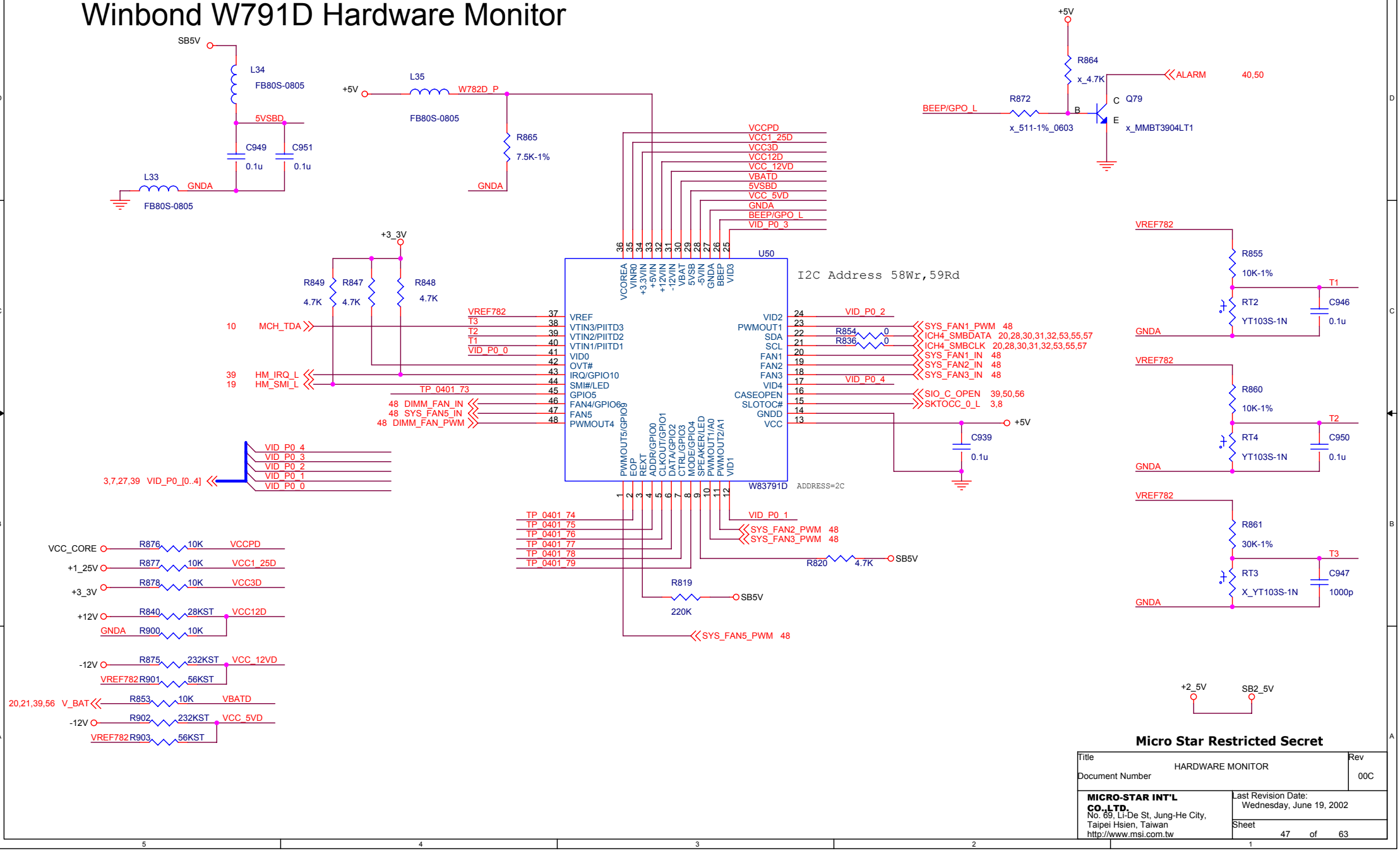
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Document Number		

MICRO-STAR INT'L CO.,LTD.
No. 69, Li-De St, Jung-He City,
Taipei Hsien, Taiwan
http://www.msi.com.tw

Last Revision Date: Wednesday, June 19, 2002

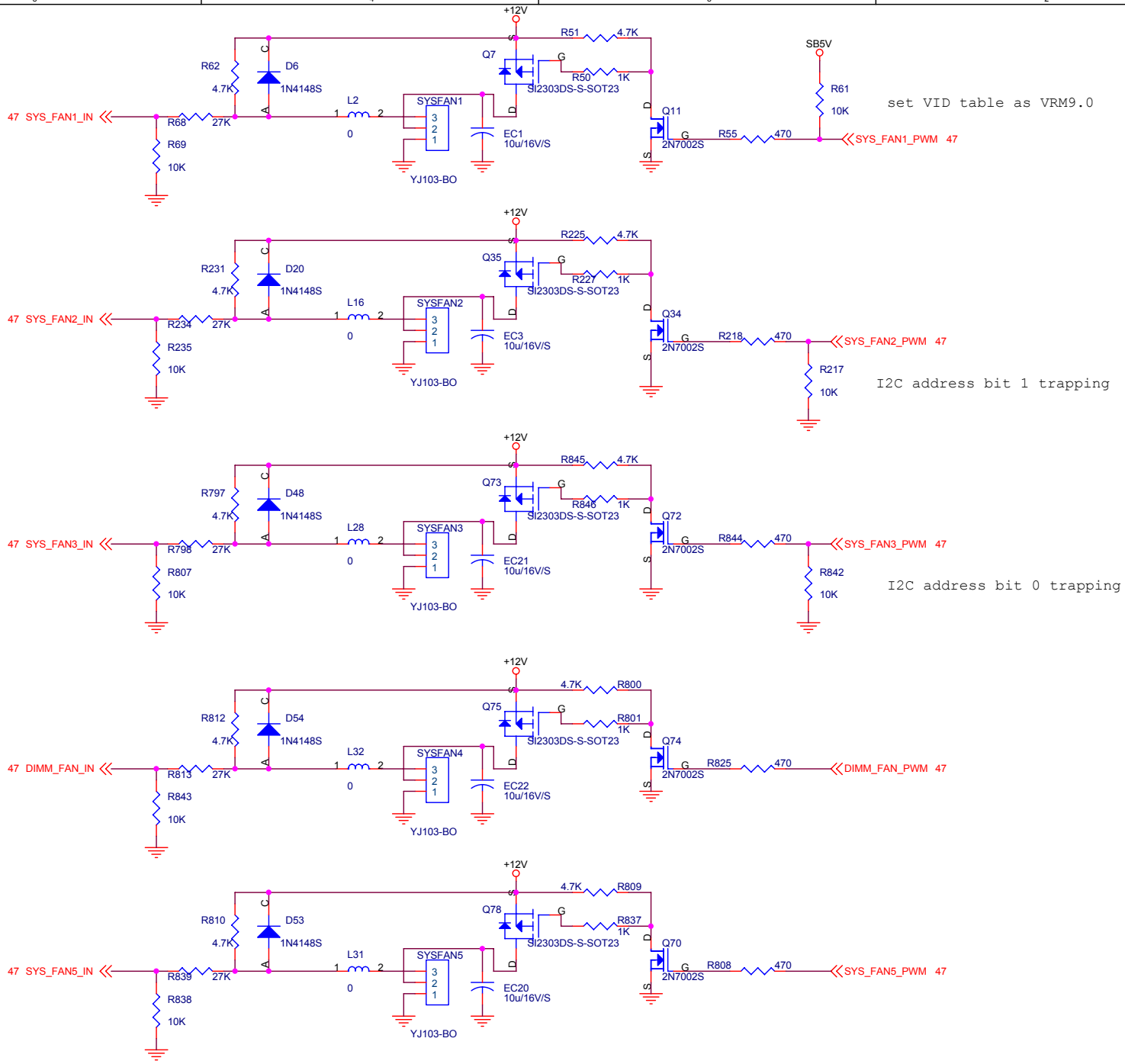
Sheet 46 of 63

Winbond W791D Hardware Monitor



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Title	HARDWARE MONITOR
Document Number	
Rev	00C
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw	Last Revision Date: Wednesday, June 19, 2002
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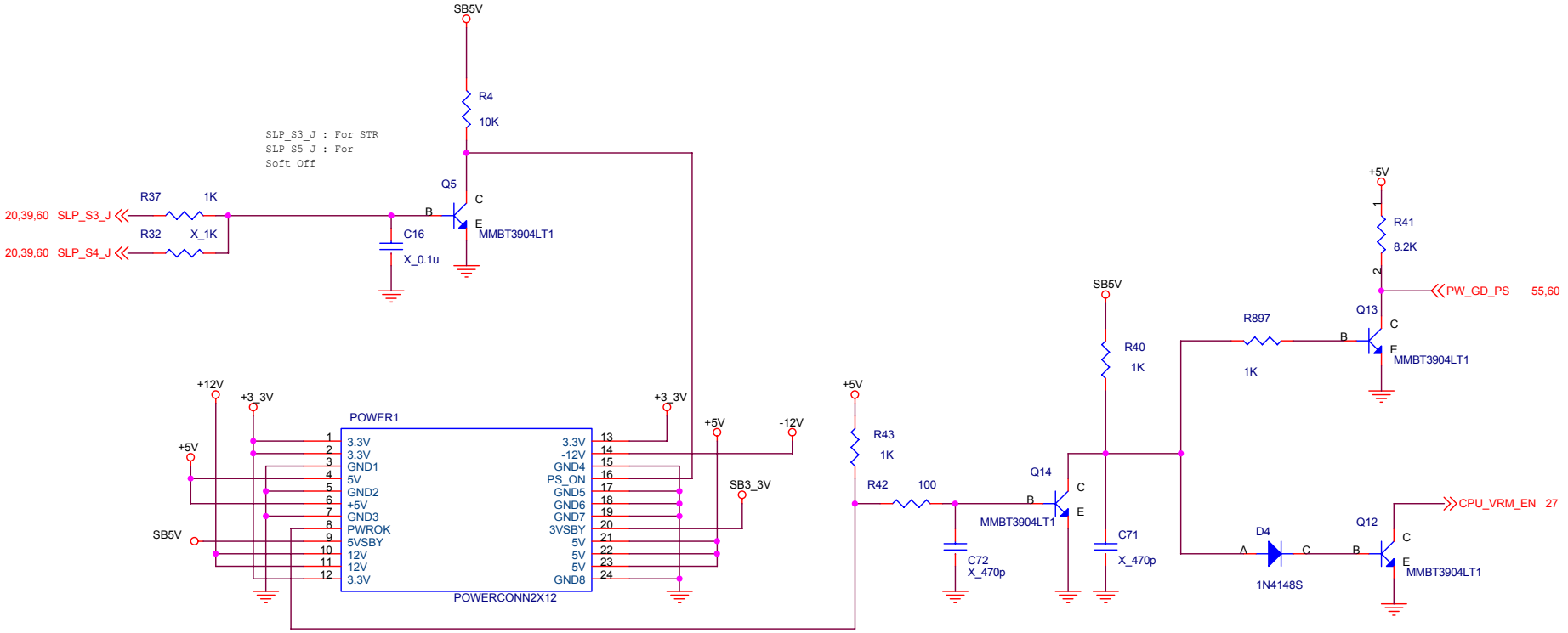
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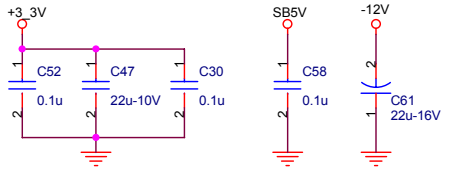
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Document Number:	

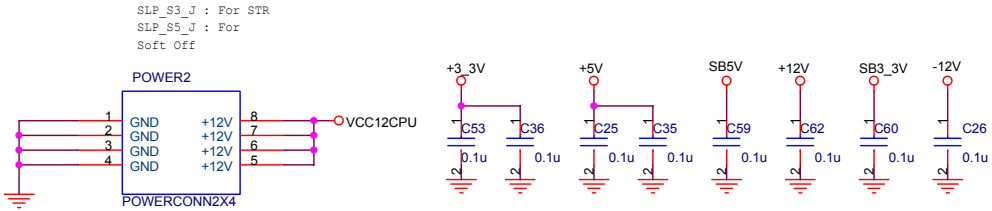
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Taipei Hsien, Taiwan
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Last Revision Date: Wednesday, June 19, 2002

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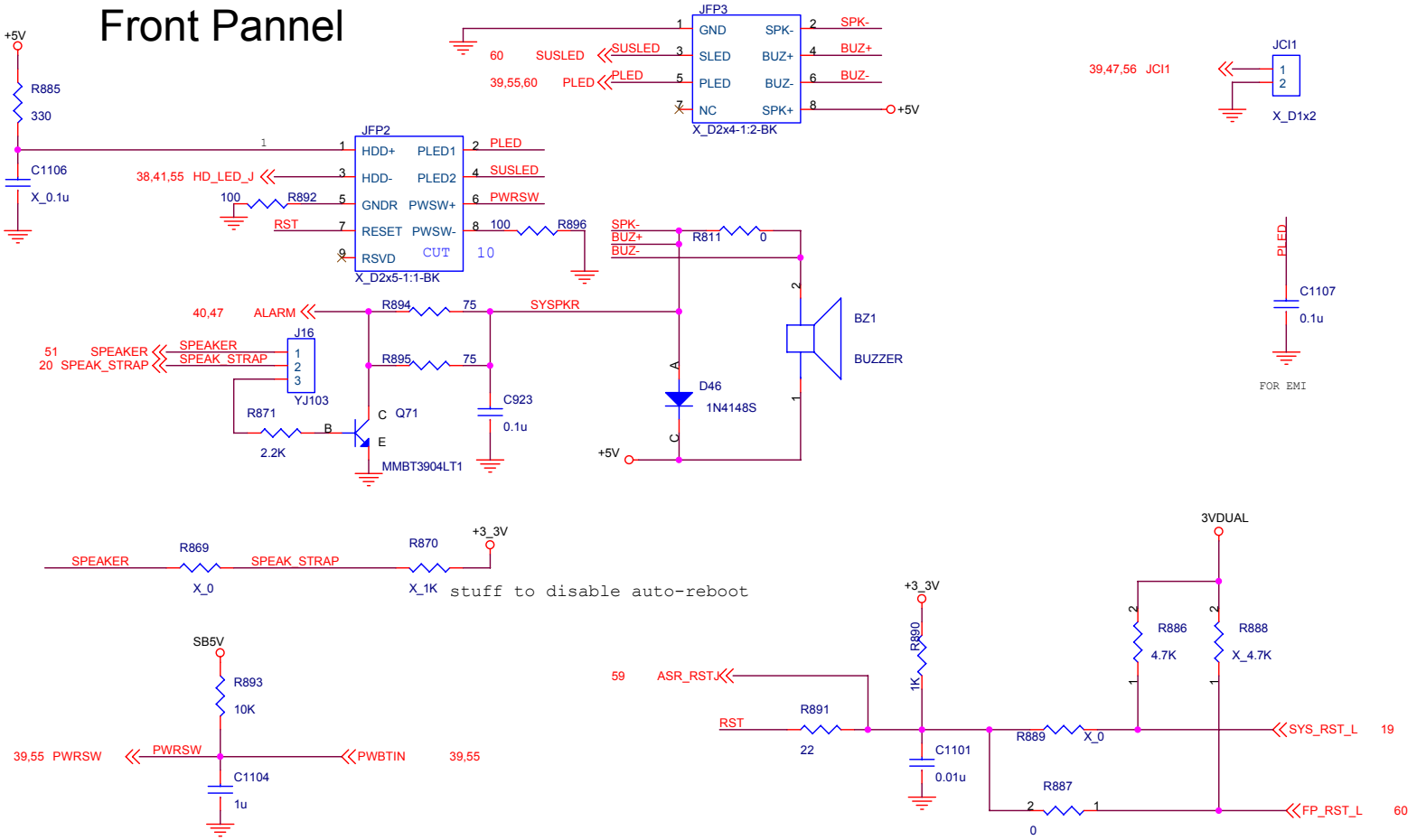
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Document Number		

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Taipei Hsien, Taiwan
http://www.msi.com.tw

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Front Pannel



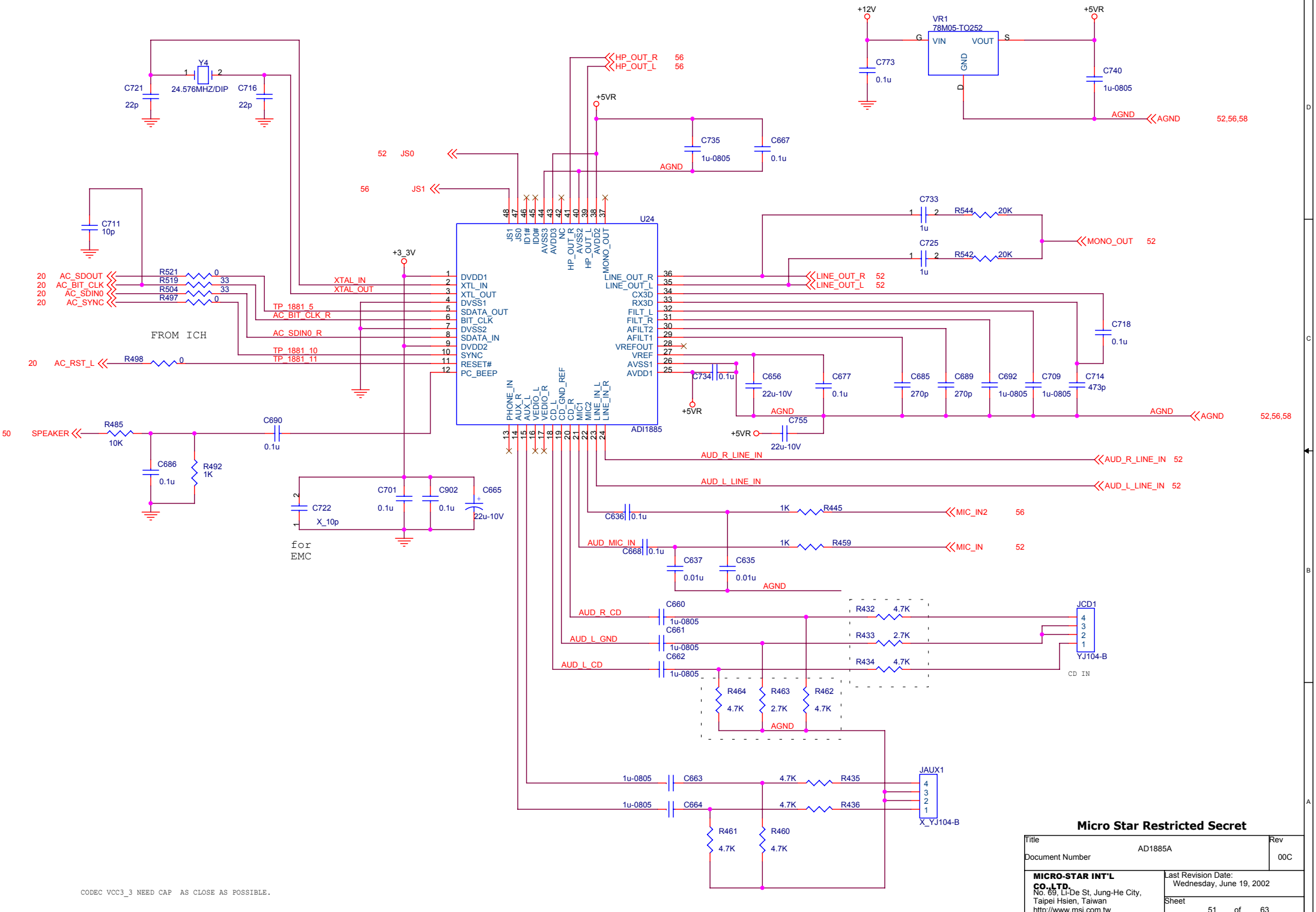
Micro Star Restricted Secret

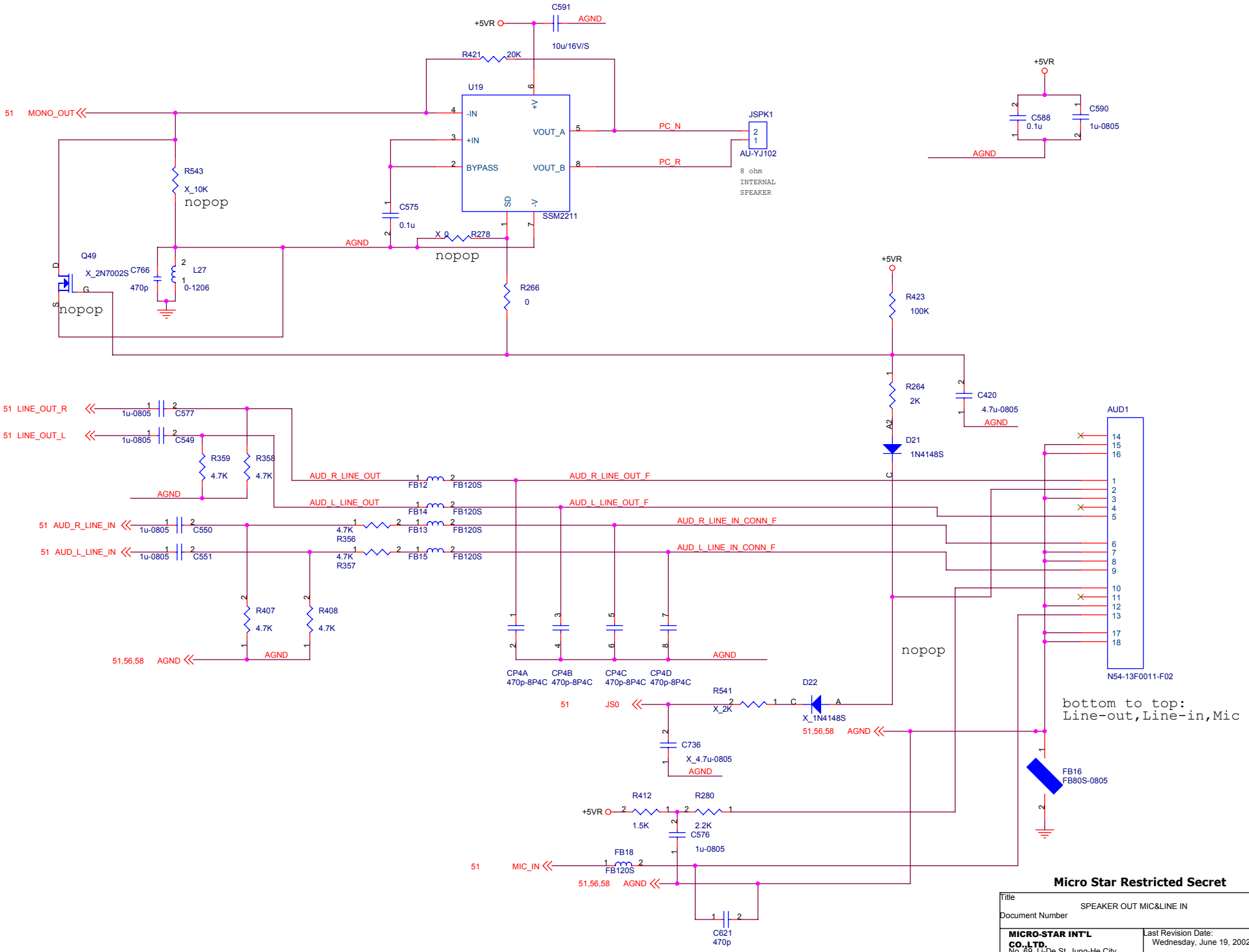
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Document Number	

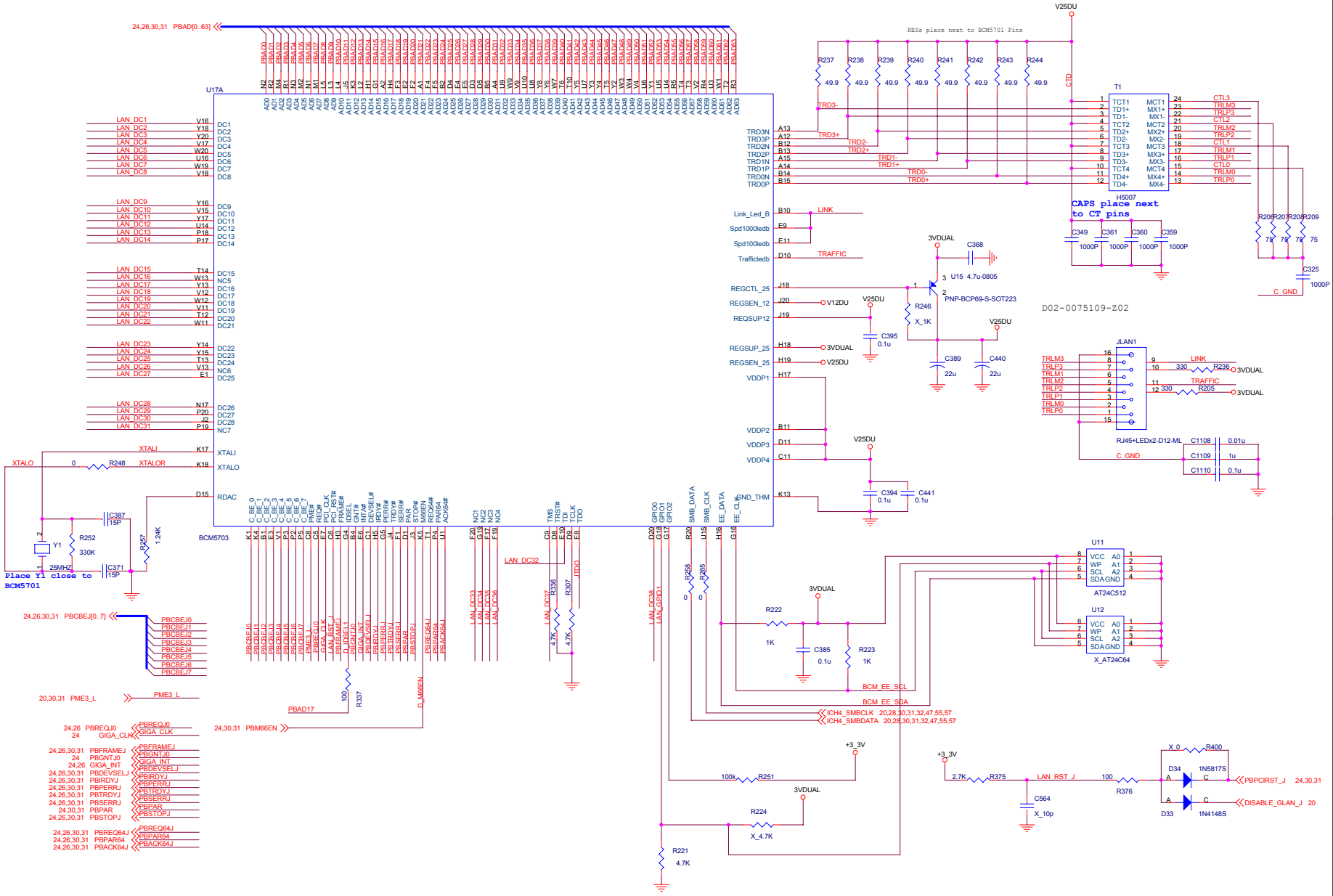
MICRO-STAR INT'L CO.,LTD.
No. 69, Li-De St, Jung-He City,
Taipei Hsien, Taiwan
http://www.msi.com.tw

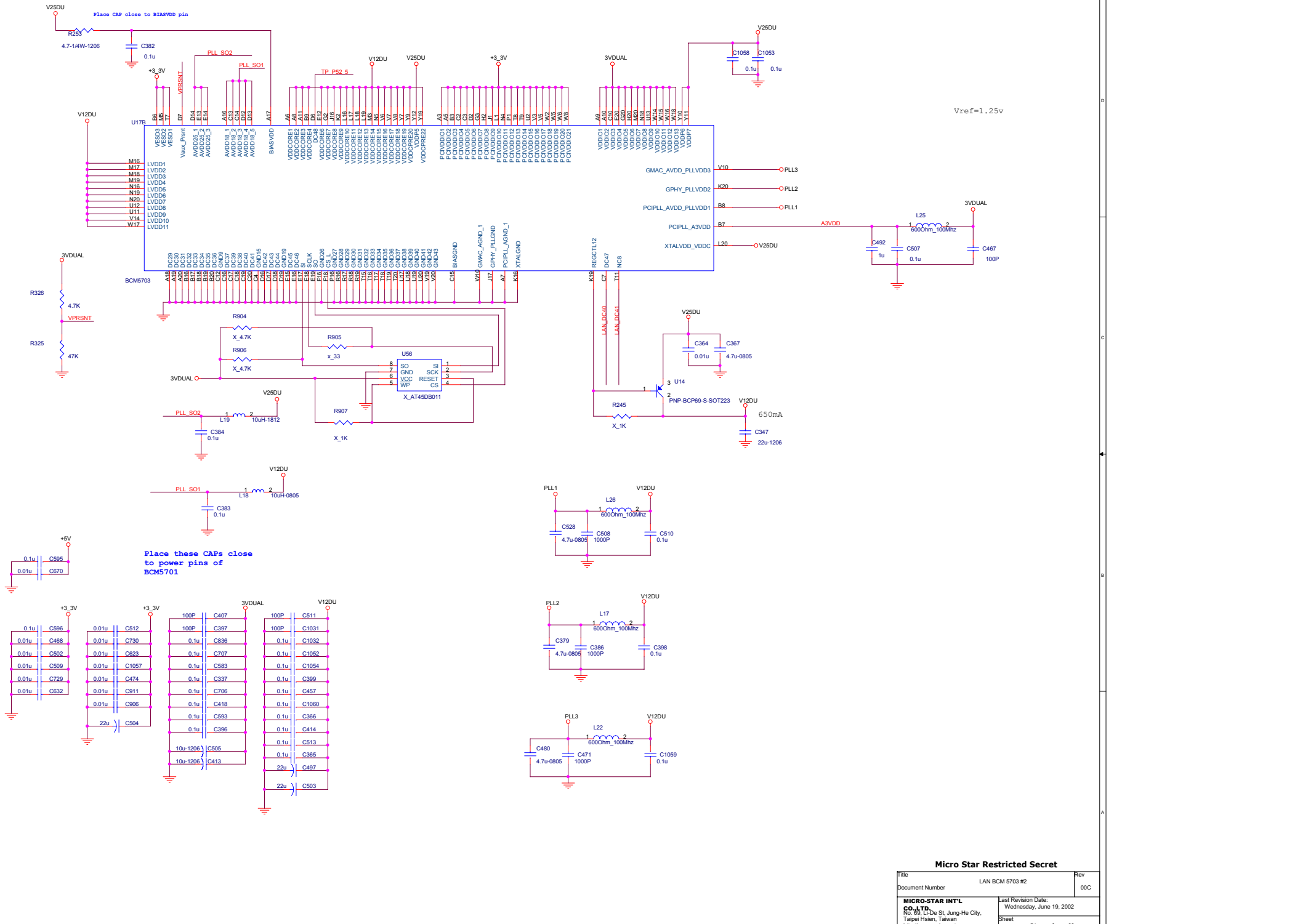
Last Revision Date: Wednesday, June 19, 2002

Sheet 50 of 63

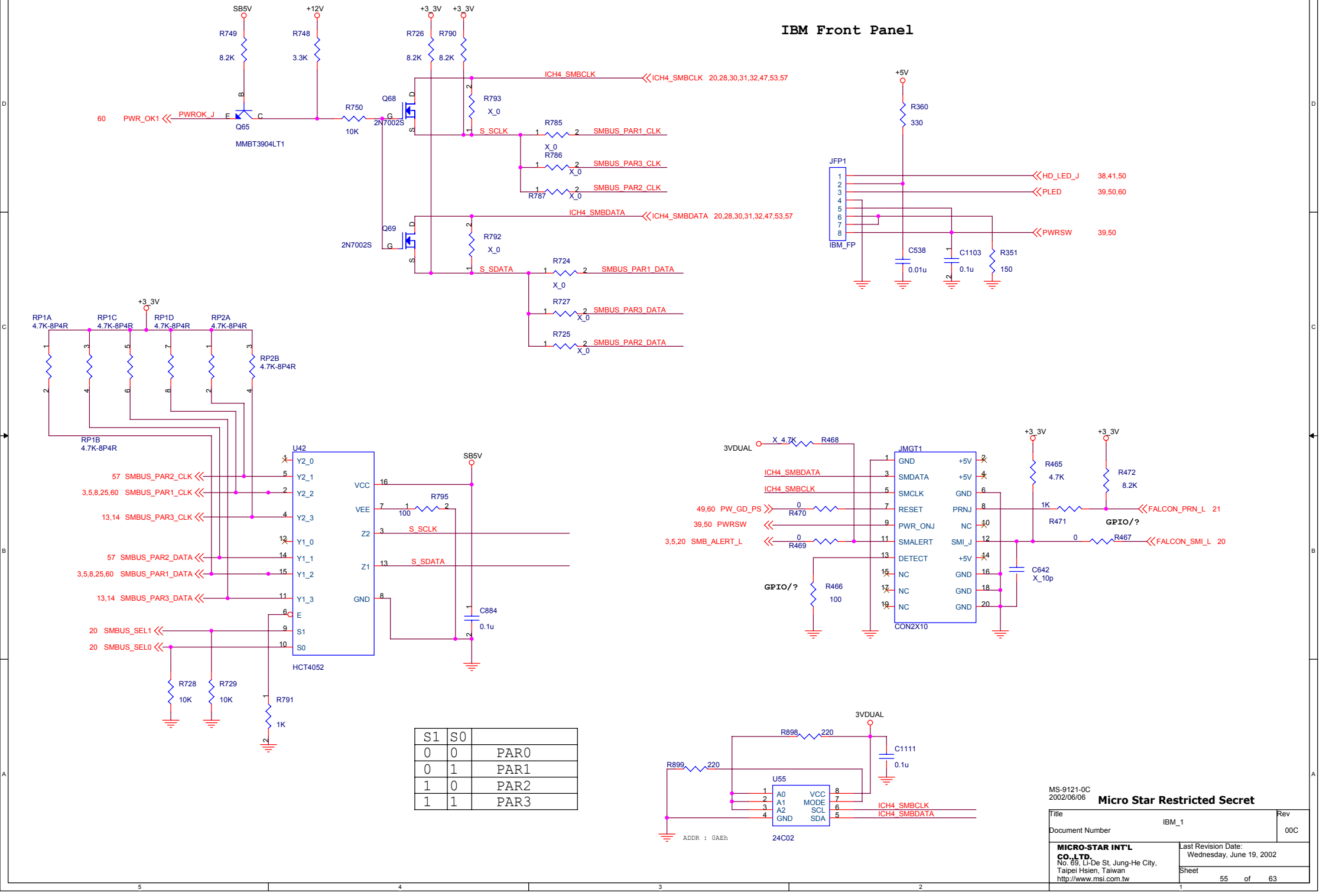

+12V
VR1
78M05-TO252
VIN
VOUT
GND
+5VR
C773
0.1u
C740
1u-0805
AGND
AGND 52,56,58
HP_OUT_R 56
HP_OUT_L 56
+5VR
C735
1u-0805
C667
0.1u
AGND
52 JS0
56 JS1
U24
JS1
JS0
ID1#
ID0#
AVSS3
AVDD3
NC
HP_OUT_R
AVSS2
HP_OUT_L
AVDD2
MONO_OUT
C733
1u
R544
20K
MONO_OUT 52
C725
1u
R542
20K
LINE_OUT_R
LINE_OUT_L
LINE_OUT_R 52
LINE_OUT_L 52
CX3D
RX3D
FILT_L
FILT_R
AFILT2
AFILT1
VREFOUT
VREF
AVSS1
AVDD1
C718
0.1u
C734 0.1u
C656
22u-10V
C677
0.1u
C685
270p
C689
270p
C692
1u-0805
C709
1u-0805
C714
473p
+5VR
AGND
AGND
AGND 52,56,58
C755
22u-10V
+5VR
Y4
24.576MHZ/DIP
C721
22p
C716
22p
C711
10p
+3_3V
20 AC_SDOUT
20 AC_BIT_CLK
20 AC_SDIN0
20 AC_SYNC
R521 0
R519 33
R504 33
R497 0
FROM ICH
XTAL_IN
XTAL_OUT
TP_1881_5
AC_BIT_CLK_R
AC_SDIN0_R
TP_1881_10
TP_1881_11
20 AC_RST_L
R498 0
DVDD1
XTL_IN
XTL_OUT
DVSS1
SDATA_OUT
BIT_CLK
DVSS2
SDATA_IN
DVDD2
SYNC
RESET#
PC_BEEP
ADI1885
PHONE_IN
AUX_R
AUX_L
VEDIO_L
VEDIO_R
CD_L
CD_GND_REF
CD_R
MIC1
MIC2
LINE_IN_L
LINE_IN_R
50 SPEAKER
R485
10K
C690
0.1u
C686
0.1u
R492
1K
C722
X_10p
C701
0.1u
C902
0.1u
C665
22u-10V
for
EMC
AUD_R_LINE_IN
AUD_L_LINE_IN
AUD_R_LINE_IN 52
AUD_L_LINE_IN 52
C636 0.1u
1K
R445
MIC_IN2 56
AUD_MIC_IN
C668 0.1u
1K
R459
MIC_IN 52
C637
0.01u
C635
0.01u
AGND
AUD_R_CD
C660
1u-0805
AUD_L_GND
C661
1u-0805
AUD_L_CD
C662
1u-0805
R432 4.7K
R433 2.7K
R434 4.7K
JCD1
4
3
2
1
YJ104-B
CD IN
R464
4.7K
R463
2.7K
R462
4.7K
AGND
1u-0805
C663
4.7K
R435
1u-0805
C664
4.7K
R436
JAUX1
4
3
2
1
X_YJ104-B
R461
4.7K
R460
4.7K
CODEC VCC3_3 NEED CAP AS CLOSE AS POSSIBLE.
Micro Star Restricted Secret
Title
AD1885A
Document Number
Rev
00C
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C591
+5VR
AGND
10u/16V/S
R421
20K
U19
+V
-IN
+IN
BYPASS
VOUT_A
VOUT_B
SD
-V
SSM2211
51 MONO_OUT
PC_N
PC_R
JSPK1
AU-YJ102
8 ohm
INTERNAL
SPEAKER
+5VR
C588
0.1u
C590
1u-0805
AGND
R543
X_10K
nopop
C575
0.1u
X_0
R278
nopop
AGND
Q49
X_2N7002S
C766
470p
L27
0-1206
nopop
R266
0
+5VR
R423
100K
R264
2K
C420
4.7u-0805
AGND
A2
D21
1N4148S
C
AUD1
N54-13F0011-F02
51 LINE_OUT_R
51 LINE_OUT_L
1u-0805
C577
C549
R359
4.7K
R358
4.7K
AGND
AUD_R_LINE_OUT
AUD_L_LINE_OUT
FB12
FB14
FB120S
AUD_R_LINE_OUT_F
AUD_L_LINE_OUT_F
51 AUD_R_LINE_IN
51 AUD_L_LINE_IN
C550
C551
R356
4.7K
R357
4.7K
FB13
FB15
AUD_R_LINE_IN_CONN_F
AUD_L_LINE_IN_CONN_F
R407
4.7K
R408
4.7K
51,56,58 AGND
CP4A 470p-8P4C
CP4B 470p-8P4C
CP4C 470p-8P4C
CP4D 470p-8P4C
nopop
51 JS0
R541
X_2K
D22
X_1N4148S
51,56,58 AGND
C736
X_4.7u-0805
AGND
FB16
FB80S-0805
R412
1.5K
R280
2.2K
C576
1u-0805
+5VR
FB18
FB120S
51 MIC_IN
51,56,58 AGND
C621
470p
bottom to top:
Line-out,Line-in,Mic
Micro Star Restricted Secret
Title
SPEAKER OUT MIC&LINE IN
Document Number
Rev
00C
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U17A
BCM5703
Place Y1 close to BCM5701
RESs place next to BCM5701 Pins
CAPS place next to CT pins
D02-0075109-Z02
JLAN1
RJ45+LEDx2-D12-ML
T1
H5007
U11
U12
AT24C512
X_AT24C64
PNP-BCP69-S-SOT223
U15 4.7u-0805
IDDIO:32mA,3.3V
IDDC:500mA,1.2V
AIDDL:175mA,1.2V
AIDD:190mA,2.5V
BIASIDD:20mA,2.5V
IDDIO-PCO:250mA,3.3V
PLLD01,02,03:10mA,1.2V
XTALIDD:8.7mA
3.3V+/-0.15
2..5V+/-0.12
1.2V+/-0.06
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Title
LAN BCM 5703 #1
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Place CAP close to BIASVDD pin
Vref=1.25v
Place these CAPs close to power pins of BCM5701
Micro Star Restricted Secret
Title
LAN BCM 5703 #2
Document Number
Rev
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IBM Front Panel



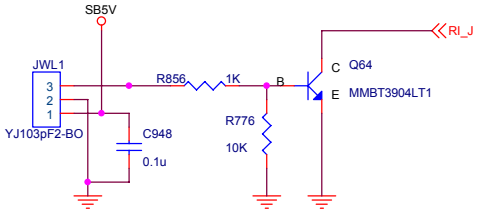
S1	S0	
0	0	PAR0
0	1	PAR1
1	0	PAR2
1	1	PAR3

MS-9121-0C
2002/06/06

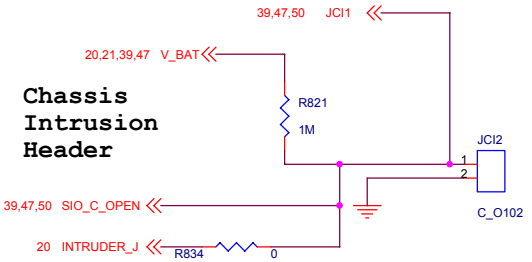
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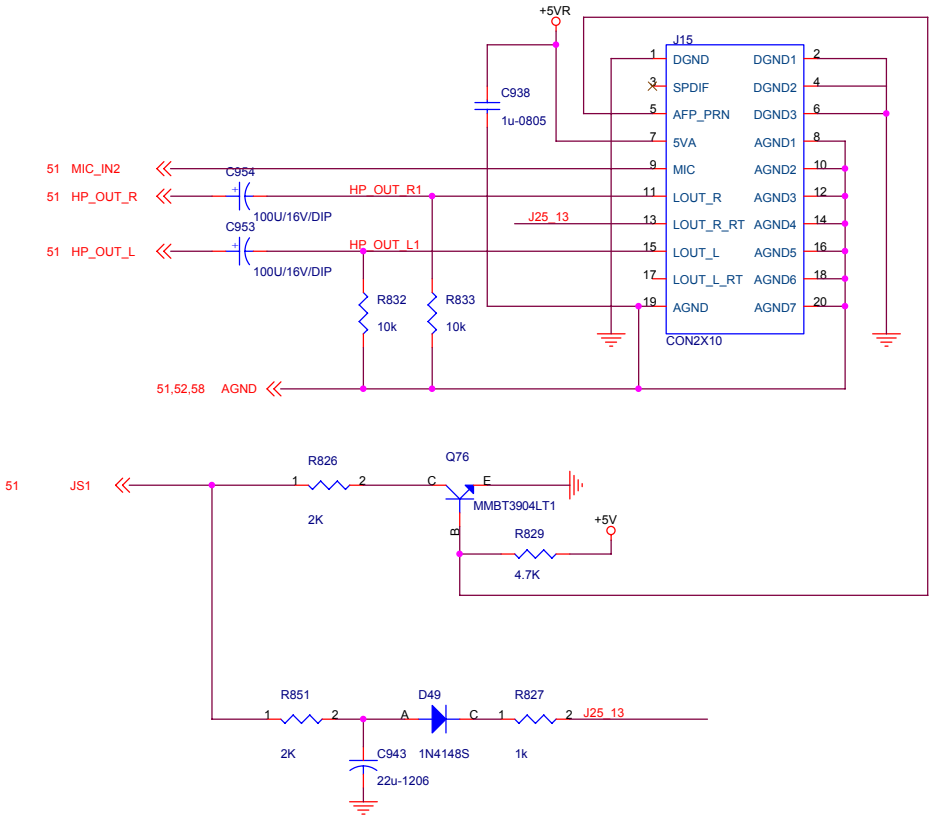
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Document Number	
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw	Last Revision Date: Wednesday, June 19, 2002 Sheet 55 of 63

Wakeup On LAN Header



Chassis Intrusion Header





MS-9121-0C
2002/06/06

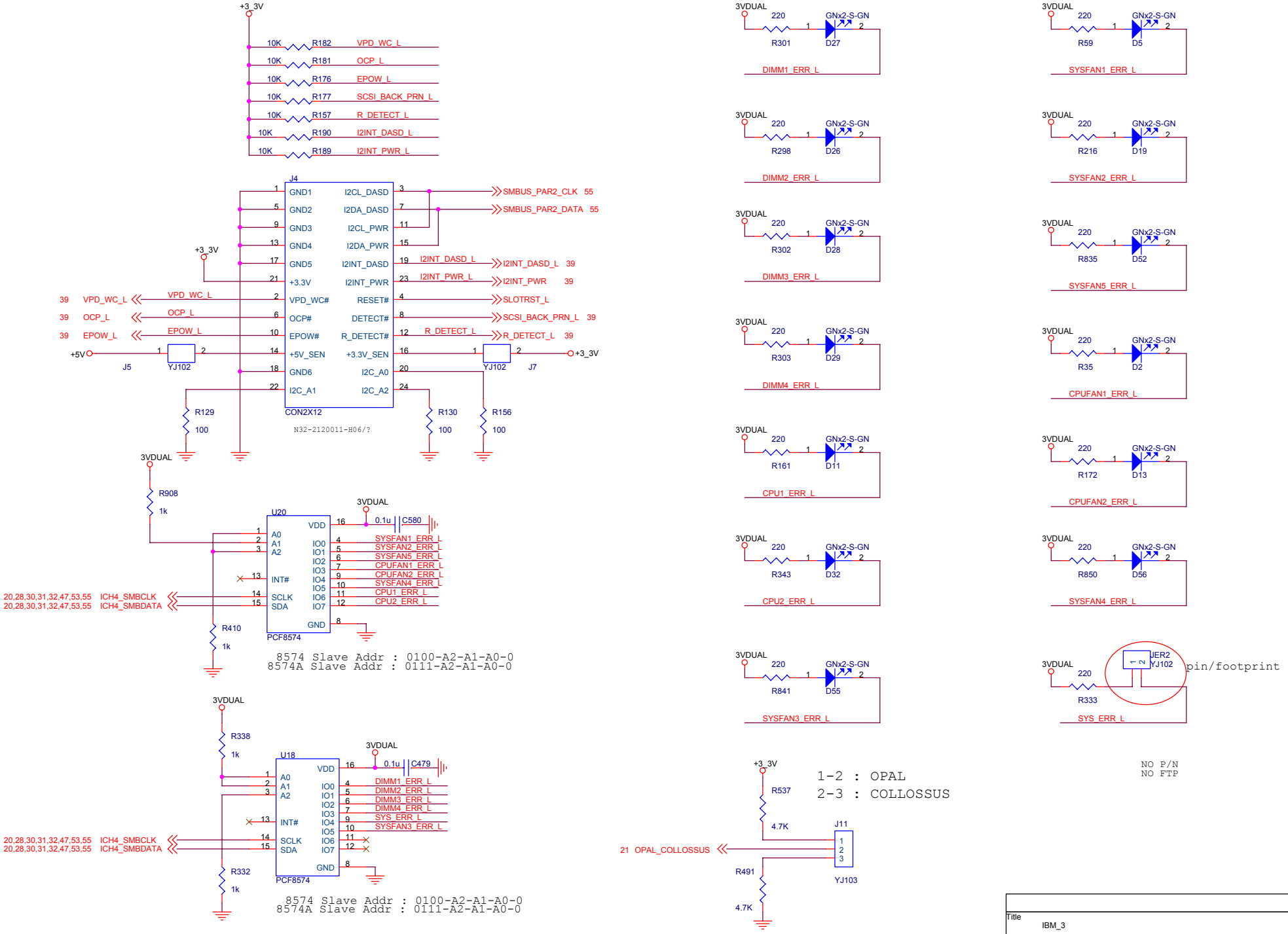
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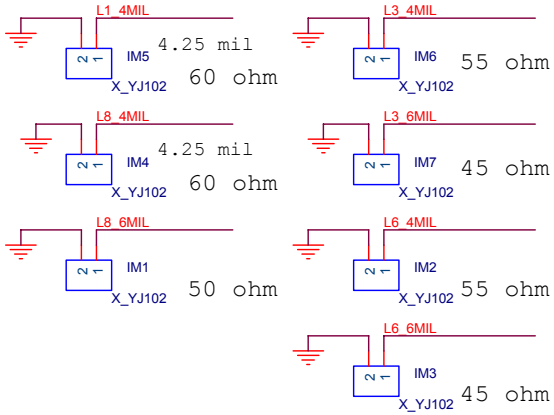
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Document Number:	

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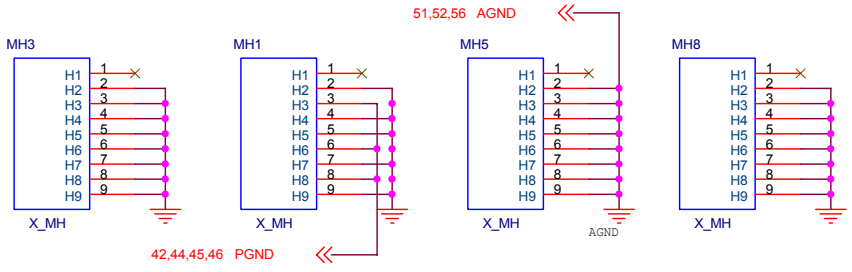
Last Revision Date: Wednesday, June 19, 2002

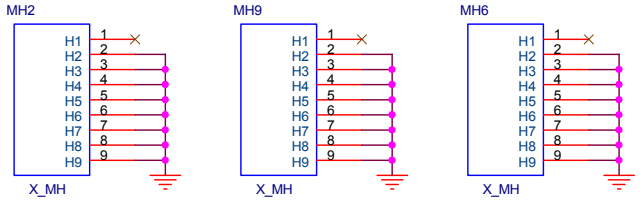
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+3_3V
10K R182 VPD_WC_L
10K R181 OCP_L
10K R176 EPOW_L
10K R177 SCSI_BACK_PRN_L
10K R157 R_DETECT_L
10K R190 I2INT_DASD_L
10K R189 I2INT_PWR_L
J4
GND1 GND2 GND3 GND4 GND5 +3.3V VPD_WC# OCP# EPOW# +5V_SEN GND6 I2C_A1
I2CL_DASD I2DA_DASD I2CL_PWR I2DA_PWR I2INT_DASD I2INT_PWR RESET# DETECT# R_DETECT# +3.3V_SEN I2C_A0 I2C_A2
SMBUS_PAR2_CLK 55
SMBUS_PAR2_DATA 55
I2INT_DASD_L 39
I2INT_PWR 39
SLOTRST_L
SCSI_BACK_PRN_L 39
R_DETECT_L 39
39 VPD_WC_L
39 OCP_L
39 EPOW_L
+5V
J5 YJ102
YJ102 J7
+3_3V
CON2X12
N32-2120011-H06/?
R129 100
R130 100
R156 100
3VDUAL
R908 1k
U20
VDD A0 A1 A2 INT# SCLK SDA GND IO0 IO1 IO2 IO3 IO4 IO5 IO6 IO7
0.1u C580
SYSFAN1_ERR_L
SYSFAN2_ERR_L
SYSFAN5_ERR_L
CPUFAN1_ERR_L
CPUFAN2_ERR_L
SYSFAN4_ERR_L
CPU1_ERR_L
CPU2_ERR_L
20,28,30,31,32,47,53,55 ICH4_SMBCLK
20,28,30,31,32,47,53,55 ICH4_SMBDATA
R410 1k
PCF8574
8574 Slave Addr : 0100-A2-A1-A0-0
8574A Slave Addr : 0111-A2-A1-A0-0
R338 1k
U18
0.1u C479
DIMM1_ERR_L
DIMM2_ERR_L
DIMM3_ERR_L
DIMM4_ERR_L
SYS_ERR_L
SYSFAN3_ERR_L
R332 1k
8574 Slave Addr : 0100-A2-A1-A0-0
8574A Slave Addr : 0111-A2-A1-A0-0
220 R301 GNx2-S-GN D27 DIMM1_ERR_L
220 R298 GNx2-S-GN D26 DIMM2_ERR_L
220 R302 GNx2-S-GN D28 DIMM3_ERR_L
220 R303 GNx2-S-GN D29 DIMM4_ERR_L
220 R161 GNx2-S-GN D11 CPU1_ERR_L
220 R343 GNx2-S-GN D32 CPU2_ERR_L
220 R841 GNx2-S-GN D55 SYSFAN3_ERR_L
220 R59 GNx2-S-GN D5 SYSFAN1_ERR_L
220 R216 GNx2-S-GN D19 SYSFAN2_ERR_L
220 R835 GNx2-S-GN D52 SYSFAN5_ERR_L
220 R35 GNx2-S-GN D2 CPUFAN1_ERR_L
220 R172 GNx2-S-GN D13 CPUFAN2_ERR_L
220 R850 GNx2-S-GN D56 SYSFAN4_ERR_L
220 R333 JER2 YJ102 pin/footprint SYS_ERR_L
NO P/N
NO FTP
+3_3V
R537 4.7K
21 OPAL_COLLOSSUS
J11
1 2 3
YJ103
R491 4.7K
1-2 : OPAL
2-3 : COLLOSSUS
Title IBM_3
Size Custom
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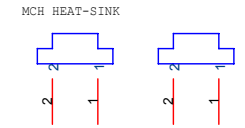


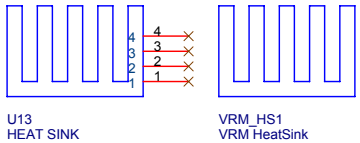
Impedence Measurement Test pad

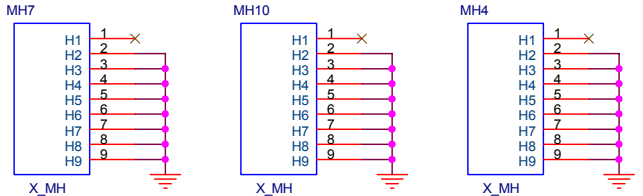


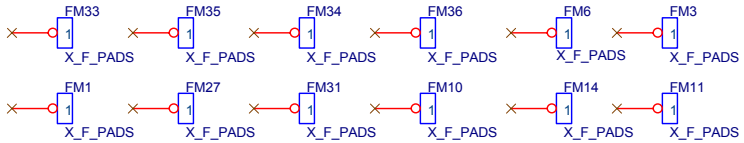


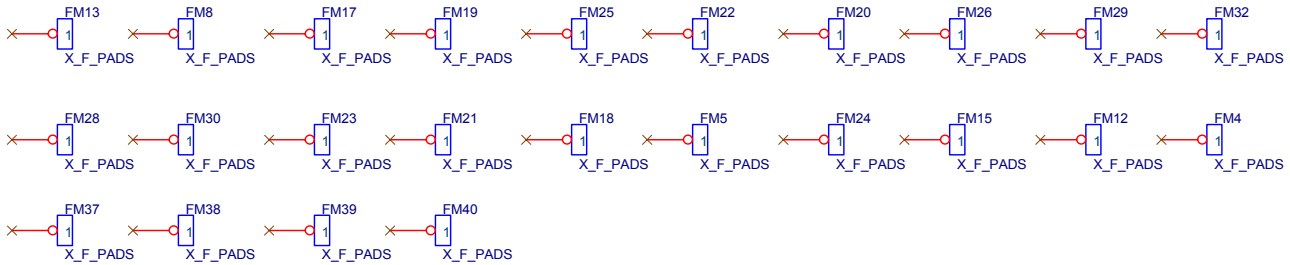


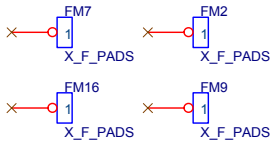












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2002/06/06

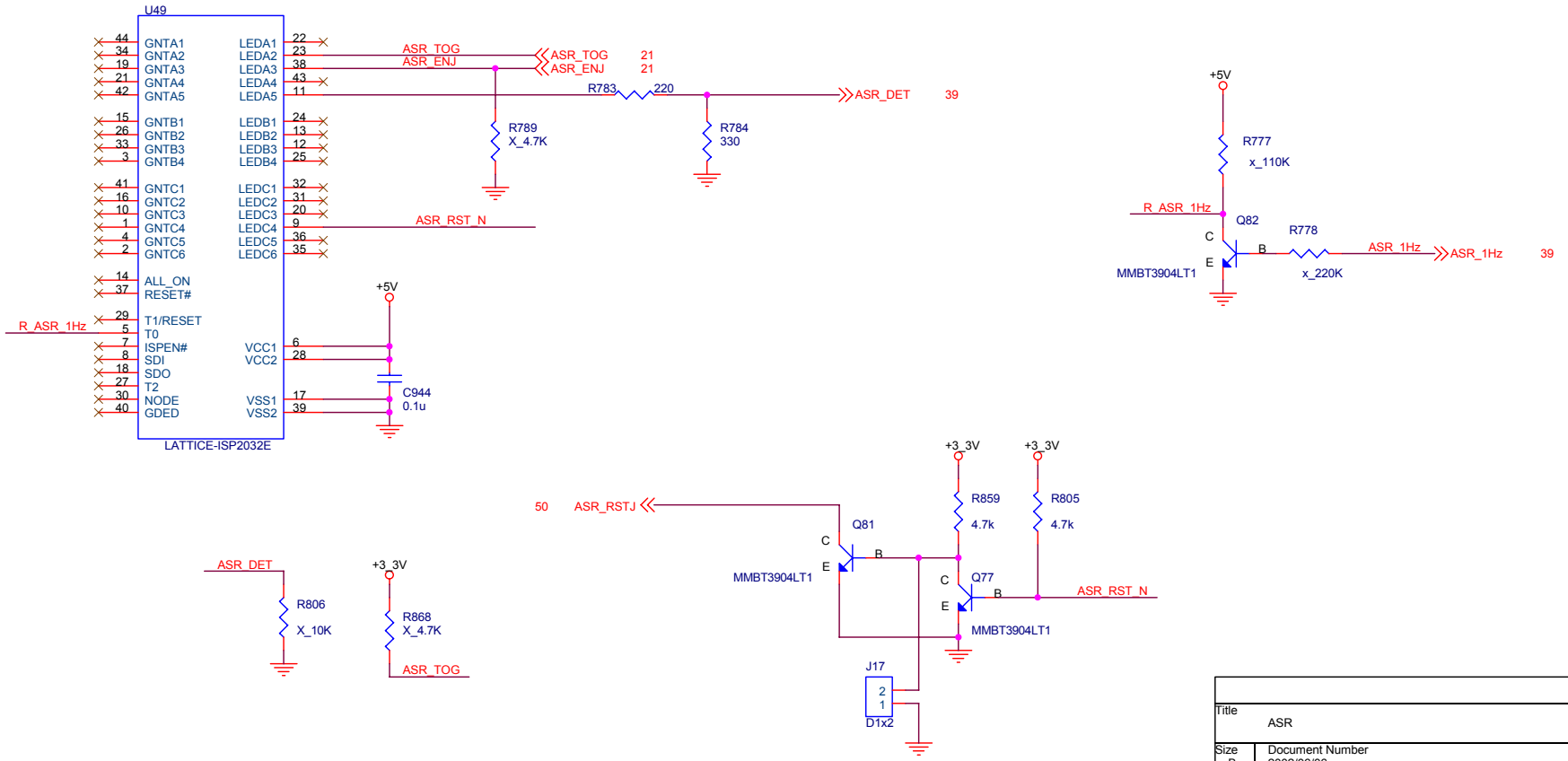
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Title	Rev
Mechanical Part	00C
Document Number	

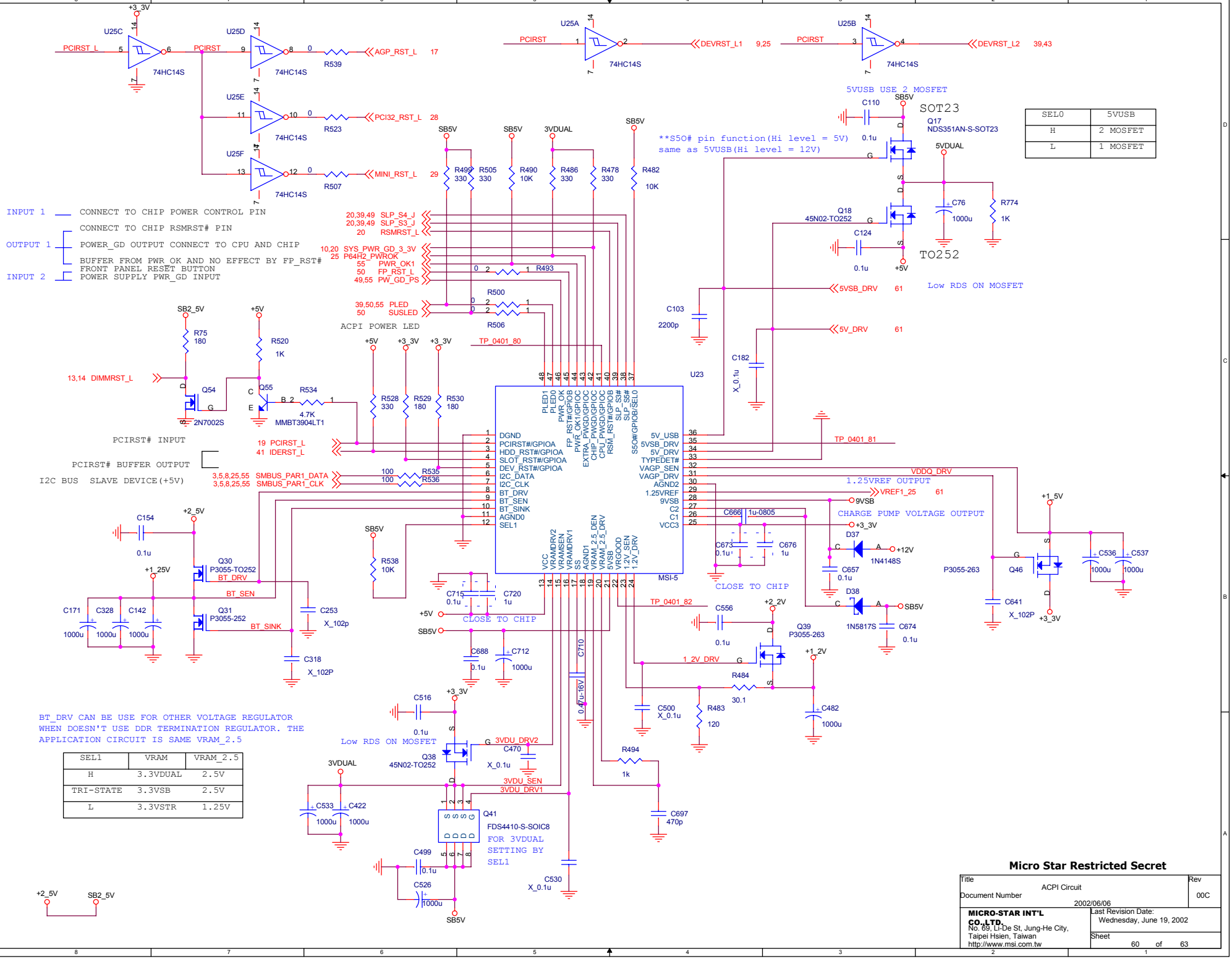
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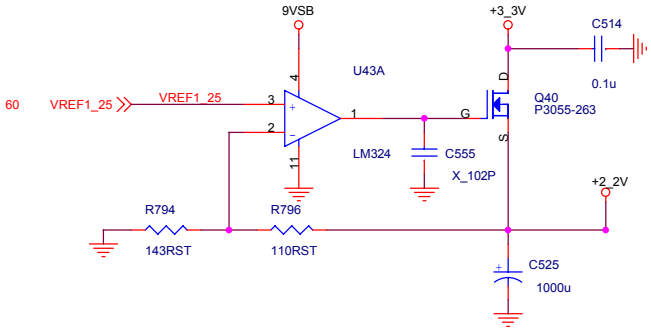
Last Revision Date: Wednesday, June 19, 2002

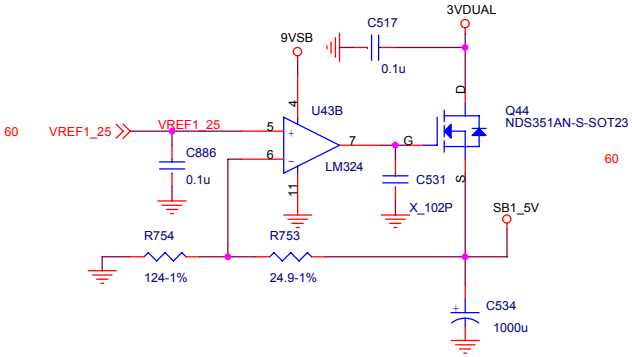
Net name	Connect to	Default state	Action
ASR_TOG		HIGH	
ASR_EN#		HIGH	Use to enable ASR function, active Low.
ASR_DET		LOW	High means ASR event occur.
ASR_RST#	System reset circuit	HIGH	If ASR function enable and ASR event occur, it will issue a 500ms low pluse to reset system.

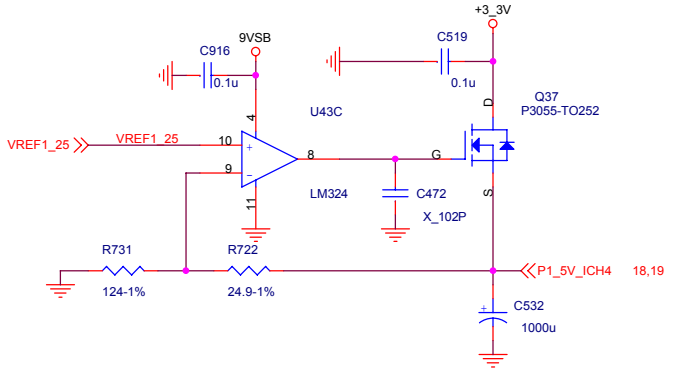


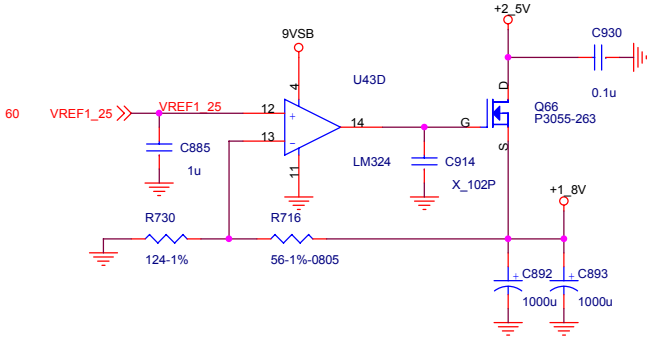
Title	ASR	
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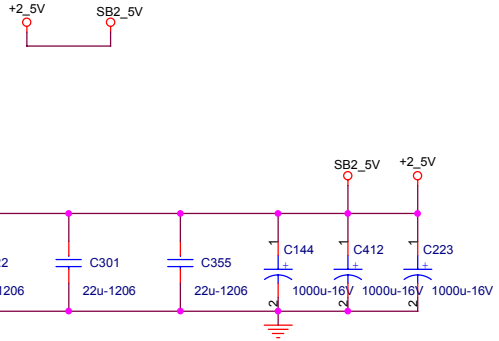

U25C
U25D
U25E
U25F
74HC14S
PCIRST_L
PCIRST
AGP_RST_L 17
PCI32_RST_L 28
MINI_RST_L 29
R539
R523
R507
U25A
U25B
DEVRST_L1 9,25
DEVRST_L2 39,43
5VUSB USE 2 MOSFET
SOT23
Q17
NDS351AN-S-SOT23
Q18
45N02-TO252
TO252
Low RDS ON MOSFET
5VSB_DRV 61
5V_DRV 61
**S50# pin function(Hi level = 5V)
same as 5VUSB(Hi level = 12V)
SEL0 | 5VUSB
H | 2 MOSFET
L | 1 MOSFET
INPUT 1 CONNECT TO CHIP POWER CONTROL PIN
CONNECT TO CHIP RSMRST# PIN
OUTPUT 1 POWER_GD OUTPUT CONNECT TO CPU AND CHIP
BUFFER FROM PWR_OK AND NO EFFECT BY FP_RST#
FRONT PANEL RESET BUTTON
INPUT 2 POWER SUPPLY PWR_GD INPUT
20,39,49 SLP_S4_J
20,39,49 SLP_S3_J
20 RSMRST_L
10,20 SYS_PWR_GD_3_3V
25 P64H2_PWROK
55 PWR_OK1
50 FP_RST_L
49,55 PW_GD_PS
39,50,55 PLED
50 SUSLED
ACPI POWER LED
13,14 DIMMRST_L
Q54
2N7002S
Q55
MMBT3904LT1
PCIRST# INPUT
19 PCIRST_L
41 IDERST_L
PCIRST# BUFFER OUTPUT
I2C BUS SLAVE DEVICE(+5V)
3,5,8,25,55 SMBUS_PAR1_DATA
3,5,8,25,55 SMBUS_PAR1_CLK
U23
MSI-5
DGND
PCIRST#/GPIOA
HDD_RST#/GPIOA
SLOT_RST#/GPIOA
DEV_RST#/GPIOA
I2C_DATA
I2C_CLK
BT_DRV
BT_SEN
BT_SINK
AGND0
SEL1
5V_USB
5VSB_DRV
5V_DRV
TYPEDET#
VAGP_SEN
VAGP_DRV
AGND2
1.25VREF
9VSB
C2
C1
VCC3
1.25VREF OUTPUT
VDDQ_DRV
VREF1_25 61
CHARGE PUMP VOLTAGE OUTPUT
CLOSE TO CHIP
TP_0401_80
TP_0401_81
TP_0401_82
BT_DRV CAN BE USE FOR OTHER VOLTAGE REGULATOR
WHEN DOESN'T USE DDR TERMINATION REGULATOR. THE
APPLICATION CIRCUIT IS SAME VRAM_2.5
SEL1 | VRAM | VRAM_2.5
H | 3.3VDUAL | 2.5V
TRI-STATE | 3.3VSB | 2.5V
L | 3.3VSTR | 1.25V
Low RDS ON MOSFET
Q38
45N02-TO252
3VDU_DRV2
3VDU_SEN
3VDU_DRV1
Q41
FDS4410-S-SOIC8
FOR 3VDUAL
SETTING BY
SEL1
Q30
P3055-TO252
Q31
P3055-252
Q39
P3055-263
Q46
P3055-263
D37
1N4148S
D38
1N5817S
1_2V_DRV
SB2_5V
+2_5V
Micro Star Restricted Secret
Title ACPI Circuit
Document Number
2002/06/06
Rev 00C
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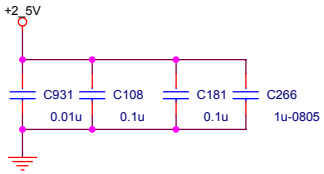








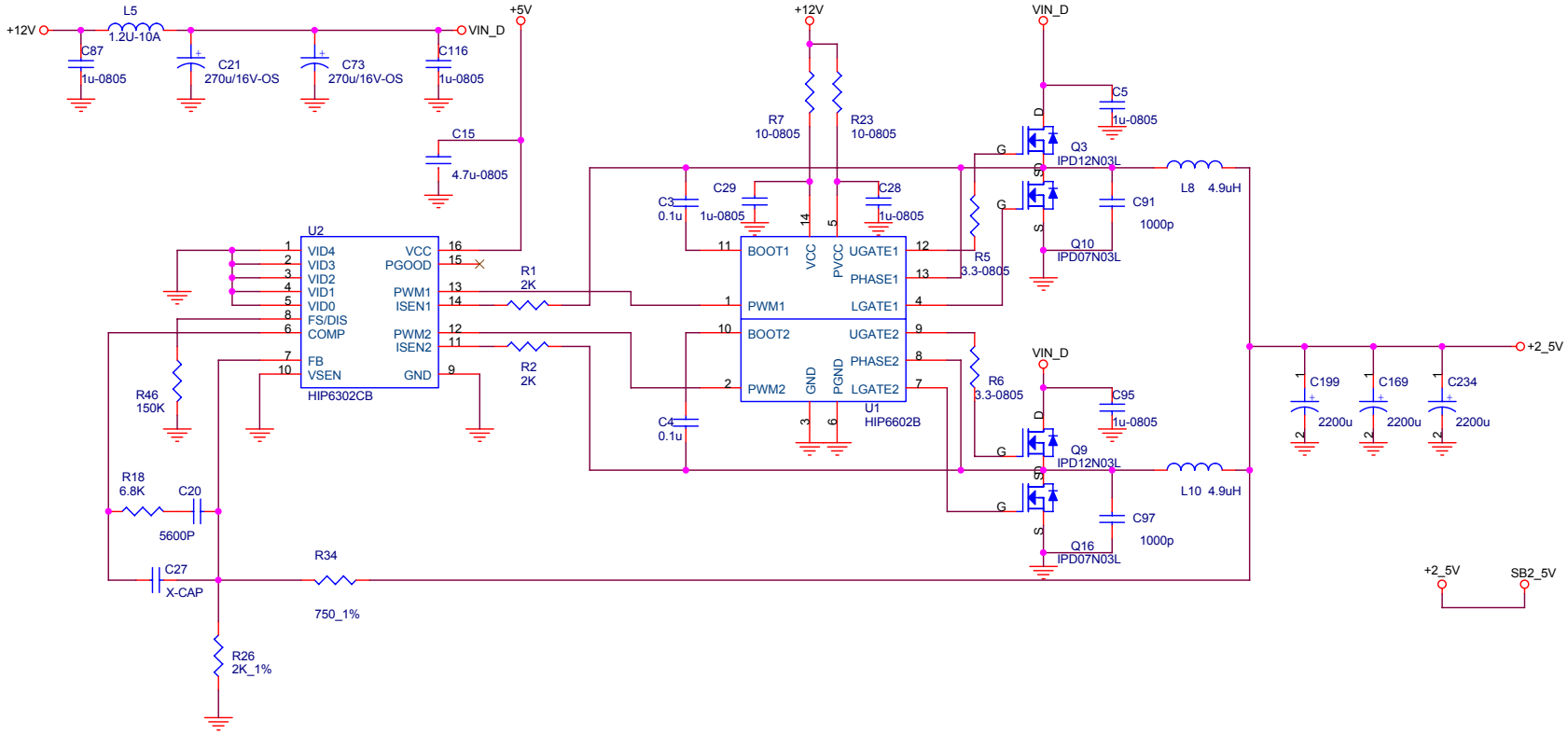
PLACE THESE CAPS BETWEEN MCH AND DIMM1



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Title: System Voltage Regulators		Rev: 00C
Document Number: 2002/06/06		
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HIP6302 FOR 26A DDR power ckt



Title	HIP6302 FOR 26A DDR power ckt	
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JGL1(1-2)
JC-D2-GN

J10_X
JC-D2-GN

J11(1-2)
JC-D2-GN

J12(1-2)
JC-D2-GN

J14(2-3)
JC-D2-GN

JBAT1(1-2)
JC-D2-GN

J16(1-2)
JC-D2-GN

J5_X
JC-D2-GN

J7_X
JC-D2-GN

Short-pin

PCB
9121PCB

PCB-9121-0B

Battery

BAT1_X1
BAT-B-CR2032-P-3V-220mAh

Battery

SST-4M-PLCC32
U32_1

512KB
Flashrom

U33_1
P64H2-HEATSINK

Heatsink

Title	Manual Part	
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